Course: High Performance Computing Lab

Practical No. 5

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Title of practical: Implementation of OpenMP programs.

Implement following Programs using OpenMP with C:

- 1. Implementation of Matrix-Matrix Multiplication.
- 2. Implementation of Matrix-scalar Multiplication.
- 3. Implementation of Matrix-Vector Multiplication.
- 4. Implementation of Prefix sum.

Problem Statement 1:

Screenshots:

```
ssignment_No_5 > C 01_.c > 😭 main()
    int main() {
   int A[N][N] = {{1, 2, 3},
        for (int i = 0; i < N; i++) {
for (int j = 0; j < N; j++) {
printf("%d ", c[i][j]);
```

```
E:\ashutosh\LY\SEM 1\LAB\HPC>cd "e:\ashutosh\LY\SEM 1\LAB\HPC\Assignment_No_5\" && gcc 01_.c -o 01_ && "e:\ashut osh\LY\SEM 1\LAB\HPC\Assignment_No_5\" && gcc 01_.c -o 01_ && "e:\ashut osh\LY\SEM 1\LAB\HPC\Assignment_No_5\" 01_ Result Matrix:

30 24 18
84 69 54
138 114 90
```

Information:

OpenMP Directive Used: #pragma omp parallel for collapse(2) runs the two outer loops in parallel.

Each thread computes one or more rows of the resulting matrix independently. Synchronization is not needed for each cell since each (i,j) position is computed by exactly one thread.

Analysis:

Without OpenMP: Sequential execution → higher execution time for large N. With OpenMP: Speed-up depends on number of threads and system cores. The collapse(2) clause merges two nested loops for better load balancing.

Problem Statement 2: Screenshots:

```
E:\ashutosh\LY\SEM 1\LAB\HPC>cd "e:\ashutosh\LY\SEM 1\LAB\HPC\Assignment_No_5\" && gcc 02_.c -o 02_ && "e:\ashutosh\LY\SEM 1\LAB\HPC\Assignment_No_5\" && gcc 02_.c -o 02_ && "e:\ashutosh\LY\SEM 1\LAB\HPC\Assignment_No_5\" 02_
Result Matrix:
3 6 9
12 15 18
21 24 27
```

Information:

OpenMP Directive: parallel for collapse(2) allows rows and columns to be processed simultaneously.

No synchronization required since each matrix element is updated independently.

Analysis:

Perfect parallelization with no dependencies.

Achieves nearly linear speed-up for large matrices.

Problem Statement 3:

Screenshots:

```
03_ && "e:\ashutosh\LY\SEM 1\LAB\HPC\Assignment_No_5\"03_
Result Vector:
14 32 50
```

Information:

Each thread calculates one row's dot product with the vector.

Since result[i] is unique to each thread, no synchronization is required.

Analysis:

Highly parallelizable, memory access pattern is sequential for the vector (good cache usage).

Performance improves with larger N.

Problem Statement 4:

Screenshots:

```
E:\ashutosh\LY\SEM 1\LAB\HPC\cd "e:\ashutosh\LY\SEM 1\LAB\HPC\Assignment_No_5\" && gcc 04_.c -o 04_ && "e:\ashut osh\LY\SEM 1\LAB\HPC\Assignment_No_5\" && gcc 04_.c -o 04_ && "e:\ashut osh\LY\SEM 1\LAB\HPC\Assignment_No_5\" 04_
Prefix Sum:
1 3 6 10 15 21 28 36
```

Information:

Prefix sum has data dependency (each element depends on previous). Fully parallel version requires special algorithms like Blelloch scan.

This example shows why naive parallelization fails and how to fix it.

Analysis:

Sequential dependency limits parallelism.

Optimized parallel algorithms can break work into partial sums and combine results.

Github Link:

https://github.com/Ashutoshbirje/HPC-LAB/tree/master/Assignment_No_5