

## BHARATI VIDYAPEETH'S COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi & Affiliated to Guru Gobind Singh Indraprastha University, Delhi)
(An ISO 9001:2015 Certified Institution)
A-4, Paschim Vihar, Main Rohtak Road, New Delhi – 110 063

## **Department of Electronics and Communication Engineering**

Student Name		Enroll. No	Enroll. No	
Semester:	V sem	Date:		
Course Code.: ETEC303 Quiz/Test No.: 02		Course Title: Digital Communication  Marks Obtained		
				Max. Marks:
Q1. For an encoded	bit stream output of a Delta	a modulator, a repeating sequence of '1's or '0's	would mean that	
the system is fa	cing	error and a repeating sequence	of alternate '1's	
and '0's would	imply that it is facing	error.	[0.2 marks]	
Q2. For a Delta mod	dulator, designed to avoid	slope overload distortion, what should be the mi	nimum sampling	
rate for a sinuso	idal input of frequency 200	$00\pi$ rad/s if the SNR must be at least 30dB?	[0.3 marks]	
•	tement: "In Differential F linear predictor."	PCM, the performance of the system depends	directly on the [0.3 marks]	
<ul><li>a) Makes weak</li><li>b) Reduces nun</li></ul>	<u>-</u>		following? [0.1 mark]	
d) All the above				
Q5. Internationally	accepted values of $\mu$ and $A$	A in $\mu$ - Law and A-Law companders are	and	
respectively.			[0.1 marks]	