Pin	Pin	Pin	5V pin			1			1							1					
N°	Name	Туре	Tolerant	Functions	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15	Default Function HydraBus
60	воото		-		-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	Default Boot from Flash (pull-down resistor 2.2Kohms)
7	NRST	VO	-	-		-	-	-	-	-	-		-	-	-	-	-	-	-	-	Reset Button / SWD_DEBUG
	PA0-WKUP			ADC123_IN0 /		TIM2_CH1															UBTN (User Button 0)
14	(PA0)	l/O	YES	WKUP(4)	-	TIM2_ETR	TIM 5_CH1	TIM8_ETR	-	-	-	USART2_CTS	UART4_TX	-	-	ETH_MII_CRS	-	-	-	EVENTOUT	Can be disabled by removing Jumper JMP1 UBTN
																ETH_MII_RX_CLK ETH_RMII_REF_ CLK					
15 16	PA1 PA2	I/O	YES YES	ADC123_IN1 ADC123_IN2	-	TIM2_CH2 TIM2_CH3	TIM5_CH2 TIM5_CH3	- TIM9 CH1	-	-	-	USART2_RTS USART2_TX	UART4_RX	-	-	CLK ETH_MDIO	-	-	-	EVENTOUT EVENTOUT	
17	PA3	1/0	YES	ADC123_IN3	-	TIM2_CH4	TIM5_CH4	TIM9_CH2	-	-	-	USART2_RX	-	-	OTG_HS_ULPI_D0	ETH_MII_COL	-	-	-	EVENTOUT	
																					ULED (User LED 0)
20	PA4	I/O	NO (3.3V)	ADC12_IN4 / DAC1_OUT	_			_	_	SPI1 NSS	SPI3_NSS I2S3 WS	USART2_CK	_				OTG_HS_SOF	DCMI HSYNC		EVENTOUT	Can be disabled by removing Jumper JMP1 ULED
	ΡΔ5			ADC12_IN5 / DAC2_OUT		TIM2_CH1 TIM2_ETR		TIM8 CH1N		SPI1 SCK					OTO 110 111 DI 014					EVENTOUT	
22		1/0	NO (3.3V) YES	ADC12_IN6	-		TIM3_CH1	TIM8_CH1N TIM8_BKIN	-	SPI1_SCK SPI1_MISO	-	-	-	TIM13_CH1	OTG_HS_ULPI_CK -	-	-	DCMI_PIXCK	-	EVENTOUT	
23	PA7	lvo.	YES	ADC12 IN7	_	TIM1 CH1N	TIM3 CH2	TIM8 CH1N	_	SPI1 MOSI	L		_	TIM14 CH1	_	ETH_MII_RX_DV ETH_RMII_CRS_DV	_			EVENTOUT	
23 41 42	PA8	1/0	YES	OTG_FS_VBUS	MCO1	TIM1_CH1 TIM1_CH2	-	-	I2C3_SCL I2C3_SMBA	-	-	USART1_CK USART1_TX	-	-	OTG_FS_SOF	-	-	DCMI D0	-	EVENTOUT EVENTOUT	
43	PA10	1/0	YES	UIG_F3_VBUS	-	TIM1_CH3	-	-	IZC3_SMBA	-	-	USARTI_TX USART1_RX	-	-	OTG_FS_ID	-	-	DCMI_D0		EVENTOUT	
44	PA11	lνο	YES		_	TIM1_CH4				_		USART1_CTS		CAN1_RX	OTG_FS_DM		-			EVENTOUT	USB D- (USB0 connector)
	PA12	VO	YES			TIM1_ETR						USART1_RTS									USB D+ (USB0 connector)
	PA12 PA13 (JTMS-SWDIO)				-	IIM1_EIR			-	-	-	USARI1_RIS	-	CAN1_IX	OTG_FS_DP	-	-				
) 1/0	YES		JTMS-SWDIO	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENTOUT	SWDIO
49	PA14 (JTCK-SWCLK)) 1/0	YES		TCK-SWCLK		-	-	-	-	-		-	-	-	-		-	-	EVENTOUT	SWCLK
50	PA15 (JTDI)	lνο	YES		JTDI	TIM 2_CH1 TIM 2_ETR	<u> </u>			SPI1_NSS	SPI3_NSS/ I2S3S_WS		-	<u> </u>	<u> </u>			<u> </u>	_	EVENTOUT	
26 27	PB0	1/0	YES YES	ADC12_IN8 ADC12_IN9	-	TIM1_CH2N TIM1_CH3N	TIM3_CH3 TIM3_CH4	TIM8_CH2N TIM8_CH3N		-					OTG_HS_ULPI_D1 OTG_HS_ULPI_D2	ETH_MII_RXD2 ETH_MII_RXD3			-	EVENTOUT EVENTOUT	
	PB2-BOOT1						5														
	(PB2) PR3	1/0	YES		-	1			-	-	-		-	-		1	-	-	H	EVENTOUT	
	(JTDO/ TRACESWO)	ΙO	YES		JTDO/ TRACESWO	TIM2_CH2	[SPI1_SCK	SPI3_SCK I2S3_CK			[[[EVENTOUT	
	PB4																				
56	(NJTRST)	1/0	YES		NJTRST	-	TIM3_CH1	-	-	SPI1_MISO	SPI3_MISO	I2S3ext_SD	-	-	-	-	-	-	-	EVENTOUT	
57	PB5	l/O	YES		-		TIM3_CH2	-	I2C1_SMBA	SPI1_MOSI	SPI3_MOSI I2S3_SD		-	CAN2_RX	OTG_HS_ULPI_D7	ETH_PPS_OUT	-	DCMI_D10	-	EVENTOUT	
58 59	PB7	I/O	YES YES		-		TIM4_CH1 TIM4_CH2	-	I2C1_SCL I2C1_SDA	-	-	USART1_TX USART1_RX	-	CAN2_TX		-	FSMC_NL	DCMI_D5 DCMI_VSYNC	-	EVENTOUT EVENTOUT	
61	PB8	1/0	YES		-	-	TIM4_CH3	TIM10_CH1	I2C1_SCL	SPI2 NSS	-	-	-	CAN1_RX	-	ETH_MII_TXD3	SDIO_D4	DCMI_D6	-	EVENTOUT	
62	PB9	l/O	YES		-		TIM4_CH4	TIM11_CH1	I2C1_SDA	SPI2_NSS I2S2_WS	-	-	-	CAN1_TX	-	-	SDIO_D5	DCMI_D7	-	EVENTOUT	
29	PB10	l/O	YES		-	TIM2_CH3	-		I2C2_SCL	SPI2_SCK I2S2_CK	-	USART3_TX	-	-	OTG_HS_ULPI_D3	ETH_ MII_RX_ER	-		-	EVENTOUT	
20	PB11	I/O	YES			TIM2_CH4			I2C2 SDA			USART3_RX			OTG HS ULPLD4	ETH_MII_TX_EN ETH_RMII_TX_EN				EVENTOUT	
										SPI2_NSS						ETH MII TXD0					USB ID
33	PB12	I/O	YES		-	TIM1_BKIN	-	-	I2C2_SMBA	I2S2_WS	-	USART3_CK	-	CAN2_RX	OTG_HS_ULPI_D5	ETH_RMII_TXD0	OTG_HS_ID	-	-		(USB1 connector)
34	PB13	lο	YES	OTG_HS_VBUS	-	TIM1_CH1N		-	-	SPI2_SCK I2S2_CK	-	USART3_CTS	-	CAN2_TX	OTG_HS_ULPI_D6	ETH_MII_TXD1 ETH_RMII_TXD1	-	-	-	EVENTOUT	Supply VUSB (USB1 connector)
35	PB14	l/O	YES		_	TIM1_CH2N		TIM8_CH2N	_	SPI2_MISO	I2S2ext_SD	USART3_RTS	-	TIM12_CH1	-	-	OTG_HS_DM		-	EVENTOUT	USB D- (USB1 connector)
36	PR15	VO	YES		RTC_50Hz	TIM1_CH3N		TIM8_CH3N		SPI2_MOSI I2S2 SD				TIM12_CH2			OTG_HS_DP			EVENTOUT	USB D+ (USB1 connector)
36 8	PC0 PC1		YES YES	ADC123_IN10 ADC123_IN11	-	-	-	-	-		-	-	-		OTG_HS_ULPI_STP	- ETH_MDC	-	-		EVENTOUT EVENTOUT	
10	PC2	1/0	YES	ADC123_IN12	-			-			I2S2ext_SD			-	OTG_HS_ULPI_DIR	ETH_MII_TXD2	-	-		EVENTOUT	
11	PC3	lνο	YES	ADC123_IN13				_	-	SPI2_MOSI I2S2_SD			-		OTG_HS_ULPI_NXT	ETH _MII_TX_CLK	-		.	EVENTOUT	
				ADC12_IN14												ETH_MII_RXD0 ETH_RMII_RXD0				EVENTOUT	
24		1/0	YES		-				-	-	-		-	-		ETH_MII_RXD1 ETH_RMII_RXD1	-				
25 37	PC5 PC6	1/0	YES YES	ADC12_IN15	-		TIM3_CH1	TIM8 CH1	-	- 12S2_MCK	-	-	- USART6_TX	-	-	ETH RMII_RXD1	SDIO_D6	- DCMI_D0		EVENTOUT EVENTOUT	
38	PC7		YES		-	-	TIM3_CH2	TIM8_CH2			I2S3_MCK		USART6_RX				SDIO_D7	DCMI_D1		EVENTOUT	
39	PC8	lο	YES		-		тімз_снз	TIM8_CH3					USART6_CK				SDIO_D0	DCMI_D2		EVENTOUT	MicroSD Card SD 4bits SDIO_D0
40	PC9	l/O	YES		MCO2	-	TIM3_CH4	TIM8_CH4	I2C3_SDA	I2S_CKIN							SDIO_D1	DCMI_D3		EVENTOUT	MicroSD Card SD 4bits SDIO_D1
											SPI3_SCK/ I2S3S_CK	LIGHTY TO									MicroSD Card SD 4bits
	PC10	1/0	YES									USART3_TX/	UART4_TX				SDIO_D2	DCMI_D8		EVENTOUT	SDIO_D2 MicroSD Card SD 4bits
52	PC11	l/O	YES		-	-	-	-	-	I2S3ext_SD	SPI3_MISO/	USART3_RX	UART4_RX	-	-	-	SDIO_D3	DCMI_D4	-	EVENTOUT	SDIO_D3
53	PC12	lο	YES		-	-	-	-	-	-	SPI3_MOSI I2S3_SD	USART3_CK	UART5_TX	-	-	-	SDIO_CK	DCMI_D9	-	EVENTOUT	MicroSD Card SD 4bits SDIO_CK
2	PC13	lνο	YES	EVENTOUT / RTC AF1		-		_	-	-	-		-	-	ļ.		_				
	PC14-OSC32_I (PC14)			EVENTOUT / OSC32_IN(4)																	Optional RTC 32,768KHz
	(PC14) PC15-	1/0	YES	USU32_IN(4)					-	-			-			1					K10 32,/68KHZ
	OSC32_OUT (PC15)	l/O	YES	EVENTOUT / OSC32 OUT(4)			[L	_	L		[[[[[_	Optional RTC 32,768KHz
				_ 3002_001(4)																	MicroSD Card SD 4bits
	PD2	I/O	YES	EVENTOUT /	-		TIM3_ETR		-	-	-		UART5_RX	-		-	SDIO_CMD	DCMI_D11		EVENTOUT	SDIO_CMD
5	PH0-OSC_IN (PH0)	VΟ	YES	OSC_IN(4)	-	-	-	-	-	-	-		-	-	-	-	-	-	-	-	Quartz 8MHz
6	PH1-OSC_OUT (PH1)	l/O	YES	EVENTOUT / OSC_OUT(4)					-	-	-		-	-					-		Quartz 8MHz
31	(PH1) VBAT VCAP_1																				+3V3
47	VCAP_2 VDD				-	-	-	-	-	-	-		-	-		-	-	-		-	
19 32 48	VDD	-								-						-			-		-
64	VDD	-	-	-	-	-	-	-	-	-	-		-	-			-		-		-
13 18	VDDA							-	-	-			-	-		-	-		-		
63	VSS	-	ŀ		-	-	-	-	-	-	-		-	-		-	-	-	-	-	
12	VSSA	-	1	-	-	i e	i e	-		-				1				-	-	-	

Pin 3.3V Only !! Not 5V tolerant HydraBus pins not reusable HydraBus pins can be reused, available on J1/J2/J3 or SWD_DEBUG

			STM32F405RGT_pinNo																			
March Marc	Pin N°	Pin Name	Pin Type	5V pin Tolerant	Functions	AFO	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15	Default Function HydraBus
Part	1	VBAT	-	-	-	-	Ar I	- Al 2	Ar 3	A/4	-	- Al-0	-	-	- Al-3	-	AFTI	AF 12	AF13	- AF 14	- AF 10	+3V3
Property column	_	PO42		VEC	EVENTOUT /																	
March Marc	- 2		100	155			-	-	-		-	-	-	-	-	-	-	-		<u> </u>	-	Ontional
Part	3	(PC14)	1/0	YES	OSC32_IN(4)	-	-	-		-	-	-		-	-	-	-	-	-	-	-	RTC 32,768KHz
Part		PC15- OSC32 OUT			EVENTOUT /																	Ontional
March Marc	4		1/0	YES	OSC32_OUT(4)					-						-	-	-		-		RTC 32,768KHz
The column The	5	PH0-OSC_IN (PH0)	I/O	YES	EVENTOUT / OSC IN(4)						_	_			_	_	2			1		Quartz 8MHz
		PH1-OSC_OUT			EVENTOUT /																	
1	6	(PH1) NPST		YES	OSC_OUT(4)	-	-	-	-	-	-	-	-	-	-	-	-	-	-		-	Quartz 8MHz
1				YES	ADC123_IN10	-	-		-	-		-	-	-	-	OTG_HS_ULPI_STP	-			-	EVENTOUT	TRESCE BUILDING TO WE BELOW
1						-	-	-	-	-	-	-	-	-	-	-		-	-	-		
1	10	PC2	1/0	YES	ADC123_IN12	-	-	-	-	-		I2S2ext_SD	-	-	-	OTG_HS_ULPI_DIR	ETH_MII_TXD2	-	-	+ -	EVENTOUT	_
1			1/0	YES	ADC123_IN13	-	-	-		-	12S2_SD	-		-	-	OTG_HS_ULPI_NXT	ETH_MII_TX_CLK	-	-	-	EVENTOUT	
No. Control			-	-	-	-	-	-	-	-	-	-		-	-	-	-	-	-	-	-	1-
No.	13	VDDA	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		-	-	· ·
No. 1																						(User Button 0)
No. 1	14	PA0-WKUP (PA0)	1/0	YES	ADC123 IN0 / WKUP(4		TIM2_CH1 TIM2_ETR	TIM 5 CH1	TIM8 ETR				USART2 CTS	UART4 TX		_	ETH MII CRS				EVENTOUT	Can be disabled by removing Jumper JMP1 UBTN
Column													_	_			ETH MIL DY CLK					
Column	15	PA1	1/0	YES	ADC123 IN1	_	TIM2 CH2	TIM5 CH2		_		_	USART2 RTS	UART4 RX		-	ETH_RMIIREF_ CLK				EVENTOUT	
1	16		1/0	YES	ADC123_IN2	-	TIM2_CH3	TIM5_CH3	TIM9_CH1	-	-	-	USART2_TX		-	-	ETH_MDIO	-		-	EVENTOUT	
1			I/O	YES	ADC123_IN3	-	TIM2_CH4	TIM5_CH4	TIM9_CH2	-	-	-	USART2_RX	-	-	OTG_HS_ULPI_D0	ETH_MII_COL	-	-	-	EVENTOUT	
Part			<u> </u>	-	-	-	-	-		-	-	-	-		-	-	-	-	-	+ :	-	1
Part																						ULED
					ADC10 INV							epia Noc										(User LED 0)
2	20	PA4	1/0	NO (3.3V)	DAC1_OUT	-	-	-	-	-	SPI1_NSS	12S3_WS	USART2_CK	-	-	-	_	OTG_HS_SOF	DCMI_HSYNC	-	EVENTOUT	JMP1 ULED
2	24	DAE	1/0	NO (2.2) 0	ADC12_IN5 /		TIM2_CH1		TIME CHAN		enii ecii					OTO HE UI DI OV					EVENTOUT	<u> </u>
Part	22							TIM3 CH1							TIM13 CH1	OTG_HS_ULPI_CK	-		DCMI PIXCK			
1																	ETH MII RX DV		non			
10	23	PA7	1/0	YES	ADC12_IN7	-	TIM1_CH1N	TIM3_CH2	TIM8_CH1N	-	SPI1_MOSI	-	-	-	TIM14_CH1	-	ETH_RMII_CRS_DV	-	-	+-	EVENTOUT	-
Part	24	PC4	1/0	YES	ADC12_IN14	-	-			-	-	-			-	-	ETH_RMII_RXD0	-		1.	EVENTOUT	
1																	ETH _MII_RXD1					
Part						-	TIM1 CH2N	TIM3 CH3	TIMS CH2N	-	-	-	-		-	OTG HS UI PL D1		-		+ :		
2	27					-				-	-	-	-	-	-			-		-		
Part		PB2-BOOT1		1000																		
10	28	(PB2)	1/0	YES		-	-	-	-	-		-	-	-	-	-	-	-	-	+ -	EVENTOUT	
1	29	PB10	1/0	YES		-	TIM2_CH3	-	-	I2C2_SCL	12S2_CK	-	USART3_TX	-	-	OTG_HS_ULPI_D3		-	-	-	EVENTOUT	
1	20	DD44		VEC			TIMO CILIA			1000 004			LICADTA DV			OTO HE HIDI DA	ETH_MILTX_EN				D/DITOUT	
Second Color			-	-	-	-	- TIW2_C114	-	-	IZCZ_SDA	-	-	- USAK15_KX	-	-	-		-	-	-		-
Part			-	-		-	-			-	-	-		-	-	-	-	-	-	-	-	-
Part	22	DD12	1/0	VEC			TIM4 DVIN			ISCS CMBA	SPI2_NSS		HEADTS OF		CAND DV	OTO HE HIDI DE	ETH_MIL_TXD0	OTO HE ID			EVENTOUT	USB ID
Part	33	FBIZ	100	TES			TIMIT_DIGIN	-	-	IZCZ_SWIDA			USAK15_CK		CANZ_KX	OTG_TIS_OEFT_DS		OTG_NG_ID		+		
18	34	PB13	1/0	YES	OTG_HS_VBUS	-	TIM1_CH1N	-	-	-	12S2_CK	-	USART3_CTS	-	CAN2_TX	OTG_HS_ULPI_D6	ETH_RMII_TXD1	-	-	-	EVENTOUT	
Second Column	35	PB14	1/0	YES			TIM1 CH2N		TIM8 CH2N	_	SPI2 MISO	I2S2ext SD	USART3 RTS		TIM12 CH1	_	_	OTG HS DM			EVENTOUT	
17 17 17 17 17 17 17 17																						USB D+
PC	36					RTC_50Hz	TIM1_CH3N	TIMO CHI		-	I2S2_SD	-	-	LICADTE TV	TIM12_CH2	-	-	OTG_HS_DP	DOME DO	-		(USB1 connector)
30 PGS 10 VSS			1/0	YES		-	-	TIM3_CH2	TIM8_CH2		LOL_MOR	I2S3_MCK		USART6_RX				SDIO_D7	DCMI_D1		EVENTOUT	
## PAS 10 VIS MACCO TIME CHI TIME CHI																						MicroSD Card SD 4bits
## PATS NO YES MCCS MC	39	PC8	1/0	YES		-	-	TIM3_CH3	TIM8_CH3					USAR16_CK				SDIO_D0	DCMI_D2		EVENTOUT	
E						MCO2		TIM3_CH4	TIM8_CH4	I2C3_SDA	I2S_CKIN							SDIO_D1	DCMI_D3			SDIO_D1
A					OTO ES VIDIO	MCO1		-	-	I2C3_SCL	-	-		-	-	OTG_FS_SOF	-	-	DOME DO	-		
44 PA11 10 VES	43				OTG_FS_VBUS					.203_SMBA						OTG_FS_ID			DCMI_D1	±÷		
## PAIS 10 YES		DA44	1/0	VEC											CAN'S DY						EVENTOUT	USB D-
A	44	PATT	1/0	TES				-					USAR IT_CIS		CAN1_RX	UIG_FS_DM	-			1	EVENTOUT	
ACCUPATION ACC	45		1/0	YES		-	TIM1_ETR	-	-	-	-	-	USART1_RTS	-	CAN1_TX	OTG_FS_DP	-	-	-	-	EVENTOUT	(USB0 connector)
ACCUPATION ACC	46	PA13 (JTMS-SWDIO)	I/O	YES		JTMS-SWDIO											_				EVENTOUT	SWDIO
AB	47		-	-	-	-	-	-		-	-	-		-	-	-	-	-	-	-	-	-
April Commonwealth Commonwealt		VDD	-	-	-	-	-	-			-	-		-	-	-	-			-	-	-
PA15 FOLD	49	PA14 (JTCK-SWCLK)	I/O	YES		TCK-SWCLK										_					EVENTOUT	SWCLK
SPI PC10 IO YES		PA15					TIM 2_CH1					SPI3_NSS/										
Second S	50	(JTDI)	1/0	YES		JTDI	TIM 2_ETR	-	-	-	SPI1_NSS	I2S3S_WS	-	-	-	-	-	-	-	-	EVENTOUT	Missage Cond CD C
Second S	51	PC10	I/O	YES		-						SPI3_SCK/ I2S3S_CK	USART3_TX/	UART4_TX	-	-	-	SDIO_D2	DCMI_D8	-	EVENTOUT	SDIO_D2
S																						MicroSD Card SD 4bits
S	52	PC11	1/0	YES							i2S3ext_SD		USART3_RX	UART4_RX		-	-	SDIO_D3	DCMI_D4	1	EVENTOUT	
Second Pock	53	PC12	1/0	YES		-	-	-	-	-	-	I2S3_SD	USART3_CK	UART5_TX	-	-	-	SDIO_CK	DCMI_D9	-	EVENTOUT	SDIO_CK
P83 C		DDA	1/0	VEC				TIMO ETC						HADTS DV				epic cur	DCM D4		EVENTOL	MicroSD Card SD 4bits
ST TRACESWO 10 VES TRACESWO TMQ CH2	54		I/O	168				TIMO_ETR		-	-	-		UAR15_KX		-	-	SDIO_CMD	DCMI_D11	-	EVENTOUT	ODIO_CMD
Fig.	1	(JTDO/		1000	1	JTDO/		1				SPI3_SCK			1						m.m.m.e:	
F			1/0	YES	 	TRACESWO	TIM2_CH2	<u> </u>	-	-	SPI1_SCK	12S3_CK	-	-	-	-	-	-	-	+-	EVENTOUT	
Fig.	56	(NJTRST)	1/0	YES		NJTRST	-	TIM3_CH1	-	-	SPI1_MISO		I2S3ext_SD	-	-	-	-	-	-	-	EVENTOUT	
Se PB6 IO YES	57	PR5	1 1/0	VES	_	1 .	Ι	TIM3 CH2	l . T	I2C1 SMPA	SPI1 MOSI	SPI3_MOSI	_	l . –	CANS BY	OTG HS UI PL D7	ETH PPS OUT	I	DCMI D10	1.	EVENTOUT	
SP	58	PB6	I/O	YES				TIM4_CH1		I2C1_SCL						-			DCMI_D5	±Ξ	EVENTOUT	
BOOTD		PB7	I/O	YES		-	-	TIM4_CH2	-	I2C1_SDA			USART1_RX	-		-	-	FSMC_NL	DCMI_VSYNC	-	EVENTOUT	
61 PB8 10 YES - 1TAM-CH3 TM10_CH1 ECT_SCL CANT_RX - ETH_MII_TXXX SDIO_CH DCM_DB EVENTOUT 62 PB9 10 YES - TAM-CH4 TM11_CH1 ECT_SCA EXS_VS - CANT_TX - SDIO_DB DCM_DT EVENTOUT 63 VSS	60	воото	1														_					Default Boot from Flash (pull-down resistor 2.2Kohms)
62 PB9 I/O YES TIMM CH4 TIM11 CH1 IZC1 SDA IZSZ WS CAN1 TX SDIO D5 DCMI D7 - EVENTOUT			1/0	YES		-	-	TIM4_CH3	TIM10_CH1	I2C1_SCL	-	-	-	-	CAN1_RX	-	ETH_MII_TXD3	SDIO_D4	DCMI_D6	-	EVENTOUT	
83 VSS	62	ppo	100	VEC				TIMA CHA	TIM11 CH	12C1 9D4	SPI2_NSS		l . –	l	CAN1 TV			SDIO DE	DCMI D7		EVENTOUT	
64 VDD			-	- 163	-	-	-	- CH4		IZUI_SUA	1232_WS	-		-		-	-	- 3010_03	- DOMI_D/	-	-	
Bin 2 2 V Code II Not GV Adversed			-	-		-	-	-		-	-	-		-	-	-	-		-	-	-	-
	Pin 3	3V Only II Not 5V	tolerant						1													