Hardware Engineering

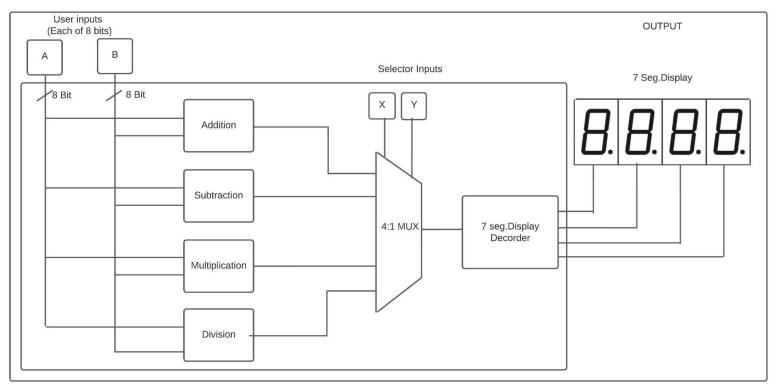
Team: Exemplary



Team members:

Arafat Kamal Tasawar Siddiquy Nirojan Navaratnarajah. ASM Nurussafa

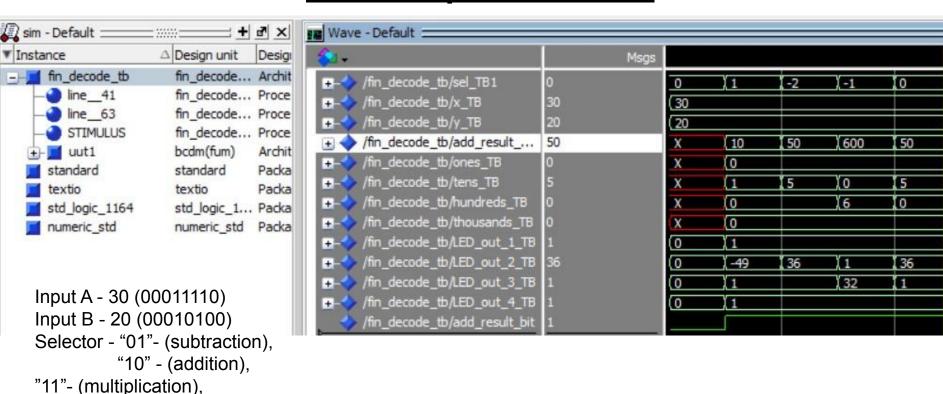
Concept



Block Diagram

components

VHDL Implementation

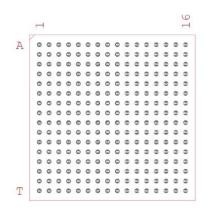


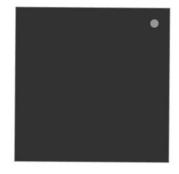
"00" - (Division)

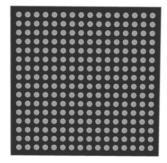
FPGA

• EP4CE22E22C8N from Intel.

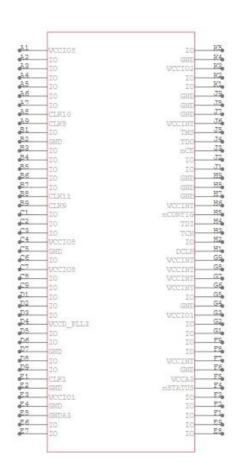
Library downloaded from <u>Ultralibrarian.com</u>



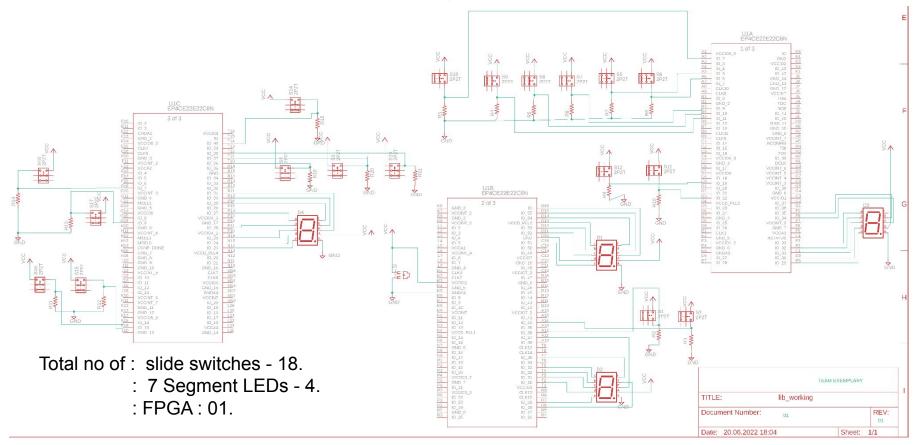




- FPGA CycloneR IV E Family
- 22320 Cells 60 nm Technology
- ♦ 1.2V
- 144-Pin EQFP EP



Schematic diagram for the PCB



Further requirements

- Inclusion of Power supply for the PCB.
- Implementation of JTAG programmer.
- Use of Oscillator/Clock module .

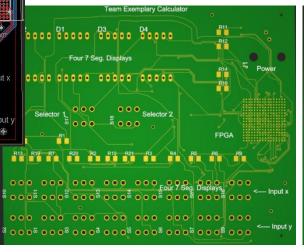
Testing/refining

- Should meet the EMC Regulations
- EMI requirments/tests.
- Output/Input ports accessibility. (e.g ;power jack)
- Refine the distance between traces.

000 900 000 000 000 000 000 000 ---- Input y 000

000 000 000 000 000

(Printed Circuit Board) No. of layers -2



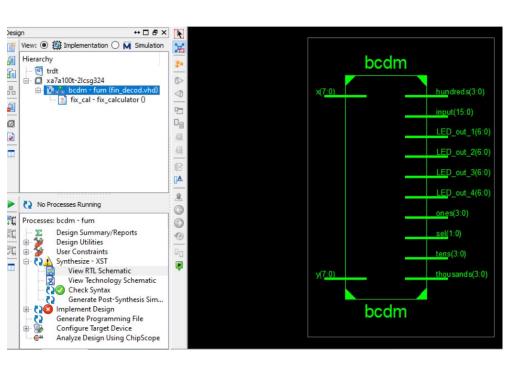
Front View (Top layer)

Rear View (bottom layer)

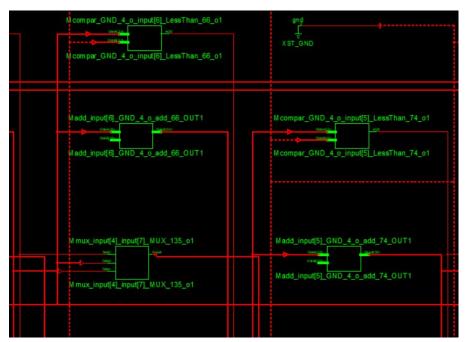
PCB Layout - Size : 4610 x 3453 mil

Track width 6 mil.

RTL??



RTL





BOM List and estimated price of PCB components

Qty Value	Device	Package	Parts	Description	Unit price	price sum
1	215876-7	215876-7	J1	AMP connector	~ 2\$	2
	4 7SEG-CA	7SEG-13	D1, D2, D3, I	D4 7-segment DISPLAY	\$0.84	\$3.36
Ť	8 R-US_M1206	M1206	R1- R21	RESISTOR, American symbol	\$0.11	\$1.98
1	8 2P2T	2P2T	S1-S18	SLIDE SWITCH 2P2T Part No. SS-22F05-G(A)4	\$1.48	\$26.64
	1 EP4CE22E22C8N	EP4CE22E22C8N	FBGA256	_THIN_WIRE-BOND-A:1.55_	\$71.04	\$71.04
Total cost						\$105.02

Resources

1.https://www.facebook.com/cyhardwareengineer/

Thank you for your attention!