

	#Week	07.05.2022	21.05.2022	04.06.2022	18.06.2022
	Task	Task 1	Task 2	Task 3	Task 4
	Short summary	Project: Implement a 8-bit Calculator.	Project: Implement a 8-bit Calculator.	Project: Implement a 8-bit Calculator.	Project: Implement a 8-bit Calculator.
Asm Nurussafa	To-do	Research about the project topic, VHDL.	- Work on VHDL on implementing basic components and Divider and verifying using testbenches for Multiplier.	- Refine basic components and Divider on VHDL and test-bench for Multiplier. - Work on putting all of the components together in VHDL. - Discussion on creating PCB Design.	- Refine and finalise basic components and Divider on VHDL and test-bench for Multiplier. - Finalise all components of the calculator. - Finalise the PCB Design. - Work on the final presentation and documentation of the project.
	Status	Done	Done	Done	Done
Tasawar Siddiquy	Short summary	Research about the project topic, VHDL.	- Work on VHDL on implementing basic components and Addition, Substraction.	- Refine basic components and Addition, Substraction on VHDL - Work on VHDL on implementing BCD - Work on putting all of the components together in VHDL. - Discussion on creating PCB Design.	- Refine basic components and Addition, Substraction, BCD Decoder - Finalise all components of the calculator. - Finalise the PCB Design. - Work on the final presentation and documentation of the project.
	To-do	Done	Done		
Arfat Kamal	Short summary	Research about the project topic, VHDL.	- Work on VHDL on implementing basic components and Half Adder ,Full Adder	- Refine basic components and Adder on VHDL - Work on putting all of the components together in VHDL. - Discussion on creating PCB Design.	- Finalise all components of the calculator. - Finalise the PCB Design. - Work on the final presentation and documentation of the project.
	To do	Done	Done	Done	Done
Nirojan Navaratnarajah	Short summary	Research about the project topic, VHDL.	- Work on VHDL on implementing basic components ,Multiplier , Multiplexer and BCD to 7 seg.Decorder	- Refine basic components,Multiplier,Multiplexer ,decoder on VHDL. - Work on putting all of the components together in VHDL. - Discussion on creating PCB Design.	- Refine and finalise basic components ,Multiplier,Decorder on VHDL and test-bench for overall calculator project - Finalise all components of the calculator. - Finalise the PCB Design. - Work on the final presentation and documentation of the project. - Work on the Xilinx ISE part for synthesizing the code
	To-do	Done	Done	Done	Done