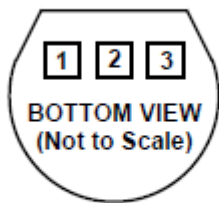


Conexión TMP36 a placa MakerAlc Lora

Figure 3. R-8 (SOIC_N)



PIN 1, $+V_S$; PIN 2, V_{OUT} ; PIN 3, GND

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Figure 4. T-3 (TO-92)

TMP36	Placa
Pin1 →	A2
Pin2 →	A1
Pin3 →	GND

