

- 0: INC SUB FSM
- 1: INC INSTR
- 2: 8 HZ
- 3: 400 HZ
- 4: FULL SPEED

- 1: THIS OUPUT CONNECTS TO ALL FLASHWE# ON THE REGISTERS
- 2: GENERAL WE# OUTPUT THAT CONNECTS TO WE#2 ON REGISTERS
- 3: GENERAL OE# OUTPUT AND CE# OUTPUT FOR RAM
- 4: VALUES FOR THE FULL SPEED OSCILLATOR WILL BE DETERMINED EXPERIMENTALLY

J2:	OUTPUT:	BOARD:
16 (DQ1 U2)	PC OE1#	5
15 (DQ2 U2)	PC WE1#	5
14 (DQ3 U2)	DIP OE1#	5
13 (DQ4 U2)	CSP+ +	5
12 (DQ5 U2)	CSP--	5
11 (DQ6 U2)	PC++	5
10 (DQ7 U2)	PC--	5
9 (DQ0 U3)	INSTR OE1#	5
8 (DQ1 U3)	STACK OE1#	5
7 (DQ2 U3)	STACK WE1#	5
6 (DQ3 U3)	MAR WE1#	3
5 (DQ4 U3)	MEM RAM OE1#	3
4 (DQ5 U3)	MEM RAM WE1#	3
3 (DQ6 U3)	REG FILE OE1#	1
2 (DQ7 U3)	**UNASSIGNED**	
1 (DQ7 U4)	**UNASSIGNED**	
P1:	REG FILE WE1#	1
1 (DQ0 U4)	IR WE1#	4
2 (DQ1 U4)		
P2:	PO WE1#	4
1 (DQ2 U4)	P1 WE1#	4
2 (DQ3 U4)		
P3:	IR MUX BIT 0	4
1 (DQ4 U4)	IR MUX BIT 1	4
2 (DQ5 U4)	ALU OE1#	4
J10:		
1 (DQ6 U4)	**UNASSIGNED**	

