NOTES:
1: THIS OUPTUT CONNECTS TO ALL FLASHWE# ON THE REGISTERS
2: GENERAL WE# OUTPUT THAT CONNECTS TO WE#2 ON REGISTERS
3: GENERAL OE# OUTPUT AND CE# OUTPUT FOR RAM

ROTARY SWITCH POSITIONS:
0: INC SUB FSM
1: INC INSTR
2: 8 HZ
3: 400 HZ
4: FULL SPEED

U63 LOCATED ON DAUGHTER BOARD ON BOARD 4

FSM OUTPUT CONNECTIONS

PIN NUMBER	HEADER PIN	IC PIN	OUTPUT:	BOARD	POLARITY
1	J20:	(DQ4 U2)	PC OE1#	5	ACTIVE LOW
2	J21:	(DQ2 U2)	PC WE1#	5	ACTIVE LOW
3	J22:	(DQ3 U2)	DIP OE1#	5	ACTIVE LOW
4	J23:	(DQ7 U3)	CSP++	5	ACTIVE LOW
5	J24:	(DQ6 U3)	CSP--	5	ACTIVE LOW
6	J25:	(DQ0 U3)	PC++	5	ACTIVE LOW
7	J26:	(DQ5 U3)	PC--	5	ACTIVE LOW
8	J27:	(DQ1 U3)	INSTR OE1#	5	ACTIVE LOW
9	J28:	(DQ2 U3)	STACK OE1#	5	ACTIVE LOW
10	J29:	(DQ4 U3)	STACK WE1#	5	ACTIVE LOW
11	J30:	(DQ7 U2)	MAR WE1#	3	ACTIVE LOW
12	J31:	(DQ0 U2)	MEM RAM 0E1#	3	ACTIVE LOW
13	J32:	(DQ6 U2)	MEM RAM WE1#	3	ACTIVE LOW
14	J33:	(DQ5 U2)	REG FILE 0E1#	1	ACTIVE LOW
15	J34:	(DQ3 U4)	CCMP	4	ACTIVE LOW
16	J35:	(DQ3 U3)	INTRES	2	ACTIVE LOW
17	J14:	(DQ1 U2)	REG FILE WE1#	1	ACTIVE LOW
18	J15:	(DQ7 U4)	IR WE1#	4	ACTIVE LOW
19	J16:	(DQ0 U4)	P0 WE1#	4	ACTIVE LOW
20	J17:	(DQ6 U4)	P1 WE1#	4	ACTIVE LOW
21	J18:	(DQ4 U4)	IR MUX BIT 0	4	ACTIVE HIGH
22	J19:	(DQ1 U4)	IR MUX BIT 1	4	ACTIVE HIGH
23	J10:	(DQ5 U4)	ALU OE1#	4	ACTIVE LOW
24	N/A	(DQ2 U4)	SET SUB	2	ACTIVE LOW

U22 U24 LOCATED ON DAUGHTER BOARD ON BOARD 4

TITLE:	Romulus I FSM, Clock, and Reset Board	REV:	1.0
	Company: WAWA	Sheet:	1/1
	Date: 2024-09-17	Drawn By:	Wolfgang Ploch