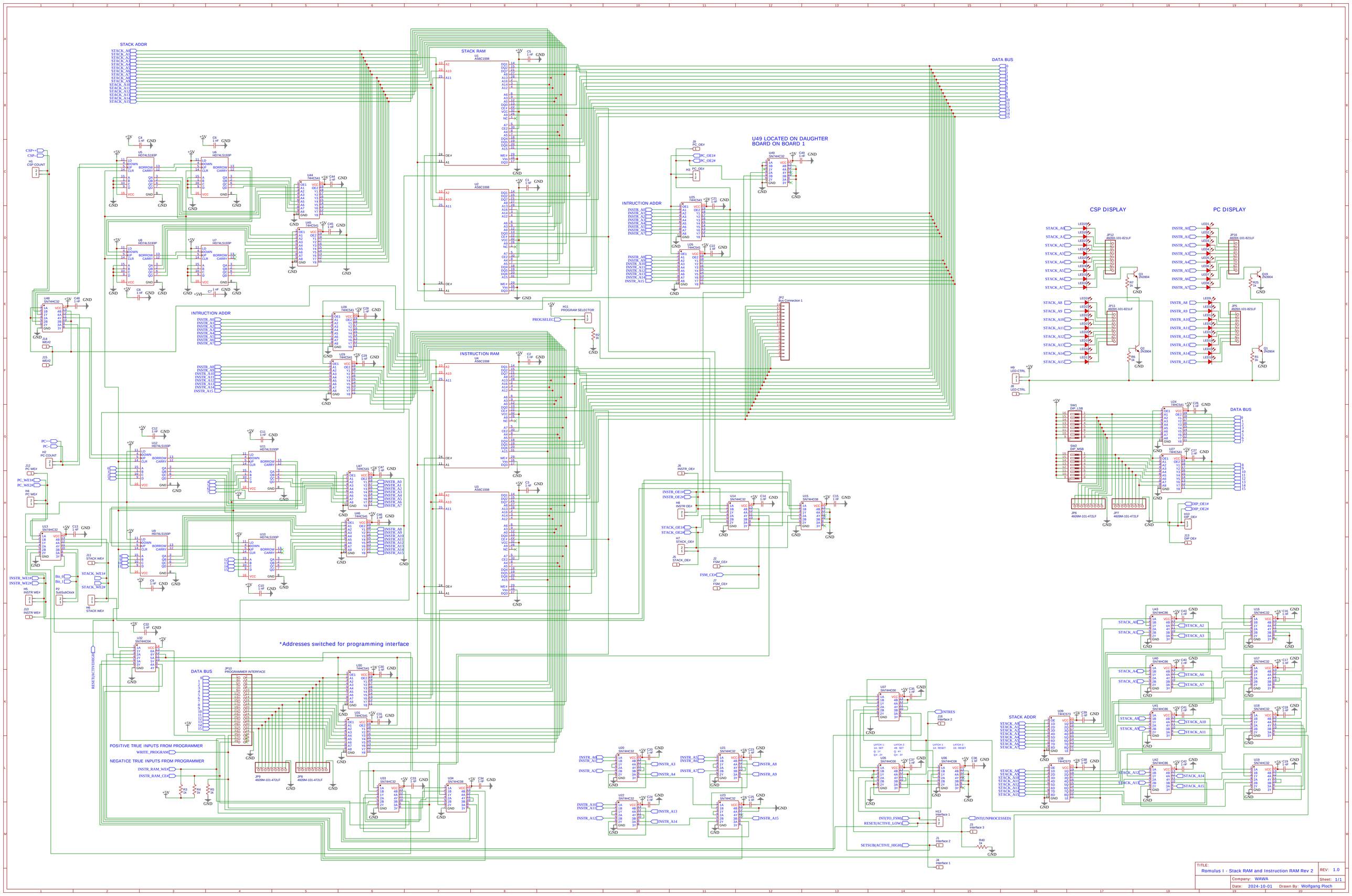
STACK ADDR
STACK RAM
U1 AS6C1008
U2 AS6C1008
DATA BUS
U49 LOCATED ON DAUGHTER BOARD ON BOARD 1
INTRUCTION ADDR
CSP DISPLAY
PC DISPLAY
INSTRUCTION ADDR
INSTRUCTION RAM
U3 AS6C1008
U4 AS6C1008
PROGRAM SELECTOR
JP2 Bus Connection 1
*Addresses switched for programming interface
DATA BUS
JP10 PROGRAMMER INTERFACE
POSITIVE TRUE INPUTS FROM PROGRAMMER
NEGATIVE TRUE INPUTS FROM PROGRAMMER
STACK ADDR
TITLE:
Romulus I - Stack RAM and Instruction RAM Rev 2
REV: 1.0
Company: WAWA
Sheet: 1/1
Date: 2024-10-01
Drawn By: Wolfgang Ploch