ASRA FATHIMA

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PROFILE

To Secure a position where I can effectively apply my skills and expertise to add significantly value to the organization , while simultaneously expanding my professional capabilities within a positive and collaborative team and I am looking forward to my first work experience.

INTERNSHIP(DHEE CODING LAB)

I have been pursuing a Java Full Stack internship at Dhee Coding Lab from past 4 months ,gaining experience in ,Sql, Core Java , collection frameworks and frontend technologies.

SKILLS

JAVA

- Familiar with Object-Oriented Programming
- Basics understanding of Java Interfaces
- Knowledge of abstract classes.
- Knowledge of exception handling .
- Knowledge on File Handling.
- Basic knowledge on Collection frameworks.

HTML and CSS

- Knowledge on HTML
- Familiar with HTML tags for formatting, linking
- Knowledge of designing simple web pages using headings, paragraphs, lists, and forms.
- Knowledge on CSS

SQL

- Basic knowledge on SQL

EDUCATION

2021 - 2025 | **R.L.Jalappa Institute of Technology (Doddaballapur)** CGPA:9.28/10
Bachelor of Engineering - Electronics and communication engineering

2019 - 2021 | St.Joseph's Convent Girls PU College(Chickballapur) Percentage:94%
Intermediate-PCM (Physics ,Chemistry, ,Mathametics)

2019 | St.Joseph's Convent Girls High School (Chickballapur) Percentage:83%
SSLC/10th -(Secondary School Leaving Certificate)

ACADEMIC PROJECTS

- **Multilingual Weather Forecast Generator using Core Java**

Developed a Core Java application to simulate a weather forecasting system that provides random but realistic weather data including temperature, pressure, humidity, and weather conditions for multiple cities.
The project supports multiple languages like English, Kannada, Urdu, Hindi, and Telugu, enhancing accessibility and localization.

- **Design Of Digital One Day Clock Using VLSI**

The project emphasizes the use of basic digital logic design techniques, including counters, registers, multiplexers, and clock division circuits, to achieve a fully functional clock model.
By implementing the design at the VLSI level, the clock achieves faster operation speed, reduced power consumption, and minimized chip area, making it highly suitable for embedded system application

CERTIFICATION

- Certificate in Web development
- Persuing Java Full Stack Development at Dhee Coding Lab

LANGUAGES

ENGLISH HINDI KANNADA