W806 Chip Design Guide V1.0 Winner Micro Address: 18th Floor, Yindu Building, No. 67, Fucheng Road, Haidian District, Beijing Tel: +86-10-62161900 Website: www.winnermicro.com Document modification record Version Revision Time Revision History Author Review V1.0 2021/08/23 First draft Linda content Document Modification History 1 Overview......7 2 Pin Definitions.....7 3 Chip Peripheral Circuit Design11 3.1 RESET reset circuit design.....11 3.2 Design of reference clock circuit.....11 3.3 ADC circuit design.....12 3.4 WAKEUP circuit design.....12 3.5 GPIO Design.....13 3.6 Touch Sensor Design.....13

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1 Overview

The W806 chip is a secure IoT MCU chip. The chip integrates a 32-bit CPU processor with built-in UART, GPIO, SPI, SDIO,

I2C, I2S, PSRAM, 7816, ADC, LCD, TouchSensor and other digital interfaces; support TEE security engine, support a variety of hardware

Decryption algorithm, built-in DSP, floating-point arithmetic unit and security engine, support code security permission setting, built-in 1MB Flash memory, support

Firmware encrypted storage, firmware signature, security debugging, security upgrade and other security measures to ensure product security features. Suitable for small appliances, smart

It can be used in a wide range of IoT fields such as home furnishing, smart toys, industrial control, and medical monitoring.

2 Pin Definition

Figure 2-1 Pin layout (QFN56)

Table 2-1 Pin Assignment Definition (QFN56)

No. Name Type Pin function after reset Alternate function Maximum frequency Pull-up and pull-down capability Drive capability

1 PB 18 I/O GPIO, input, high impedance UART5 TX/LCD SEG30 10MHz UP/DOWN 12mA

2 PB_26 I/O GPIO, input, high impedance LSPI_MOSI/PWM4/LCD_SEG1 20MHz UP/DOWN 12mA

3 PB 25 I/O GPIO, input, high impedance LSPI MISO/PWM3/LCD COM0 20MHz UP/DOWN 12mA

4 PB_24 I/O GPIO, input, high impedance LSPI_CK/PWM2/LCD_SEG2 20MHz UP/DOWN 12mA

5 PB_22 I/O GPIO, input, high impedance UARTO_CTS/PCM_CK/LCD_COM2 10MHz UP/DOWN 12mA

6 PB_21 I/O GPIO, input, high impedance UARTO_RTS/PCM_SYNC/LCD_COM1 10MHz UP/DOWN 12mA

7 PB 20 I/O UART RX UARTO RX/PWM1/UART1 CTS/I2C SCL 10MHz UP/DOWN 12mA

8 PB_19 I/O UART_TX UARTO_TX/PWM0/UART1_RTS/I2C_SDA 10MHz UP/DOWN 12mA

9 WAKEUP I WAKEUP Wake-up function DOWN

10 RESET I RESET Reset UP

11 XTAL OUT O External crystal oscillator output

12 XTAL IN I External crystal input

```
13 VDD33 P chip power supply, 3.3V
14 NC
15 VDD33 P chip power supply, 3.3V
16 VDD33 P chip power supply, 3.3V
17 VDD33 P chip power supply, 3.3V
18 BOOTMODE I/O BOOTMODE I2S_MCLK/LSPI_CS/PWM2/I2S_DO 20MHz UP/DOWN 12mA
19 PA_1 I/O JTAG_CK
JTAG_CK/I2C_SCL/PWM3/I2S_LRCK/AD
C_1
20MHz
UP/DOWN
12mA
20 PA_2 I/O GPIO, input, high impedance
UART1_RTS/UART2_TX/PWM0/UART3_RT
S/ADC_4
20MHz
UP/DOWN
12mA
21 PA_3 I/O GPIO, input, high impedance
UART1_CTS/UART2_RX/PWM1/UART3_CT
S/ADC_3
20MHz
UP/DOWN
12mA
22 PA_4 I/O JTAG_SWO
JTAG_SWO/I2C_SDA/PWM4/I2S_BCK/AD
C_2
20MHz
UP/DOWN
```

```
12mA
```

23 PA_5 I/O GPIO, input, high impedance

UART3_TX/UART2_RTS/PWM_BREAK/UAR

T4_RTS/VRP_EXT

20MHz

UP/DOWN

12mA

24 PA_6 I/O GPIO, input, high impedance

UART3_RX/UART2_CTS/NULL/UART4_CT

S/LCD_SEG31/VRP_EXT

20MHz

UP/DOWN

12mA

25 PA_7 I/O GPIO, input, high impedance

PWM4/LSPI_MOSI/I2S_MCK/I2S_DI/LC

D_SEG3/Touch_1

20MHz

UP/DOWN

12mA

26 PA_8 I/O GPIO, input, high impedance

PWM_BREAK/UART4_TX/UART5_TX/I2S_

BCLK/LCD SEG4

20MHz

UP/DOWN

12mA

27 PA_9 I/O GPIO, input, high impedance

MMC_CLK/UART4_RX/UART5_RX/I2S_LR

CLK/LCD_SEG5/TOUCH_2

50MHz

```
UP/DOWN
12mA
28 PA_10 I/O GPIO, input, high impedance
MMC_CMD/UART4_RTS/PWM0/I2S_DO/LC
D_SEG6/TOUCH_3
50MHz
UP/DOWN
12mA
29 VDD33 P chip power supply, 3.3V
30 PA_11 I/O GPIO, input, high impedance
MMC_DAT0/UART4_CTS/PWM1/I2S_DI/L
CD_SEG7
50MHz
UP/DOWN
12mA
31 PA_12 I/O GPIO, input, high impedance
MMC_DAT1/UART5_TX/PWM2/LCD_SEG8/
TOUCH_14
50MHz
UP/DOWN
12mA
32 PA_13 I/O GPIO, input, high impedance MMC_DAT2/UART5_RX/PWM3/LCD_SEG9 50MHz UP/DOWN
12mA
33 PA_14 I/O GPIO, input, high impedance
MMC_DAT3/UART5_CTS/PWM4/LCD_SEG1
0/TOUCH_15
50MHz
UP/DOWN
12mA
```

```
34 PA_15 I/O GPIO, input, high impedance
PSRAM_CK/UART5_RTS/PWM_BREAK/LCD
_SEG11
50MHz
UP/DOWN
12mA
35 PB_0 I/O GPIO, input, high impedance
PWM0/LSPI_MISO/UART3_TX/PSRAM_CK
/LCD_SEG12/Touch_4
80MHz
UP/DOWN 12mA
36
PB_1
I/O GPIO, input, high impedance
PWM1/LSPI_CK/UART3_RX/PSRAM_CS/L
CD_SEG13/Touch_5
80MHz
UP/DOWN 12mA
37
PB_2
I/O GPIO, input, high impedance
PWM2/LSPI_CK/UART2_TX/PSRAM_D0/L
CD_SEG14/Touch_6
80MHz
UP/DOWN 12mA
38
PB_3
I/O GPIO, input, high impedance
PWM3/LSPI_MISO/UART2_RX/PSRAM_D1
```

/LCD_SEG15/Touch_7

80MHz

UP/DOWN 12mA

39 PB_27 I/O GPIO, input