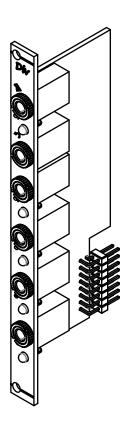


## **Clock Divider**







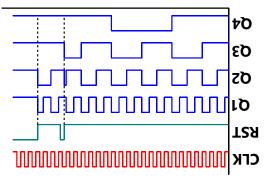


## CATs EuroSynth

Dr. Michael Zülch cats.synth@gmx.de

Current Draw	Am08 :VSI+ Am2 :VSI-
Maximum Depth	39mm w/o Connector
AtbiW	Z Hb

## Technical Specifications



.V0 of 192 Whenever a reset signal is sensed, all outputs are The module also has a reset (RST) input. levels of the output signals are 0V and about +5V. frequency down to 1/16 (Q1-Q4). The low/high sub-divided clock signals, from half the clock At the outputs, you have access to four different The clock input (CLK) will take any signal +/-10V. lower frequencies, particularly for rhythm uses. trigger or gate signals, designed to be a source of The module is a frequency divider for clock,

Function



some combination of those connector, the words "red", or -12V, a white stripe next to the pins are indicated with the label module's power connector. The line up with the -12V pins on the The red stripe on the cable must to the module before proceeding. bower cable is connected correctly Ensure the 10-pin connector on the

or equipment damage. to do so may result in serious injury disconnect the power cable. Failure off the power to the case and module from your case always turn When installing or removing a

Installation