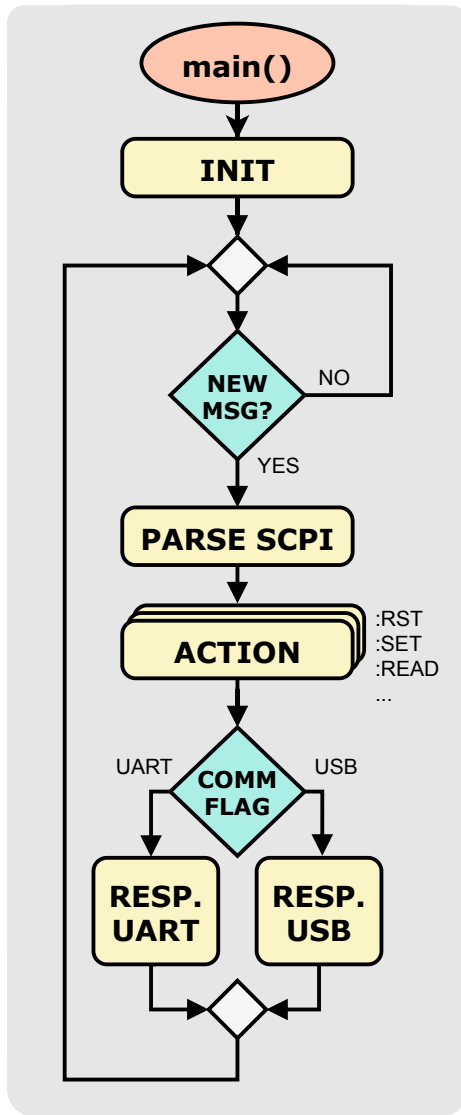
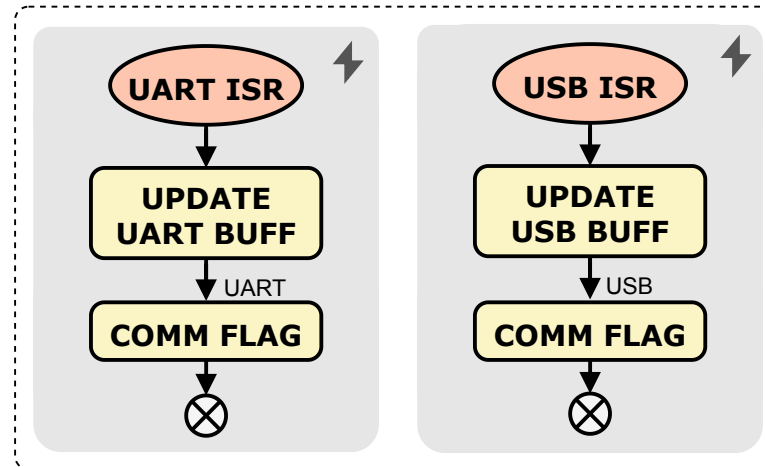


# UniLabTool - Firmware Design

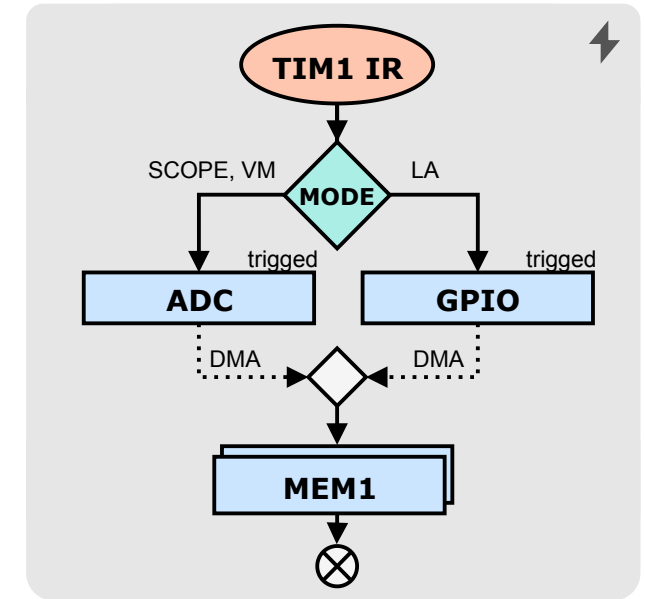
## Init & Process Msg



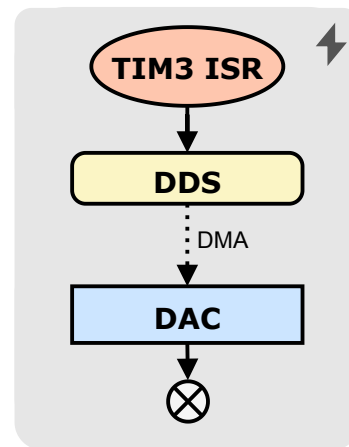
## Receive Msg



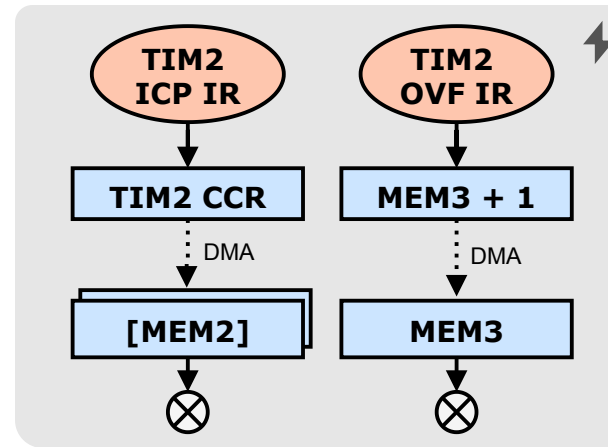
## Data Acquisition



## Signal Generator



## Counter



## Main Memory (MEM1)

SCOPE	LA	VM
n[4x 8/16-bit]	n[4x 8-bit]	5x 16-bit

Legend:

