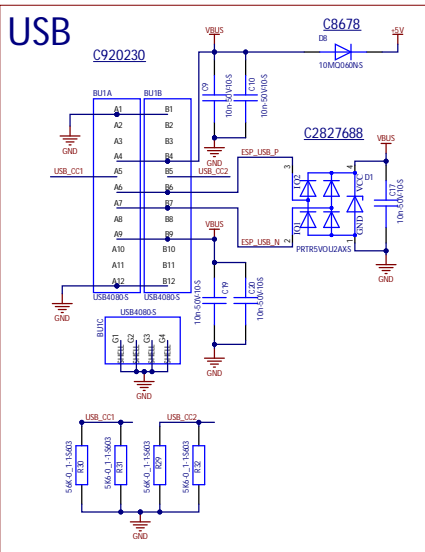
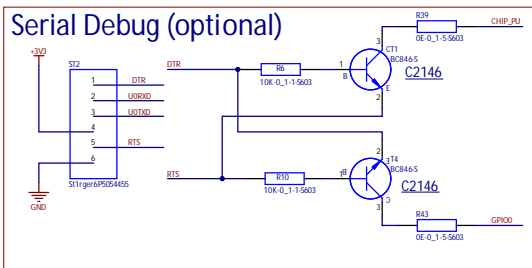


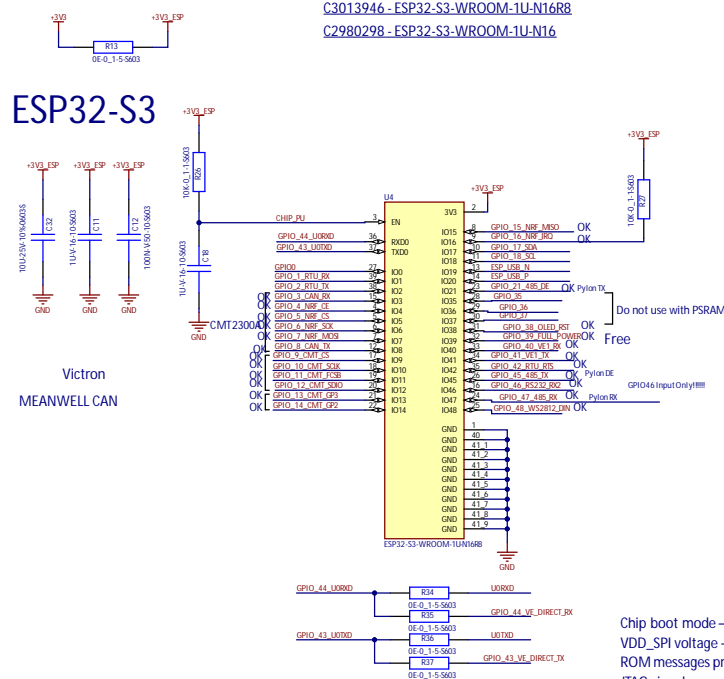
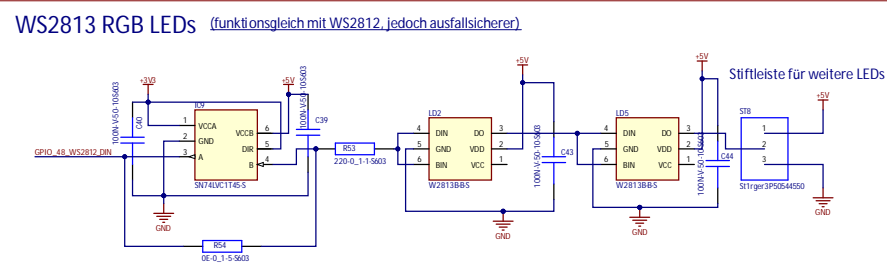
## USB



## Serial Debug (optional)

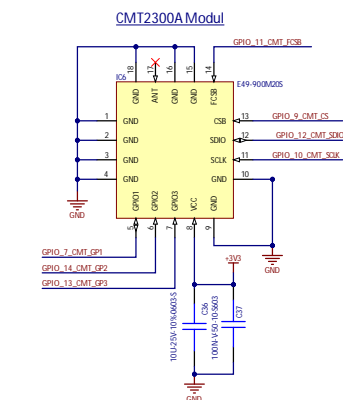
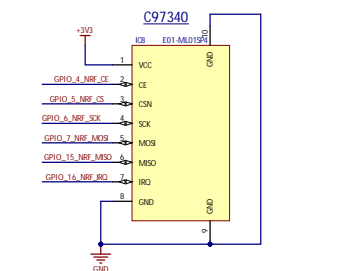
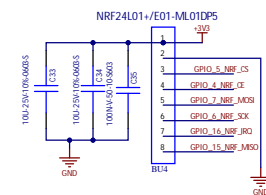


WS2813 RGB LEDs (funktionsgleich mit WS2812, jedoch ausfallsicherer)

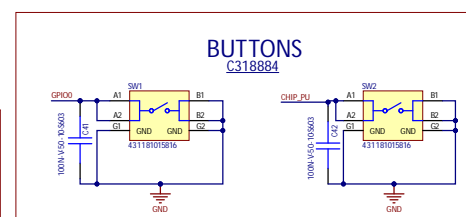
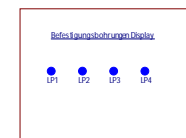
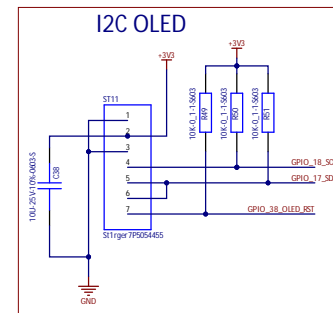


Chip boot mode – GPIO0 and GPIO46  
VDD\_SPI voltage – GPIO45  
ROM messages printing – GPIO46  
JTAG signal source – GPIO3

## RF



## I2C OLED



**Table 6: Chip Boot Mode Control**

Boot Mode	GPIO0	GPIO46
Default Configuration	1 (Pull-up)	0 (Pull-down)
SPI Boot (default)	1	Any value
Joint Download Boot <sup>1</sup>	0	0

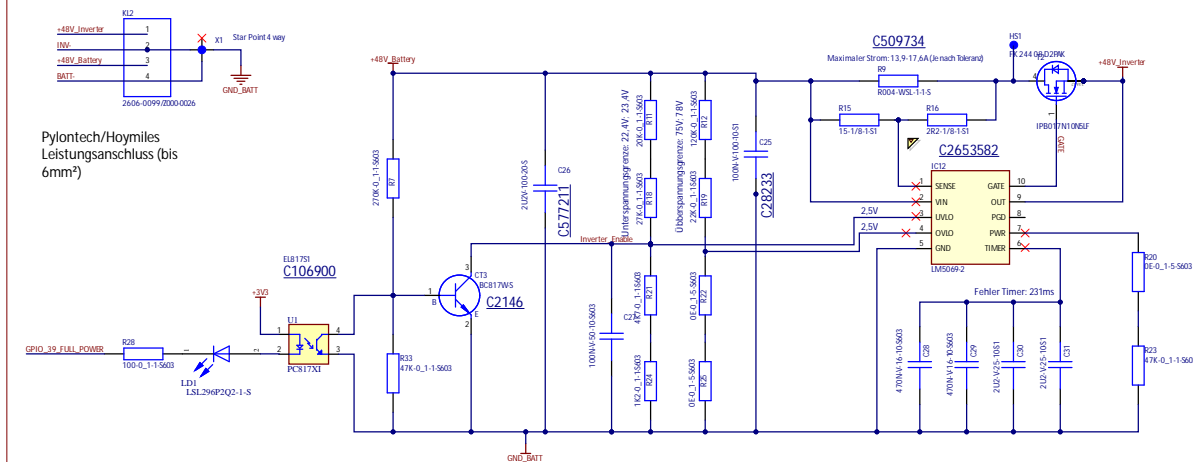
<sup>1</sup> Joint Download Boot mode supports the following download methods:

- USB Download Boot:
  - USB-Serial-JTAG Download Boot
  - USB-OTG Download Boot
- UART Download Boot

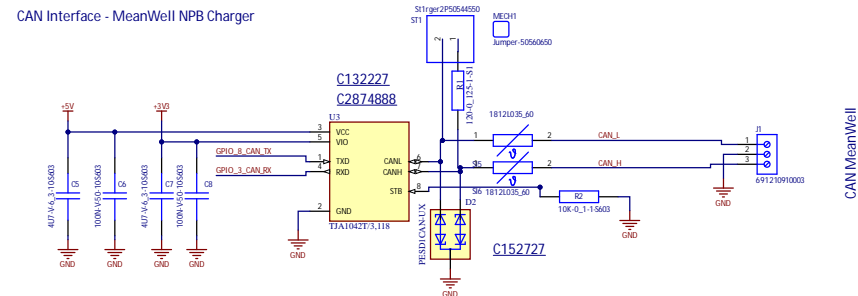
Title			OpenDTU-OnBattery - Meanwell Edition		
Size	Number			Revision	V1.0
A2					
Date:	2.25.2005		Sheet of	2	
File:	D:\ALTIUM\101 SchDoc		Drawn By:	Assafand	

# Interfaces

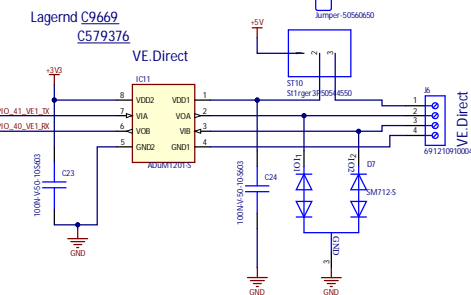
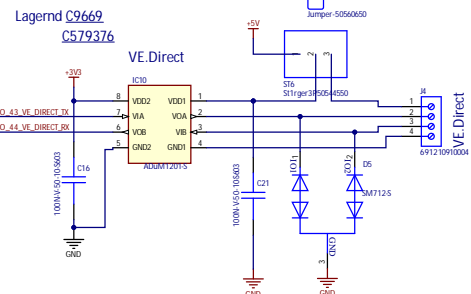
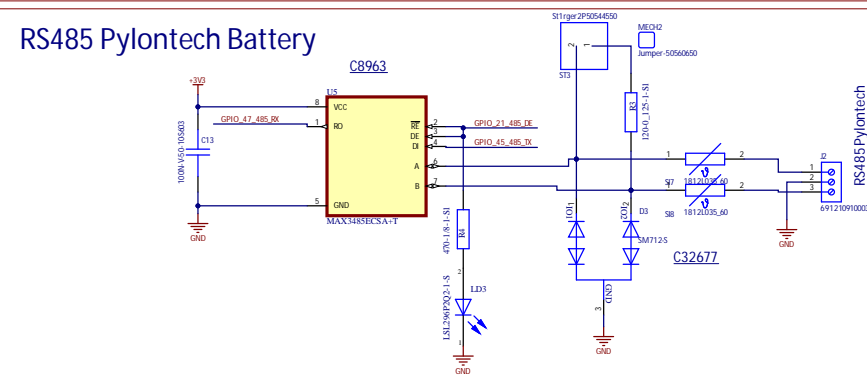
## Power-Switch (Inrush Current Controller)



## CAN Interface - MeanWell NPB Charger



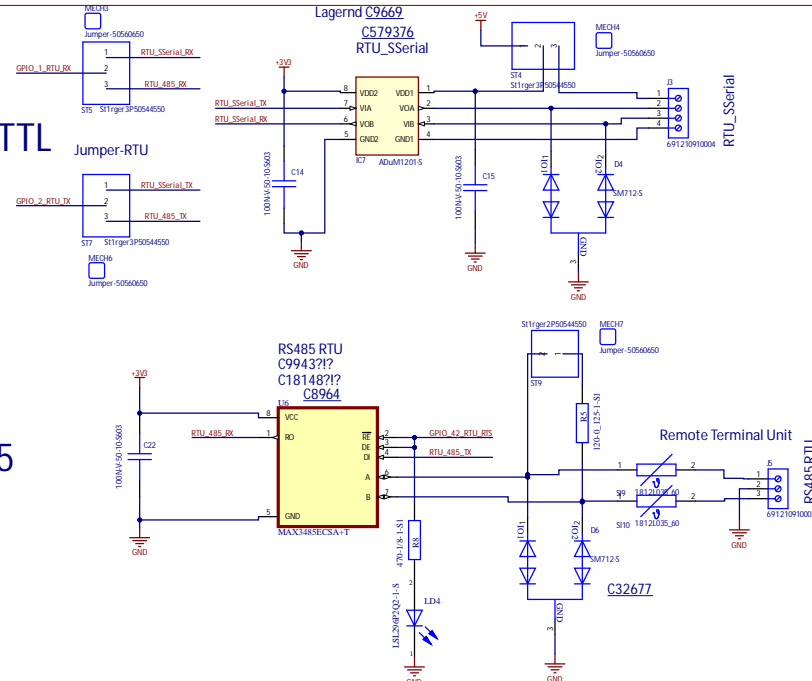
## RS485 Pylontech Battery



## RTU Modbus

## Serial TTL

## RS485



OpenDTU-OnBattery - MeanWell Edition			
Size	Number	Revision	
A2		V1.0	
Date	2.25.2025	Drawn By	Assolad
File	D:\ALTUM\VO2 Schöck	Drawn By	Assolad