

# **ROCm**

# Release Notes v3.3

Publication # 1.0 Revision: 1.0

Issue Date: April 2020

#### **Specification Agreement**

This Specification Agreement (this "Agreement") is a legal agreement between Advanced Micro Devices, Inc. ("AMD") and "You" as the recipient of the attached AMD Specification (the "Specification"). If you are accessing the Specification as part of your performance of work for another party, you acknowledge that you have authority to bind such party to the terms and conditions of this Agreement. If you accessed the Specification by any means or otherwise use or provide Feedback (defined below) on the Specification, You agree to the terms and conditions set forth in this Agreement. If You do not agree to the terms and conditions set forth in this Agreement, you are not licensed to use the Specification; do not use, access or provide Feedback about the Specification. In consideration of Your use or access of the Specification (in whole or in part), the receipt and sufficiency of which are acknowledged, You agree as follows:

- 1. You may review the Specification only (a) as a reference to assist You in planning and designing Your product, service or technology ("Product") to interface with an AMD product in compliance with the requirements as set forth in the Specification and (b) to provide Feedback about the information disclosed in the Specification to AMD.
- 2. Except as expressly set forth in Paragraph 1, all rights in and to the Specification are retained by AMD. This Agreement does not give You any rights under any AMD patents, copyrights, trademarks or other intellectual property rights. You may not (i) duplicate any part of the Specification; (ii) remove this Agreement or any notices from the Specification, or (iii) give any part of the Specification, or assign or otherwise provide Your rights under this Agreement, to anyone else.
- 3. The Specification may contain preliminary information, errors, or inaccuracies, or may not include certain necessary information. Additionally, AMD reserves the right to discontinue or make changes to the Specification and its products at any time without notice. The Specification is provided entirely "AS IS." AMD MAKES NO WARRANTY OF ANY KIND AND DISCLAIMS ALL EXPRESS, IMPLIED AND STATUTORY WARRANTIES, INCLUDING BUT NOT LIMITED TO IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, NONINFRINGEMENT, TITLE OR THOSE WARRANTIES ARISING AS A COURSE OF DEALING OR CUSTOM OF TRADE. AMD SHALL NOT BE LIABLE FOR DIRECT, INDIRECT, CONSEQUENTIAL, SPECIAL, INCIDENTAL, PUNITIVE OR EXEMPLARY DAMAGES OF ANY KIND (INCLUDING LOSS OF BUSINESS, LOSS OF INFORMATION OR DATA, LOST PROFITS, LOSS OF CAPITAL, LOSS OF GOODWILL) REGARDLESS OF THE FORM OF ACTION WHETHER IN CONTRACT, TORT (INCLUDING NEGLIGENCE) AND STRICT PRODUCT LIABILITY OR OTHERWISE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.
- 4. Furthermore, AMD's products are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or in any other application in which the failure of AMD's product could create a situation where personal injury, death, or severe property or environmental damage may occur.
- 5. You have no obligation to give AMD any suggestions, comments or feedback ("Feedback") relating to the Specification. However, any Feedback You voluntarily provide may be used by AMD without restriction, fee or obligation of confidentiality. Accordingly, if You do give AMD Feedback on any version of the Specification, You agree AMD may freely use, reproduce, license, distribute, and otherwise commercialize Your Feedback in any product, as well as has the right to sublicense third parties to do the same. Further, You will not give AMD any Feedback that You may have reason to believe is (i) subject to any patent, copyright or other intellectual property claim or right of any third party; or (ii) subject to license terms which seek to require any product or intellectual property incorporating or derived from Feedback or any Product or other AMD intellectual property to be licensed to or otherwise provided to any third party.
- 6. You shall adhere to all applicable U.S. import/export laws and regulations, as well as the import/export control laws and regulations of other countries as applicable. You further agree to not export, re-export, or transfer, directly or indirectly, any product, technical data, software or source code received from AMD under this license, or the direct product of such technical data or software to any country for which the United States or any other applicable government requires an export license or other governmental approval without first obtaining such licenses or approvals; or in violation of any applicable laws or regulations of the United States or the country where the technical

Release Notes v3.3

data or software was obtained. You acknowledge the technical data and software received will not, in the absence of authorization from U.S. or local law and regulations as applicable, be used by or exported, re-exported or transferred to: (i) any sanctioned or embargoed country, or to nationals or residents of such countries; (ii) any restricted end-user as identified on any applicable government end-user list; or (iii) any party where the end-use involves nuclear, chemical/biological weapons, rocket systems, or unmanned air vehicles. For the most current Country Group listings, or for additional information about the EAR or Your obligations under those regulations, please refer to the U.S. Bureau of Industry and Security's website at <a href="http://www.bis.doc.gov/">http://www.bis.doc.gov/</a>.

- 7. The Software and related documentation are "commercial items", as that term is defined at 48 C.F.R. §2.101, consisting of "commercial computer software" and "commercial computer software documentation", as such terms are used in 48 C.F.R. §12.212 and 48 C.F.R. §227.7202, respectively. Consistent with 48 C.F.R. §12.212 or 48 C.F.R. §227.7202-1 through 227.7202-4, as applicable, the commercial computer software and commercial computer software documentation are being licensed to U.S. Government end users (a) only as commercial items and (b) with only those rights as are granted to all other end users pursuant to the terms and conditions set forth in this Agreement. Unpublished rights are reserved under the copyright laws of the United States.
- 8. This Agreement is governed by the laws of the State of California without regard to its choice of law principles. Any dispute involving it must be brought in a court having jurisdiction of such dispute in Santa Clara County, California, and You waive any defenses and rights allowing the dispute to be litigated elsewhere. If any part of this agreement is unenforceable, it will be considered modified to the extent necessary to make it enforceable, and the remainder shall continue in effect. The failure of AMD to enforce any rights granted hereunder or to take action against You in the event of any breach hereunder shall not be deemed a waiver by AMD as to subsequent enforcement of rights or subsequent actions in the event of future breaches. This Agreement is the entire agreement between You and AMD concerning the Specification; it may be changed only by a written document signed by both You and an authorized representative of AMD.

#### DISCLAIMER

The information contained herein is for informational purposes only and is subject to change without notice. While every precaution has been taken in the preparation of this document, it may contain technical inaccuracies, omissions and typographical errors, and AMD is under no obligation to update or otherwise correct this information. Advanced Micro Devices, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this document, and assumes no liability of any kind, including the implied warranties of noninfringement, merchantability or fitness for particular purposes, with respect to the operation or use of AMD hardware, software or other products described herein. No license, including implied or arising by estoppel, to any intellectual property rights is granted by this document. Terms and limitations applicable to the purchase or use of AMD's products are as set forth in a signed agreement between the parties or in AMD's Standard Terms and Conditions of Sale. S

AMD, the AMD Arrow logo, Radeon, Ryzen, Epyc, and combinations thereof are trademarks of Advanced Micro Devices, Inc.

Google® is a registered trademark of Google LLC.

PCIe® is a registered trademark of PCI-SIG Corporation.

Linux is the registered trademark of Linus Torvalds in the U.S. and other countries.

Ubuntu and the Ubuntu logo are registered trademarks of Canonical Ltd.

Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies.

As of Jun 19, 2019. Radeon instinct" MiSD and MiBO "Vega 7nm" technology-based accelerators support PCIe" 6 end -07 providing up to 64 GB/s peak theoretical transport data bandwidth from CPL to GPU per card. Previous Gen Radeon Instinct compute GPU card are based on PCE 66 B-3. Di providing up to 32 GB/s peak theoretical transport rate bandwidth performance. Peak theoretical transport rate performance is calculated by Baud Rate 'width in bytes "## directions = GB/s per card. PCIe Gen3: 8" 2" 2" 2 GB/s. PCE 66 end: 15" 2" 2" 2" 64 GB/s.
Radeon Instinct" MISD and MIBO "Vega 7nm" technology-based accelerators include dual Infinity "Fabric" Units providing up to 184 GB/s peak theoretical GPU to GPU or Peer-to-Peer (PZP) transport rate bandwidth performance publienies are estimated only and may vary. Previous Gen Radeon Instinct compute GPU card. Combined with PCIE Gen 4 compatibility providing an aggregate GPU card I/O peak bandwidth of up to 248 GB/s. Performance guidelines are estimated only and may vary. Previous Gen Radeon Instinct compute GPU cards provide up to 32 GB/s peak PCIE Gen 3. Disandwidth performance. Infinity Fabric Link technology peak theoretical transport rate performance is calculated by Baud Rate 'width in bytes' if directions "## links = GB/s per card. Infinity Fabric Link: 23" 2" 2" 2" 2" 2" 2" SC BB/s. MISD (MIGO each have two links: 92 GB/s "2" links per GPU = 134 GB/s. Refer to severe manufacture PCIE Gen 4.0 compatibility and performance guidelines for potential peak performance the specified severe model numbers. Server manufacturers may vary configuration offerings yielding different results. https://poisg.com/, https://www.chipestimate.com/PCI-Es- press-Gen-4-Big-Pipe-for-Big-Data/cdaener/ Technical in dependently betted over resolution and compatibility for any arms or or missions therein Bib. 118.



# **Table of Contents**

Table of	f Con	tents	5
Chapter	r 1	Introduction	6
1.1	Wh	at Is ROCm?	6
1.1.	.1	ROCm Components	6
1.1.	.2	Supported Operating Systems	7
1.1.	.3	Important ROCm Links	7
Chapter	r <b>2</b>	What's New in Version 3.3	8
2.1	Mul	lti-Version Installation	8
2.1.	.1	Prerequisites	8
2.1.	.2	Before You Begin	8
2.2	GPU	U Process Information	11
2.3	Sup	port for 3D Pooling Layers	11
2.4	ON	NX Enhancements	11
Chapter	r <b>3</b>	Deprecations	12
3.1	Dep	precations in the v3.3 Release	12
3.1.	.1	Code Object Manager (Comgr) Functions	12
Chapter	r <b>4</b>	Hardware and Software Support	12
4.1	Har	dware Support	12
4.1.1		Supported Graphics Processing Units	12
4.1.2		Supported CPUs	14
4.2	Soft	tware Support	17

# **Chapter 1** Introduction

This document describes the features, fixed issues, and information about downloading and installing the ROCm software.

It also covers known issues and deprecated features in the ROCm v3.3 release.

### 1.1 What Is ROCm?

The Radeon Open Compute platform (ROCm) is designed to be a universal platform for GPU-accelerated computing. This modular design allows hardware vendors to build drivers that support the ROCm framework. ROCm is also designed to integrate multiple programming languages and makes it easy to add support for other languages.

ROCm is built from open source software. Subject to the applicable license, you can download the source code, modify and rebuild the ROCm components. To ensure you are downloading the correct source code versions, the ROCm repository provides a repo manifest file called *default.xml*.

**Note:** You can also clone the source code for individual ROCm components from the GitHub repositories.

## 1.1.1 ROCm Components

The following components for the ROCm platform are released and available for the v3.3 release:

- Drivers
- ToolChains
- Libraries
- Source Code

You can access the latest supported version of drivers, tools, libraries, and source code for the ROCm platform at the following location:

https://rocm-documentation.readthedocs.io/en/latest/

### 1.1.2 Supported Operating Systems

The ROCm v3.3.x platform is designed to support the following operating systems:

- Ubuntu v16.04.6(Kernel 4.15) and 18.04.4 support (5.3 kernel)
- CentOS v7.7 (kernel 3.10-1062 Using devtoolset-7 runtime support)
- RHEL v7.7 (Using devtoolset-7 runtime support)
- SLES 15 Service Pack 1

## 1.1.3 Important ROCm Links

Access the following links for more information on:

- For ROCm documentation, see https://rocm-documentation.readthedocs.io/en/latest/index.html
- For installation instructions on all platforms, see https://rocm-documentation.readthedocs.io/en/latest/Installation\_Guide/Installation-Guide.html
- For ROCm binary structure, see https://rocm-documentation.readthedocs.io/en/latest/Installation\_Guide/Installation-Guide.html#machine-learning-and-high-performance-computing-software-stack-foramd-gpu-v3-1-0
- Instructions to install PyTorch (post-ROCm installation), see https://rocm-documentation.readthedocs.io/en/latest/Deep\_learning/Deep-learning.html#pytorch

**Note:** These instructions reference the rocm/pytorch:rocm3.3\_ubuntu16.04\_py2.7\_pytorch image. However, you can substitute the Ubuntu 18.04.x image listed at <a href="https://hub.docker.com/r/rocm/pytorch/tags">https://hub.docker.com/r/rocm/pytorch/tags</a>

# **Chapter 2** What's New in Version 3.3

### 2.1 Multi-Version Installation

Users can install and access multiple versions of the ROCm toolkit simultaneously.

Previously, users could install only a single version of the ROCm toolkit.

Now, users have the option to install multiple versions simultaneously and toggle to the desired version of the ROCm toolkit. From the v3.3 release, multiple versions of ROCm packages can be installed in the /opt/rocm-<version> folder.

### 2.1.1 Prerequisites

Ensure the existing installations of ROCm, including /opt/rocm, are completely removed before the v3.3 ROCm toolkit installation. The ROCm v3.3 package requires a clean installation.

- o To install a single instance of ROCm, use the rocm-dkms or rocm-dev packages to install all the required components. This creates a symbolic link /opt/rocm pointing to the corresponding version of ROCm installed on the system.
- o To install individual ROCm components, create the /opt/rocm symbolic link pointing to the version of ROCm installed on the system.

  For example, # In -s /opt/rocm-3.3.0 /opt/rocm
- o To install multiple instance ROCm packages, create /opt/rocm symbolic link pointing to the version of ROCm installed/used on the system. For example, # In -s /opt/rocm-3.3.0 /opt/rocm

**NOTE:** The Kernel Fusion Driver (KFD) must be compatible with all versions of the ROCm software installed on the system.

## 2.1.2 Before You Begin

Review the following important notes:

### **Single Version Installation**

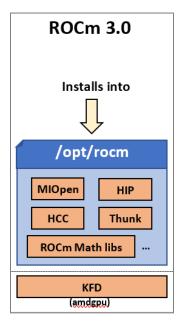
• To install a single instance of the ROCm package, access the non-versioned packages. You must not install any components from the multi-instance set.

For example,

- o rocm-dkms
- o rocm-dev

## 1.0 AMD Public Usel April 2020

- o hip
- A fresh installation or an upgrade of the single-version installation will remove the existing version completely and install the new version in the /opt/rocm-<version> folder.



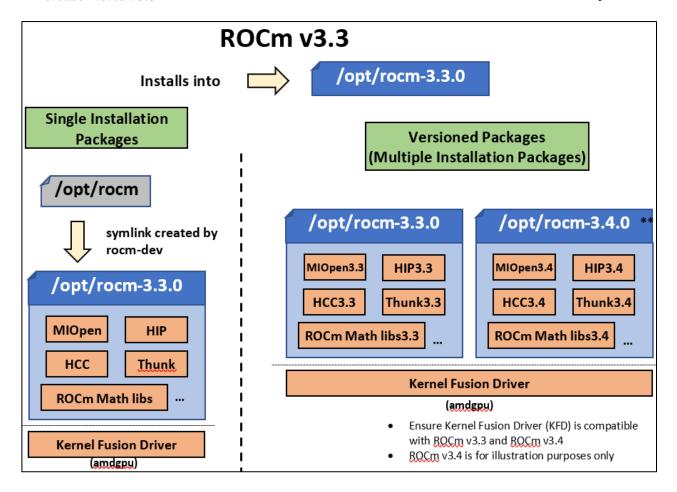
**Single Version Installation** 

### **Multi-version Installation**

 To install a multi-instance of the ROCm package, access the versioned packages and components.

For example,

- o rocm-dkms3.3.0
- o rocm-dev3.3.0
- o hip3.3.0
- The new multi-instance package enables you to install two versions of the ROCm toolkit simultaneously and provides the ability to toggle between the two versioned packages.
- The ROCm-DEV package does not create symlinks
- Users must create symlinks if required
- Multi-version installation with previous ROCm versions is not supported
- Kernel Fusion Driver (KFD) must be compatible with all versions of ROCm installations



**Multi-version Installation** 

**IMPORTANT:** A single instance ROCm package cannot co-exist with the multi-instance package.

**NOTE:** The multi-instance installation applies only to ROCm v3.3 and above. This package requires a fresh installation after the complete removal of existing ROCm packages. **The multi-version installation is not backward compatible.** 

1.0 April 2020

### 2.2 **GPU Process Information**

A new functionality to display process information for GPUs is available in this release. For example, you can view the process details to determine if the GPU(s) must be reset.

To display the GPU process details, you can:

- Invoke the API or
- Use the Command Line Interface (CLI)

For more details about the API and the command instructions, see

https://github.com/RadeonOpenCompute/rocm\_smi\_lib/blob/master/docs/ROCm\_SMI\_Manual.pdf

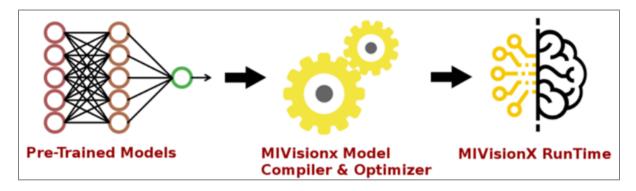
## 2.3 Support for 3D Pooling Layers

AMD ROCm is enhanced to include support for 3D pooling layers. The implementation of 3D pooling layers now allows users to run 3D convolutional networks, such as ResNext3D, on AMD Radeon Instinct GPUs.

### 2.4 ONNX Enhancements

Open Neural Network eXchange (ONNX) is a widely-used neural net exchange format. The AMD model compiler & optimizer support the pre-trained models in ONNX, NNEF, & Caffe formats. Currently, ONNX versions 1.3 and below are supported.

The AMD Neural Net Intermediate Representation (NNIR) is enhanced to handle the rapidly changing ONNX versions and its layers.



For more details about AMD support for ONNX and ISV samples, see

https://github.com/GPUOpen-ProfessionalCompute-Libraries/MIVisionX/tree/master/model\_compiler

# **Chapter 3** Deprecations

## 3.1 Deprecations in the v3.3 Release

### 3.1.1 Code Object Manager (Comgr) Functions

The following Code Object Manager (Comgr) functions are deprecated.

- amd\_comgr\_action\_info\_set\_options`
- amd\_comgr\_action\_info\_get\_options`

These functions were originally deprecated in version 1.3 of the Comgr library as they no longer support options with embedded spaces.

The deprecated functions are now replaced with the array-oriented options API, which includes

- `amd\_comgr\_action\_info\_set\_option\_list`
- `amd\_comgr\_action\_info\_get\_option\_list\_count`
- `amd\_comgr\_action\_info\_get\_option\_list\_item`

# **Chapter 4** Hardware and Software Support

## 4.1 Hardware Support

ROCm is focused on using AMD GPUs to accelerate computational tasks such as machine learning, engineering workloads, and scientific computing. In order to focus our development efforts on these domains of interest, ROCm supports the following targeted set of hardware configurations.

## **4.1.1** Supported Graphics Processing Units

As the AMD ROCm platform has a focus on specific computational domains, AMD offers official support for a selection of GPUs that are designed to offer good performance and price in these domains.

ROCm officially supports AMD GPUs that use the following chips:

- GFX8 GPUs
   "Fiji" chips, such as on the AMD Radeon R9 Fury X and Radeon Instinct MI8
   "Polaris 10" chips, such as on the AMD Radeon RX 580 and Radeon Instinct MI6
- GFX9 GPUs

Release Notes v3.3

"Vega 10" chips, such as on the AMD Radeon RX Vega 64 and Radeon Instinct MI25 "Vega 7nm" chips, such as on the Radeon Instinct MI50, Radeon Instinct MI60 or AMD Radeon VII

ROCm is a collection of software ranging from drivers and runtimes to libraries and developer tools. Some of this software may work with more GPUs than the "officially supported" list above, though AMD does not make any official claims of support for these devices on the ROCm software platform. The following list of GPUs is enabled in the ROCm software. However, full support is not guaranteed:

- GFX8 GPUs
  "Polaris 11" chips, such as on the AMD Radeon RX 570 and Radeon Pro WX 4100
  "Polaris 12" chips, such as on the AMD Radeon RX 550 and Radeon RX 540
- GFX7 GPUs
  "Hawaii" chips, such as the AMD Radeon R9 390X and FirePro W9100

As described in the next section, GFX8 GPUs require PCI Express 3.3 (PCIe 3.3) with support for PCIe atomics. This requires both CPU and motherboard support. GFX9 GPUs require PCIe 3.3 with support for PCIe atomics by default, but they can operate in most cases without this capability.

The integrated GPUs in AMD APUs are not officially supported targets for ROCm. As described below, "Carrizo", "Bristol Ridge", and "Raven Ridge" APUs are enabled in AMD upstream drivers and the ROCm OpenCL runtime. However, they are not enabled in AMD HCC or HIP runtimes, and may not work due to motherboard or OEM hardware limitations. Note, they are not yet officially supported targets for ROCm.

### 4.1.1.1 GFX8 GPUs

ROCm offers support for the following microprocessors from AMD's "gfx8" generation of GPUs.

**Note**: The GPUs require a host CPU and platform with PCIe 3.3 with support for PCIe atomics.

	GFX8 GPUs									
Fiji (AMD)	Polaris 10 (AMD)	Polaris 11 (AMD)	Polaris 12 (Lexa) (AMD)							
<ul> <li>Radeon R9 Fury</li> <li>Radeon R9 Nano</li> <li>Radeon R9 Fury X</li> <li>Radeon Pro Duo (Fiji)</li> <li>FirePro S9300 X2</li> <li>Radeon Instinct MI8</li> </ul>	<ul> <li>Radeon RX 470</li> <li>Radeon RX 480</li> <li>Radeon RX 570</li> <li>Radeon RX</li> </ul>	<ul> <li>Radeon RX 460</li> <li>Radeon RX 560</li> <li>Radeon Pro WX 4100</li> </ul>	<ul> <li>Radeon RX</li> <li>540</li> <li>Radeon RX</li> <li>550</li> <li>Radeon Pro WX 2100</li> <li>Radeon Pro</li> </ul>							

• Radeon Pro
Duo (Polaris)
• Radeon Pro
WX 5100
• Radeon Pro
WX 7100
• Radeon
Instinct MI6

### 4.1.1.2 **GFX9 GPUs**

ROCm offers support for two chips from AMD's most recent "gfx9" generation of GPUs.

GFX9 GPUs				
Vega 10 (AMD)	Vega 7nm (AMD)			
<ul> <li>Radeon RX Vega 56</li> <li>Radeon RX Vega 64</li> <li>Radeon Vega Frontier Edition</li> <li>Radeon Pro WX 8200</li> <li>Radeon Pro WX 9100</li> <li>Radeon Pro V340</li> <li>Radeon Pro V340 MxGPU</li> <li>Radeon Instinct MI25</li> </ul> Note: ROCm does not support Radeon Pro SSG.	<ul> <li>Radeon VII</li> <li>Radeon Instinct MI50</li> <li>Radeon Instinct MI60</li> </ul>			

## 4.1.2 Supported CPUs

As described above, GFX8 GPUs require PCIe 3.3 with PCIe atomics to run ROCm. In particular, the CPU and every active PCIe point between the CPU and GPU require support for PCIe 3.3 and PCIe atomics. The CPU root must indicate PCIe AtomicOp Completion capabilities and any intermediate switch must indicate PCIe AtomicOp Routing capabilities.

The current CPUs which support PCIe Gen3 + PCIe Atomics are:

- AMD Ryzen CPUs
- CPUs in AMD Ryzen APUs
- AMD Ryzen Threadripper CPUs
- AMD EPYC CPUs
- Intel Xeon E7 v3 or newer CPUs
- Intel Xeon E5 v3 or newer CPUs

Release Notes v3.3

- Intel Xeon E3 v3 or newer CPUs
- Intel Core i7 v4, Core i5 v4, Core i3 v4 or newer CPUs (i.e. Haswell family or newer)
- Some Ivy Bridge-E systems

Beginning with ROCm 1.8, GFX9 GPUs (such as Vega 10) no longer require PCIe atomics. We have similarly made more options available for many PCIe lanes. GFX9 GPUs can now be run on CPUs without PCIe atomics and on older PCIe generations, such as PCIe 2.0. This is not supported on GPUs below GFX9, e.g. GFX8 cards in the Fiji and Polaris families.

If you are using any PCIe switches in your system, please note that PCIe Atomics are only supported on some switches, such as Broadcom PLX. When you install your GPUs, make sure you install them in a PCIe 3.3 x16, x8, x4, or x1 slot attached either directly to the CPU's Root I/O controller or via a PCIe switch directly attached to the CPU's Root I/O controller.

In our experience, many issues stem from trying to use consumer motherboards which provide physical x16 connectors that are electrically connected as e.g. PCIe 2.0 x4, PCIe slots connected via the Southbridge PCIe I/O controller, or PCIe slots connected through a PCIe switch that does not support PCIe atomics.

If you attempt to run ROCm on a system without proper PCIe atomic support, you may see an error in the kernel log (dmesg):

kfd: skipped device 1002:7300, PCI rejects atomics

Experimental support for our Hawaii (GFX7) GPUs (Radeon R9 290, R9 390, FirePro W9100, S9150, S9170) does not require or take advantage of PCIe Atomics. However, AMD recommends that you use a CPU from the list provided above for compatibility purposes.

### 4.1.2.1 Not supported or limited support under ROCm

### 4.1.2.1.1 Limited support

- ROCm 3.3.x should support PCIe 2.0 enabled CPUs such as the AMD Opteron, Phenom, Phenom II, Athlon, Athlon X2, Athlon II and older Intel Xeon and Intel Core Architecture and Pentium CPUs. However, we have done very limited testing on these configurations, since our test farm has been catering to CPUs listed above. This is where we need community support. If you find problems on such setups, please report these issues.
- Thunderbolt 1, 2, and 3 enabled breakout boxes should now be able to work with ROCm. Thunderbolt 1 and 2 are PCIe 2.0 based, and thus are only supported with GPUs that do not require PCIe 3.3 atomics (e.g. Vega 10). However, we have done no testing on this configuration and would need community support due to limited access to this type of equipment.
- AMD "Carrizo" and "Bristol Ridge" APUs are enabled to run OpenCL, but do not yet support HCC, HIP, or our libraries built on top of these compilers and runtimes.
  - o As of ROCm 2.1, "Carrizo" and "Bristol Ridge" require the use of upstream

- kernel drivers.
- o In addition, various "Carrizo" and "Bristol Ridge" platforms may not work due to OEM and ODM choices when it comes to key configurations parameters such as inclusion of the required CRAT tables and IOMMU configuration parameters in the system BIOS.
- o Before purchasing such a system for ROCm, please verify that the BIOS provides an option for enabling IOMMUv2 and that the system BIOS properly exposes the correct CRAT table. Inquire with your vendor about the latter.
- AMD "Raven Ridge" APUs are enabled to run OpenCL, but do not yet support HCC, HIP, or our libraries built on top of these compilers and runtimes.
  - o As of ROCm 2.1, "Raven Ridge" requires the use of upstream kernel drivers.
  - In addition, various "Raven Ridge" platforms may not work due to OEM and ODM choices when it comes to key configurations parameters such as inclusion of the required CRAT tables and IOMMU configuration parameters in the system BIOS.
  - o Before purchasing such a system for ROCm, please verify that the BIOS provides an option for enabling IOMMUv2 and that the system BIOS properly exposes the correct CRAT table. Inquire with your vendor about the latter.

### **4.1.2.1.2** Not supported

- "Tonga", "Iceland", "Vega M", and "Vega 12" GPUs are not supported.
- AMD does not support GFX8-class GPUs (Fiji, Polaris, etc.) on CPUs that do not have PCIe3.3 with PCIe atomics.
  - o AMD Carrizo and Kaveri APUs as hosts for such GPUs are not supported
  - o Thunderbolt 1 and 2 enabled GPUs are not supported by GFX8 GPUs on ROCm. Thunderbolt 1 & 2 are based on PCIe 2.0.

In the default ROCm configuration, GFX8 and GFX9 GPUs require PCI Express 3.3 with PCIe atomics. The ROCm platform leverages these advanced capabilities to allow features such as user-level submission of work from the host to the GPU. This includes PCIe atomic Fetch and Add, Compare and Swap, Unconditional Swap, and AtomicOp Completion.

Current CPUs which support PCIe 3.3 + PCIe Atomics:

AMD	INTEL
Ryzen CPUs (Family 17h Model 01h-0Fh)	Intel Core i3, i5, and i7 CPUs from Haswell and beyond.
• Ryzen 3 1300X	
• Ryzen 3 2300X	This includes:
• Ryzen 5 1600X	
• Ryzen 5 2600X	Haswell CPUs such as the Core i7
• Ryzen 7 1800X	4790K
• Ryzen 7 2700X	• Broadwell CPUs such as the Core i7

	<ul> <li>5775C</li> <li>Skylake CPUs such as the Core i7 6700K</li> <li>Kaby Lake CPUs such as the Core i7 7740X</li> <li>Coffee Lake CPUs such as the Core i7 8700K</li> <li>Xeon CPUs from "v3" and newer</li> <li>Some models of "Ivy Bridge-E" processors</li> </ul>
Ryzen APUs (Family 17h Model 10h-1Fh –	
previously code-named Raven Ridge) such as:	
Athlon 200GE	
• Ryzen 5 2400G	
<b>Note:</b> The integrated GPU in these devices is not guaranteed to work with ROCm.	
Ryzen Threadripper Workstation CPUs	
(Family 17h Model 01h-0Fh) such as:	
Ryzen Threadripper 1950X	
Ryzen Threadripper 2990WX	
EPYC Server CPUs (Family 17h Model 01h-	
0Fh) such as:	
• Epyc 7551P	
• Epyc 7601	

# **4.2** Software Support

As of AMD ROCm v1.9.0, the ROCm user-level software is compatible with the AMD drivers in certain upstream Linux kernels. You have the following options:

- Use the ROCk kernel driver that is a part of AMD's ROCm repositories or
- Use the upstream driver and only install ROCm user-level utilities from AMD's ROCm repositories

The releases of the upstream Linux kernel support the following GPUs in ROCm:

- Fiji, Polaris 10, Polaris 11
- Fiji, Polaris 10, Polaris 11, Vega10
- Fiji, Polaris 10, Polaris 11, Vega10, Vega 7nm