

## HIP API

Generated by Doxygen 1.8.20



<b>1 Module Index</b>	<b>1</b>
1.1 Modules	1
<b>2 Hierarchical Index</b>	<b>3</b>
2.1 Class Hierarchy	3
<b>3 Class Index</b>	<b>7</b>
3.1 Class List	7
<b>4 Module Documentation</b>	<b>11</b>
4.1 Global enum and defines	11
4.1.1 Detailed Description	14
4.1.2 Macro Definition Documentation	14
4.1.2.1 hipDeviceScheduleSpin	15
4.1.2.2 hipDeviceScheduleYield	15
4.1.2.3 hipEventDefault	15
4.1.2.4 hipEventInterprocess	15
4.1.2.5 hipEventReleaseToSystem	15
4.1.2.6 hipHostMallocCoherent	15
4.1.2.7 hipHostMallocDefault	16
4.1.2.8 hipHostMallocMapped	16
4.1.2.9 hipHostMallocNonCoherent	16
4.1.2.10 hipHostRegisterDefault	16
4.1.2.11 hipHostRegisterMapped	16
4.1.2.12 hipMallocSignalMemory	16
4.1.2.13 hipMemAttachSingle	17
4.1.2.14 hipStreamDefault	17
4.1.2.15 hipStreamNonBlocking	17
4.1.3 Typedef Documentation	17
4.1.3.1 dim3	17
4.1.3.2 hipExternalSemaphoreWaitParams	17
4.1.3.3 hipFuncAttribute	17
4.1.3.4 hipFuncCache_t	18
4.1.3.5 hipSharedMemConfig	18
4.1.4 Enumeration Type Documentation	18
4.1.4.1 hipDeviceAttribute_t	18
4.1.4.2 hipFuncAttribute	20
4.1.4.3 hipFuncCache_t	20
4.1.4.4 hipGLDeviceList	20
4.1.4.5 hipGraphicsRegisterFlags	21
4.1.4.6 hipMemoryAdvise	21
4.1.4.7 hipMemRangeAttribute	21
4.1.4.8 hipMemRangeCoherencyMode	22

4.1.4.9 hipSharedMemConfig . . . . .	22
4.2 HIP API . . . . .	23
4.2.1 Detailed Description . . . . .	23
4.3 Initialization and Version . . . . .	24
4.3.1 Detailed Description . . . . .	24
4.3.2 Function Documentation . . . . .	24
4.3.2.1 hipDeviceComputeCapability() . . . . .	24
4.3.2.2 hipDeviceGet() . . . . .	25
4.3.2.3 hipDeviceGetByPCIBusId() . . . . .	25
4.3.2.4 hipDeviceGetName() . . . . .	25
4.3.2.5 hipDeviceGetP2PAttribute() . . . . .	27
4.3.2.6 hipDeviceGetPCIBusId() . . . . .	27
4.3.2.7 hipDeviceTotalMem() . . . . .	28
4.3.2.8 hipDriverGetVersion() . . . . .	28
4.3.2.9 hipInit() . . . . .	29
4.3.2.10 hipRuntimeGetVersion() . . . . .	29
4.4 Device Management . . . . .	30
4.4.1 Detailed Description . . . . .	30
4.4.2 Function Documentation . . . . .	31
4.4.2.1 hipChooseDevice() . . . . .	31
4.4.2.2 hipDeviceGetAttribute() . . . . .	31
4.4.2.3 hipDeviceGetCacheConfig() . . . . .	31
4.4.2.4 hipDeviceGetLimit() . . . . .	33
4.4.2.5 hipDeviceGetSharedMemConfig() . . . . .	33
4.4.2.6 hipDeviceReset() . . . . .	34
4.4.2.7 hipDeviceSetCacheConfig() . . . . .	34
4.4.2.8 hipDeviceSetSharedMemConfig() . . . . .	34
4.4.2.9 hipDeviceSynchronize() . . . . .	35
4.4.2.10 hipExtGetLinkTypeAndHopCount() . . . . .	35
4.4.2.11 hipGetDevice() . . . . .	36
4.4.2.12 hipGetDeviceCount() . . . . .	36
4.4.2.13 hipGetDeviceFlags() . . . . .	36
4.4.2.14 hipGetDeviceProperties() . . . . .	37
4.4.2.15 hipIpcCloseMemHandle() . . . . .	37
4.4.2.16 hipIpcGetEventHandle() . . . . .	38
4.4.2.17 hipIpcGetMemHandle() . . . . .	38
4.4.2.18 hipIpcOpenEventHandle() . . . . .	39
4.4.2.19 hipIpcOpenMemHandle() . . . . .	39
4.4.2.20 hipSetDevice() . . . . .	40
4.4.2.21 hipSetDeviceFlags() . . . . .	41
4.5 Execution Control . . . . .	42
4.5.1 Detailed Description . . . . .	42

4.5.2 Function Documentation	42
4.5.2.1 hipFuncSetAttribute()	42
4.5.2.2 hipFuncSetCacheConfig()	42
4.5.2.3 hipFuncSetSharedMemConfig()	43
4.6 Error Handling	44
4.6.1 Detailed Description	44
4.6.2 Function Documentation	44
4.6.2.1 hipGetErrorName()	44
4.6.2.2 hipGetErrorString()	44
4.6.2.3 hipGetLastError()	45
4.6.2.4 hipPeekAtLastError()	45
4.7 Stream Management	46
4.7.1 Detailed Description	46
4.7.2 Typedef Documentation	47
4.7.2.1 hipStreamCallback_t	47
4.7.3 Function Documentation	47
4.7.3.1 hipDeviceGetStreamPriorityRange()	47
4.7.3.2 hipExtStreamCreateWithCUMask()	47
4.7.3.3 hipExtStreamGetCUMask()	48
4.7.3.4 hipStreamAddCallback()	48
4.7.3.5 hipStreamCreate()	49
4.7.3.6 hipStreamCreateWithFlags()	49
4.7.3.7 hipStreamCreateWithPriority()	49
4.7.3.8 hipStreamDestroy()	50
4.7.3.9 hipStreamGetFlags()	50
4.7.3.10 hipStreamGetPriority()	51
4.7.3.11 hipStreamQuery()	51
4.7.3.12 hipStreamSynchronize()	52
4.7.3.13 hipStreamWaitEvent()	52
4.7.3.14 hipStreamWaitValue32()	53
4.7.3.15 hipStreamWaitValue64()	53
4.7.3.16 hipStreamWriteValue32()	54
4.7.3.17 hipStreamWriteValue64()	55
4.8 Event Management	56
4.8.1 Detailed Description	56
4.8.2 Function Documentation	56
4.8.2.1 hipEventCreate()	56
4.8.2.2 hipEventCreateWithFlags()	56
4.8.2.3 hipEventDestroy()	57
4.8.2.4 hipEventElapsedTime()	57
4.8.2.5 hipEventQuery()	58
4.8.2.6 hipEventRecord()	59

---

4.8.2.7 hipEventSynchronize()	59
4.9 Memory Management	60
4.9.1 Detailed Description	63
4.9.2 Function Documentation	64
4.9.2.1 hipDestroyExternalMemory()	64
4.9.2.2 hipDestroyExternalSemaphore()	64
4.9.2.3 hipDrvMemcpy3D()	64
4.9.2.4 hipDrvMemcpy3DAsync()	65
4.9.2.5 hipExternalMemoryGetMappedBuffer()	65
4.9.2.6 hipExtMallocWithFlags()	65
4.9.2.7 hipFree()	66
4.9.2.8 hipFreeArray()	66
4.9.2.9 hipFreeHost()	67
4.9.2.10 hipFreeMipmappedArray()	67
4.9.2.11 hipGetMipmappedArrayLevel()	67
4.9.2.12 hipHostAlloc()	68
4.9.2.13 hipHostFree()	68
4.9.2.14 hipHostGetDevicePointer()	68
4.9.2.15 hipHostGetFlags()	69
4.9.2.16 hipHostMalloc()	69
4.9.2.17 hipHostRegister()	70
4.9.2.18 hipHostUnregister()	70
4.9.2.19 hipImportExternalMemory()	71
4.9.2.20 hipImportExternalSemaphore()	71
4.9.2.21 hipMalloc()	71
4.9.2.22 hipMalloc3DArray()	72
4.9.2.23 hipMallocArray()	72
4.9.2.24 hipMallocHost()	73
4.9.2.25 hipMallocManaged()	73
4.9.2.26 hipMallocMipmappedArray()	73
4.9.2.27 hipMallocPitch()	74
4.9.2.28 hipMemAdvise()	74
4.9.2.29 hipMemAllocHost()	75
4.9.2.30 hipMemAllocPitch()	75
4.9.2.31 hipMemcpy()	76
4.9.2.32 hipMemcpy2D()	76
4.9.2.33 hipMemcpy2DAsync()	77
4.9.2.34 hipMemcpy2DFromArray()	78
4.9.2.35 hipMemcpy2DFromArrayAsync()	78
4.9.2.36 hipMemcpy2DToArray()	79
4.9.2.37 hipMemcpy2DToArrayAsync()	79
4.9.2.38 hipMemcpy3D()	80

---

4.9.2.39 hipMemcpy3DAsync()	80
4.9.2.40 hipMemcpyAsync()	82
4.9.2.41 hipMemcpyAtoH()	83
4.9.2.42 hipMemcpyDtoD()	83
4.9.2.43 hipMemcpyDtoDAsync()	83
4.9.2.44 hipMemcpyDtoH()	84
4.9.2.45 hipMemcpyDtoHAsync()	84
4.9.2.46 hipMemcpyFromArray()	85
4.9.2.47 hipMemcpyHtoA()	86
4.9.2.48 hipMemcpyHtoD()	86
4.9.2.49 hipMemcpyHtoDAsync()	86
4.9.2.50 hipMemcpyParam2D()	87
4.9.2.51 hipMemcpyParam2DAsync()	87
4.9.2.52 hipMemcpyToArray()	88
4.9.2.53 hipMemGetInfo()	88
4.9.2.54 hipMemPrefetchAsync()	89
4.9.2.55 hipMemRangeGetAttribute()	89
4.9.2.56 hipMemRangeGetAttributes()	89
4.9.2.57 hipMemset()	90
4.9.2.58 hipMemset2D()	90
4.9.2.59 hipMemset2DAsync()	91
4.9.2.60 hipMemset3D()	91
4.9.2.61 hipMemset3DAsync()	91
4.9.2.62 hipMemsetAsync()	93
4.9.2.63 hipMemsetD16()	93
4.9.2.64 hipMemsetD16Async()	94
4.9.2.65 hipMemsetD32()	94
4.9.2.66 hipMemsetD32Async()	94
4.9.2.67 hipMemsetD8()	95
4.9.2.68 hipMemsetD8Async()	95
4.9.2.69 hipModuleGetGlobal()	96
4.9.2.70 hipPointerGetAttributes()	96
4.9.2.71 hipSignalExternalSemaphoresAsync()	96
4.9.2.72 hipStreamAttachMemAsync()	97
4.9.2.73 hipWaitExternalSemaphoresAsync()	97
4.10 PeerToPeer Device Memory Access	99
4.10.1 Detailed Description	99
4.10.2 Function Documentation	99
4.10.2.1 hipDeviceCanAccessPeer()	99
4.10.2.2 hipDeviceDisablePeerAccess()	100
4.10.2.3 hipDeviceEnablePeerAccess()	100
4.10.2.4 hipMemcpyPeer()	100

4.10.2.5 hipMemcpyPeerAsync()	101
4.10.2.6 hipMemGetAddressRange()	101
4.11 Context Management	103
4.11.1 Detailed Description	103
4.11.2 Function Documentation	103
4.11.2.1 hipDevicePrimaryCtxGetState()	103
4.11.2.2 hipDevicePrimaryCtxRelease()	103
4.11.2.3 hipDevicePrimaryCtxReset()	104
4.11.2.4 hipDevicePrimaryCtxRetain()	104
4.11.2.5 hipDevicePrimaryCtxSetFlags()	105
4.12 Module Management	106
4.12.1 Detailed Description	106
4.12.2 Function Documentation	107
4.12.2.1 hipExtLaunchMultiKernelMultiDevice()	107
4.12.2.2 hipExtModuleLaunchKernel()	107
4.12.2.3 hipFuncGetAttribute()	108
4.12.2.4 hipFuncGetAttributes()	108
4.12.2.5 hipLaunchCooperativeKernel()	109
4.12.2.6 hipLaunchCooperativeKernelMultiDevice()	109
4.12.2.7 hipModuleGetFunction()	110
4.12.2.8 hipModuleGetTexRef()	110
4.12.2.9 hipModuleLaunchKernel()	110
4.12.2.10 hipModuleLoad()	111
4.12.2.11 hipModuleLoadData()	111
4.12.2.12 hipModuleLoadDataEx()	112
4.12.2.13 hipModuleUnload()	112
4.13 Occupancy	113
4.13.1 Detailed Description	113
4.13.2 Function Documentation	113
4.13.2.1 hipModuleOccupancyMaxActiveBlocksPerMultiprocessor()	113
4.13.2.2 hipModuleOccupancyMaxActiveBlocksPerMultiprocessorWithFlags()	113
4.13.2.3 hipModuleOccupancyMaxPotentialBlockSize()	114
4.13.2.4 hipModuleOccupancyMaxPotentialBlockSizeWithFlags()	114
4.13.2.5 hipOccupancyMaxActiveBlocksPerMultiprocessor()	115
4.13.2.6 hipOccupancyMaxActiveBlocksPerMultiprocessorWithFlags()	115
4.13.2.7 hipOccupancyMaxPotentialBlockSize()	115
4.14 Profiler Control[Deprecated]	117
4.14.1 Detailed Description	117
4.14.2 Function Documentation	117
4.14.2.1 hipProfilerStart()	117
4.14.2.2 hipProfilerStop()	117
4.15 Launch API to support the triple-chevron syntax	118



4.15.1 Detailed Description	118
4.15.2 Function Documentation	118
4.15.2.1 __hipPopCallConfiguration()	118
4.15.2.2 __hipPushCallConfiguration()	119
4.15.2.3 hipConfigureCall()	119
4.15.2.4 hipDrvMemcpy2DUnaligned()	119
4.15.2.5 hipLaunchByPtr()	121
4.15.2.6 hipLaunchKernel()	121
4.15.2.7 hipSetupArgument()	121
4.16 Texture Management	123
4.16.1 Detailed Description	123
4.17 Runtime Compilation	124
4.17.1 Detailed Description	125
4.17.2 Function Documentation	125
4.17.2.1 hiprtcAddNameExpression()	125
4.17.2.2 hiprtcCompileProgram()	125
4.17.2.3 hiprtcCreateProgram()	126
4.17.2.4 hiprtcDestroyProgram()	126
4.17.2.5 hiprtcGetCode()	126
4.17.2.6 hiprtcGetCodeSize()	127
4.17.2.7 hiprtcGetErrorString()	127
4.17.2.8 hiprtcGetLoweredName()	128
4.17.2.9 hiprtcGetProgramLog()	128
4.17.2.10 hiprtcGetProgramLogSize()	128
4.17.2.11 hiprtcVersion()	129
4.18 Callback Activity APIs	130
4.18.1 Detailed Description	130
4.19 Graph Management	131
4.19.1 Detailed Description	132
4.19.2 Typedef Documentation	132
4.19.2.1 hipGraph_t	133
4.19.2.2 hipGraphExec_t	133
4.19.2.3 hipGraphNode_t	133
4.19.3 Enumeration Type Documentation	133
4.19.3.1 hipGraphExecUpdateResult	133
4.19.3.2 hipGraphNodeType	133
4.19.3.3 hipStreamCaptureStatus	134
4.19.4 Function Documentation	134
4.19.4.1 hipGraphAddDependencies()	134
4.19.4.2 hipGraphAddEmptyNode()	134
4.19.4.3 hipGraphAddKernelNode()	135
4.19.4.4 hipGraphAddMemcpyNode()	135

4.19.4.5	hipGraphAddMemcpyNode1D()	136
4.19.4.6	hipGraphAddMemsetNode()	136
4.19.4.7	hipGraphCreate()	137
4.19.4.8	hipGraphDestroy()	137
4.19.4.9	hipGraphExecDestroy()	138
4.19.4.10	hipGraphExecKernelNodeSetParams()	138
4.19.4.11	hipGraphGetNodes()	138
4.19.4.12	hipGraphGetRootNodes()	139
4.19.4.13	hipGraphInstantiate()	139
4.19.4.14	hipGraphKernelNodeGetParams()	140
4.19.4.15	hipGraphKernelNodeSetParams()	140
4.19.4.16	hipGraphLaunch()	141
4.19.4.17	hipGraphMemcpyNodeGetParams()	141
4.19.4.18	hipGraphMemcpyNodeSetParams()	141
4.19.4.19	hipGraphMemsetNodeGetParams()	142
4.19.4.20	hipGraphMemsetNodeSetParams()	142
4.19.4.21	hipStreamBeginCapture()	143
4.19.4.22	hipStreamEndCapture()	143
4.20	Interop	144
4.20.1	Detailed Description	144
4.21	Context Management [Deprecated]	145
4.21.1	Detailed Description	145
4.21.2	Function Documentation	145
4.21.2.1	hipCtxCreate()	145
4.21.2.2	hipCtxDestroy()	146
4.21.2.3	hipCtxDisablePeerAccess()	146
4.21.2.4	hipCtxEnablePeerAccess()	147
4.21.2.5	hipCtxGetApiVersion()	147
4.21.2.6	hipCtxGetCacheConfig()	148
4.21.2.7	hipCtxGetCurrent()	148
4.21.2.8	hipCtxGetDevice()	149
4.21.2.9	hipCtxGetFlags()	149
4.21.2.10	hipCtxGetSharedMemConfig()	149
4.21.2.11	hipCtxPopCurrent()	150
4.21.2.12	hipCtxPushCurrent()	150
4.21.2.13	hipCtxSetCacheConfig()	150
4.21.2.14	hipCtxSetCurrent()	151
4.21.2.15	hipCtxSetSharedMemConfig()	151
4.21.2.16	hipCtxSynchronize()	152
4.22	Texture Management [Deprecated]	153
4.22.1	Detailed Description	153
4.23	Texture Management [Not supported]	154

4.23.1 Detailed Description	154
<b>5 Class Documentation</b>	<b>155</b>
5.1 __half2_raw Struct Reference	155
5.2 __half_raw Struct Reference	155
5.3 __hip_enable_if< __B, __T > Struct Template Reference	155
5.4 __hip_enable_if< true, __T > Struct Template Reference	155
5.5 char1 Union Reference	155
5.6 char16 Union Reference	155
5.7 char2 Union Reference	155
5.8 char3 Union Reference	156
5.9 char4 Union Reference	156
5.10 char8 Union Reference	156
5.11 dim3 Struct Reference	156
5.11.1 Detailed Description	156
5.12 double1 Union Reference	156
5.13 double16 Union Reference	156
5.14 double2 Union Reference	156
5.15 double3 Union Reference	157
5.16 double4 Union Reference	157
5.17 double8 Union Reference	157
5.18 float1 Union Reference	157
5.19 float16 Union Reference	157
5.20 float2 Union Reference	157
5.21 float3 Union Reference	157
5.22 float4 Union Reference	157
5.23 float8 Union Reference	157
5.24 gl_dim3 Struct Reference	158
5.25 grid_launch_parm Struct Reference	158
5.25.1 Member Data Documentation	158
5.25.1.1 av	158
5.25.1.2 barrier_bit	158
5.25.1.3 cf	158
5.25.1.4 dynamic_group_mem_bytes	159
5.25.1.5 launch_fence	159
5.26 grid_launch_parm_cxx Class Reference	159
5.27 hip_api_data_s Struct Reference	159
5.28 HIP_ARRAY3D_DESCRIPTOR Struct Reference	184
5.29 HIP_ARRAY_DESCRIPTOR Struct Reference	184
5.30 hip_bfloat16 Struct Reference	184
5.30.1 Detailed Description	184
5.31 hip_Memcpy2D Struct Reference	185

5.32 HIP_MEMCPY3D Struct Reference	185
5.33 HIP_RESOURCE_DESC_st Struct Reference	186
5.33.1 Member Data Documentation	186
5.33.1.1 devPtr	186
5.33.1.2 flags	186
5.33.1.3 format	186
5.33.1.4 hArray	186
5.33.1.5 height	187
5.33.1.6 hMipmappedArray	187
5.33.1.7 numChannels	187
5.33.1.8 pitchInBytes	187
5.33.1.9 resType	187
5.33.1.10 sizeInBytes	187
5.33.1.11 width	187
5.34 HIP_RESOURCE_VIEW_DESC_st Struct Reference	187
5.34.1 Detailed Description	187
5.34.2 Member Data Documentation	187
5.34.2.1 depth	188
5.34.2.2 firstLayer	188
5.34.2.3 firstMipmapLevel	188
5.34.2.4 format	188
5.34.2.5 height	188
5.34.2.6 lastLayer	188
5.34.2.7 lastMipmapLevel	188
5.34.2.8 width	188
5.35 HIP_TEXTURE_DESC_st Struct Reference	188
5.35.1 Detailed Description	188
5.35.2 Member Data Documentation	189
5.35.2.1 addressMode	189
5.35.2.2 borderColor	189
5.35.2.3 filterMode	189
5.35.2.4 flags	189
5.35.2.5 maxAnisotropy	189
5.35.2.6 maxMipmapLevelClamp	189
5.35.2.7 minMipmapLevelClamp	189
5.35.2.8 mipmapFilterMode	189
5.35.2.9 mipmapLevelBias	189
5.36 hipArray Struct Reference	189
5.37 hipChannelFormatDesc Struct Reference	190
5.38 hipDeviceArch_t Struct Reference	190
5.39 hipDeviceProp_t Struct Reference	191
5.39.1 Detailed Description	193

5.39.2 Member Data Documentation	193
5.39.2.1 clockInstructionRate	193
5.39.2.2 cooperativeMultiDeviceUnmatchedBlockDim	193
5.39.2.3 cooperativeMultiDeviceUnmatchedFunc	193
5.39.2.4 cooperativeMultiDeviceUnmatchedGridDim	193
5.39.2.5 cooperativeMultiDeviceUnmatchedSharedMem	193
5.39.2.6 major	193
5.39.2.7 minor	193
5.39.2.8 pageableMemoryAccess	193
5.40 hipExtent Struct Reference	193
5.41 hipExternalMemoryBufferDesc_st Struct Reference	194
5.42 hipExternalMemoryHandleDesc_st Struct Reference	194
5.43 hipExternalSemaphoreHandleDesc_st Struct Reference	194
5.44 hipExternalSemaphoreSignalParams_st Struct Reference	194
5.45 hipExternalSemaphoreWaitParams_st Struct Reference	195
5.45.1 Detailed Description	195
5.46 hipFuncAttributes Struct Reference	195
5.47 hipHostNodeParams Struct Reference	195
5.48 hipIpcEventHandle_st Struct Reference	195
5.49 hipIpcMemHandle_st Struct Reference	196
5.50 hipKernelNodeParams Struct Reference	196
5.51 hipLaunchParams_t Struct Reference	196
5.52 hipMemcpy3DParms Struct Reference	196
5.53 hipMemsetParams Struct Reference	196
5.54 hipMipmappedArray Struct Reference	197
5.55 hipPitchedPtr Struct Reference	197
5.56 hipPointerAttribute_t Struct Reference	197
5.56.1 Detailed Description	197
5.57 hipPos Struct Reference	197
5.58 hipResourceDesc Struct Reference	198
5.58.1 Detailed Description	198
5.59 hipResourceViewDesc Struct Reference	198
5.59.1 Detailed Description	198
5.60 hipTextureDesc Struct Reference	198
5.60.1 Detailed Description	199
5.61 int1 Union Reference	199
5.62 int16 Union Reference	199
5.63 int2 Union Reference	199
5.64 int3 Union Reference	199
5.65 int4 Union Reference	199
5.66 int8 Union Reference	199
5.67 hip_impl::is_callable< Call > Struct Template Reference	199

5.68 hip_impl::is_callable_impl< typename, typename > Struct Template Reference . . . . .	200
5.69 hip_impl::is_callable_impl< F(Ts...), void_t< decltype(simple_invoke(std::declval< F >()), std::declval< Ts >())...>> Struct Template Reference . . . . .	200
5.70 hip_impl::kernarg Class Reference . . . . .	200
5.71 hip_impl::kernargs_size_align Class Reference . . . . .	200
5.72 long1 Union Reference . . . . .	201
5.73 long16 Union Reference . . . . .	201
5.74 long2 Union Reference . . . . .	201
5.75 long3 Union Reference . . . . .	201
5.76 long4 Union Reference . . . . .	201
5.77 long8 Union Reference . . . . .	201
5.78 longlong1 Union Reference . . . . .	201
5.79 longlong16 Union Reference . . . . .	202
5.80 longlong2 Union Reference . . . . .	202
5.81 longlong3 Union Reference . . . . .	202
5.82 longlong4 Union Reference . . . . .	202
5.83 longlong8 Union Reference . . . . .	202
5.84 hip_impl::program_state Class Reference . . . . .	202
5.85 short1 Union Reference . . . . .	202
5.86 short16 Union Reference . . . . .	203
5.87 short2 Union Reference . . . . .	203
5.88 short3 Union Reference . . . . .	203
5.89 short4 Union Reference . . . . .	203
5.90 short8 Union Reference . . . . .	203
5.91 surfaceReference Struct Reference . . . . .	203
5.91.1 Detailed Description . . . . .	203
5.92 TData Union Reference . . . . .	203
5.93 textureReference Struct Reference . . . . .	203
5.93.1 Detailed Description . . . . .	204
5.94 uchar1 Union Reference . . . . .	204
5.95 uchar16 Union Reference . . . . .	204
5.96 uchar2 Union Reference . . . . .	204
5.97 uchar2Holder Struct Reference . . . . .	204
5.98 uchar3 Union Reference . . . . .	204
5.99 uchar4 Union Reference . . . . .	205
5.100 uchar8 Union Reference . . . . .	205
5.101 ucharHolder Struct Reference . . . . .	205
5.102 uint1 Union Reference . . . . .	205
5.103 uint16 Union Reference . . . . .	205
5.104 uint2 Union Reference . . . . .	205
5.105 uint3 Union Reference . . . . .	205
5.106 uint4 Union Reference . . . . .	205

---

5.107 uint8 Union Reference . . . . .	206
5.108 ulong1 Union Reference . . . . .	206
5.109 ulong16 Union Reference . . . . .	206
5.110 ulong2 Union Reference . . . . .	206
5.111 ulong3 Union Reference . . . . .	206
5.112 ulong4 Union Reference . . . . .	206
5.113 ulong8 Union Reference . . . . .	206
5.114 ulonglong1 Union Reference . . . . .	206
5.115 ulonglong16 Union Reference . . . . .	206
5.116 ulonglong2 Union Reference . . . . .	207
5.117 ulonglong3 Union Reference . . . . .	207
5.118 ulonglong4 Union Reference . . . . .	207
5.119 ulonglong8 Union Reference . . . . .	207
5.120 ushort1 Union Reference . . . . .	207
5.121 ushort16 Union Reference . . . . .	207
5.122 ushort2 Union Reference . . . . .	207
5.123 ushort3 Union Reference . . . . .	207
5.124 ushort4 Union Reference . . . . .	207
5.125 ushort8 Union Reference . . . . .	208





# Chapter 1

## Module Index

### 1.1 Modules

Here is a list of all modules:

Global enum and defines . . . . .	11
HIP API . . . . .	23
Initialization and Version . . . . .	24
Device Management . . . . .	30
Execution Control . . . . .	42
Error Handling . . . . .	44
Stream Management . . . . .	46
Event Management . . . . .	56
Memory Management . . . . .	60
PeerToPeer Device Memory Access . . . . .	99
Context Management . . . . .	103
Context Management [Deprecated] . . . . .	145
Module Management . . . . .	106
Occupancy . . . . .	113
Profiler Control[Deprecated] . . . . .	117
Launch API to support the triple-chevron syntax . . . . .	118
Texture Management . . . . .	123
Texture Management [Deprecated] . . . . .	153
Texture Management [Not supported] . . . . .	154
Runtime Compilation . . . . .	124
Callback Activity APIs . . . . .	130
Graph Management . . . . .	131
Interop . . . . .	144



## Chapter 2

# Hierarchical Index

### 2.1 Class Hierarchy

This inheritance list is sorted roughly, but not completely, alphabetically:

__half2_raw . . . . .	155
__half_raw . . . . .	155
__hip_enable_if< __B, __T > . . . . .	155
__hip_enable_if< true, __T > . . . . .	155
char1 . . . . .	155
char16 . . . . .	155
char2 . . . . .	155
char3 . . . . .	156
char4 . . . . .	156
char8 . . . . .	156
dim3 . . . . .	156
double1 . . . . .	156
double16 . . . . .	156
double2 . . . . .	156
double3 . . . . .	157
double4 . . . . .	157
double8 . . . . .	157
false_type	
hip_impl::is_callable_impl< typename, typename > . . . . .	200
hip_impl::is_callable_impl< Call > . . . . .	200
hip_impl::is_callable< Call > . . . . .	199
float1 . . . . .	157
float16 . . . . .	157
float2 . . . . .	157
float3 . . . . .	157
float4 . . . . .	157
float8 . . . . .	157
gl_dim3 . . . . .	158
grid_launch_parm . . . . .	158
grid_launch_parm_cxx . . . . .	159
hip_api_data_s . . . . .	159
HIP_ARRAY3D_DESCRIPTOR . . . . .	184
HIP_ARRAY_DESCRIPTOR . . . . .	184
hip_bfloat16 . . . . .	184
hip_Memcpy2D . . . . .	185

HIP_MEMCPY3D . . . . .	185
HIP_RESOURCE_DESC_st . . . . .	186
HIP_RESOURCE_VIEW_DESC_st . . . . .	187
HIP_TEXTURE_DESC_st . . . . .	188
hipArray . . . . .	189
hipChannelFormatDesc . . . . .	190
hipDeviceArch_t . . . . .	190
hipDeviceProp_t . . . . .	191
hipExtent . . . . .	193
hipExternalMemoryBufferDesc_st . . . . .	194
hipExternalMemoryHandleDesc_st . . . . .	194
hipExternalSemaphoreHandleDesc_st . . . . .	194
hipExternalSemaphoreSignalParams_st . . . . .	194
hipExternalSemaphoreWaitParams_st . . . . .	195
hipFuncAttributes . . . . .	195
hipHostNodeParams . . . . .	195
hipIpcEventHandle_st . . . . .	195
hipIpcMemHandle_st . . . . .	196
hipKernelNodeParams . . . . .	196
hipLaunchParams_t . . . . .	196
hipMemcpy3DParms . . . . .	196
hipMemsetParams . . . . .	196
hipMipmappedArray . . . . .	197
hipPitchedPtr . . . . .	197
hipPointerAttribute_t . . . . .	197
hipPos . . . . .	197
hipResourceDesc . . . . .	198
hipResourceViewDesc . . . . .	198
hipTextureDesc . . . . .	198
int1 . . . . .	199
int16 . . . . .	199
int2 . . . . .	199
int3 . . . . .	199
int4 . . . . .	199
int8 . . . . .	199
hip_impl::kernarg . . . . .	200
hip_impl::kernargs_size_align . . . . .	200
long1 . . . . .	201
long16 . . . . .	201
long2 . . . . .	201
long3 . . . . .	201
long4 . . . . .	201
long8 . . . . .	201
longlong1 . . . . .	201
longlong16 . . . . .	202
longlong2 . . . . .	202
longlong3 . . . . .	202
longlong4 . . . . .	202
longlong8 . . . . .	202
hip_impl::program_state . . . . .	202
short1 . . . . .	202
short16 . . . . .	203
short2 . . . . .	203
short3 . . . . .	203
short4 . . . . .	203
short8 . . . . .	203
surfaceReference . . . . .	203
TData . . . . .	203

textureReference . . . . .	203
true_type	
hip_impl::is_callable_impl< F(Ts...), void_t_< decltype(simple_invoke(std::declval< F >()), std::declval< Ts >())>> . . . . .	200
uchar1 . . . . .	204
uchar16 . . . . .	204
uchar2 . . . . .	204
uchar2Holder . . . . .	204
uchar3 . . . . .	204
uchar4 . . . . .	205
uchar8 . . . . .	205
ucharHolder . . . . .	205
uint1 . . . . .	205
uint16 . . . . .	205
uint2 . . . . .	205
uint3 . . . . .	205
uint4 . . . . .	205
uint8 . . . . .	206
ulong1 . . . . .	206
ulong16 . . . . .	206
ulong2 . . . . .	206
ulong3 . . . . .	206
ulong4 . . . . .	206
ulong8 . . . . .	206
ulonglong1 . . . . .	206
ulonglong16 . . . . .	206
ulonglong2 . . . . .	207
ulonglong3 . . . . .	207
ulonglong4 . . . . .	207
ulonglong8 . . . . .	207
ushort1 . . . . .	207
ushort16 . . . . .	207
ushort2 . . . . .	207
ushort3 . . . . .	207
ushort4 . . . . .	207
ushort8 . . . . .	208



## Chapter 3

# Class Index

### 3.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

<a href="#">__half2_raw</a>	155
<a href="#">__half_raw</a>	155
<a href="#">__hip_enable_if&lt; __B, __T &gt;</a>	155
<a href="#">__hip_enable_if&lt; true, __T &gt;</a>	155
<a href="#">char1</a>	155
<a href="#">char16</a>	155
<a href="#">char2</a>	155
<a href="#">char3</a>	156
<a href="#">char4</a>	156
<a href="#">char8</a>	156
<a href="#">dim3</a>	156
<a href="#">double1</a>	156
<a href="#">double16</a>	156
<a href="#">double2</a>	156
<a href="#">double3</a>	157
<a href="#">double4</a>	157
<a href="#">double8</a>	157
<a href="#">float1</a>	157
<a href="#">float16</a>	157
<a href="#">float2</a>	157
<a href="#">float3</a>	157
<a href="#">float4</a>	157
<a href="#">float8</a>	157
<a href="#">gl_dim3</a>	158
<a href="#">grid_launch_parm</a>	158
<a href="#">grid_launch_parm_cxx</a>	159
<a href="#">hip_api_data_s</a>	159
<a href="#">HIP_ARRAY3D_DESCRIPTOR</a>	184
<a href="#">HIP_ARRAY_DESCRIPTOR</a>	184
<a href="#">hip_bfloat16</a>	
Struct to represent a 16 bit brain floating point number	184
<a href="#">hip_Memcpy2D</a>	185
<a href="#">HIP_MEMCPY3D</a>	185
<a href="#">HIP_RESOURCE_DESC_st</a>	186
<a href="#">HIP_RESOURCE_VIEW_DESC_st</a>	187

<a href="#">HIP_TEXTURE_DESC_st</a>	188
<a href="#">hipArray</a>	189
<a href="#">hipChannelFormatDesc</a>	190
<a href="#">hipDeviceArch_t</a>	190
<a href="#">hipDeviceProp_t</a>	191
<a href="#">hipExtent</a>	193
<a href="#">hipExternalMemoryBufferDesc_st</a>	194
<a href="#">hipExternalMemoryHandleDesc_st</a>	194
<a href="#">hipExternalSemaphoreHandleDesc_st</a>	194
<a href="#">hipExternalSemaphoreSignalParams_st</a>	194
<a href="#">hipExternalSemaphoreWaitParams_st</a>	195
<a href="#">hipFuncAttributes</a>	195
<a href="#">hipHostNodeParams</a>	195
<a href="#">hipIpcEventHandle_st</a>	195
<a href="#">hipIpcMemHandle_st</a>	196
<a href="#">hipKernelNodeParams</a>	196
<a href="#">hipLaunchParams_t</a>	196
<a href="#">hipMemcpy3DParms</a>	196
<a href="#">hipMemsetParams</a>	196
<a href="#">hipMipmappedArray</a>	197
<a href="#">hipPitchedPtr</a>	197
<a href="#">hipPointerAttribute_t</a>	197
<a href="#">hipPos</a>	197
<a href="#">hipResourceDesc</a>	198
<a href="#">hipResourceViewDesc</a>	198
<a href="#">hipTextureDesc</a>	198
<a href="#">int1</a>	199
<a href="#">int16</a>	199
<a href="#">int2</a>	199
<a href="#">int3</a>	199
<a href="#">int4</a>	199
<a href="#">int8</a>	199
<a href="#">hip_impl::is_callable&lt; Call &gt;</a>	199
<a href="#">hip_impl::is_callable_impl&lt; typename, typename &gt;</a>	200
<a href="#">hip_impl::is_callable_impl&lt; F(Ts...), void_t&lt; decltype(simple_invoke(std::declval&lt; F &gt;()), std::declval&lt; Ts &gt;()...)) &gt;</a>	200
<a href="#">hip_impl::kernarg</a>	200
<a href="#">hip_impl::kernargs_size_align</a>	200
<a href="#">long1</a>	201
<a href="#">long16</a>	201
<a href="#">long2</a>	201
<a href="#">long3</a>	201
<a href="#">long4</a>	201
<a href="#">long8</a>	201
<a href="#">longlong1</a>	201
<a href="#">longlong16</a>	202
<a href="#">longlong2</a>	202
<a href="#">longlong3</a>	202
<a href="#">longlong4</a>	202
<a href="#">longlong8</a>	202
<a href="#">hip_impl::program_state</a>	202
<a href="#">short1</a>	202
<a href="#">short16</a>	203
<a href="#">short2</a>	203
<a href="#">short3</a>	203
<a href="#">short4</a>	203
<a href="#">short8</a>	203
<a href="#">surfaceReference</a>	203



TData	203
textureReference	203
uchar1	204
uchar16	204
uchar2	204
uchar2Holder	204
uchar3	204
uchar4	205
uchar8	205
ucharHolder	205
uint1	205
uint16	205
uint2	205
uint3	205
uint4	205
uint8	206
ulong1	206
ulong16	206
ulong2	206
ulong3	206
ulong4	206
ulong8	206
ulonglong1	206
ulonglong16	206
ulonglong2	207
ulonglong3	207
ulonglong4	207
ulonglong8	207
ushort1	207
ushort16	207
ushort2	207
ushort3	207
ushort4	207
ushort8	208



## Chapter 4

# Module Documentation

### 4.1 Global enum and defines

#### Classes

- struct [dim3](#)
- struct [hipLaunchParams\\_t](#)
- struct [hipExternalMemoryHandleDesc\\_st](#)
- struct [hipExternalMemoryBufferDesc\\_st](#)
- struct [hipExternalSemaphoreHandleDesc\\_st](#)
- struct [hipExternalSemaphoreSignalParams\\_st](#)
- struct [hipExternalSemaphoreWaitParams\\_st](#)

#### Macros

- `#define __HIP_NODISCARD`
- `#define hipStreamDefault 0x00`  
*Flags that can be used with [hipStreamCreateWithFlags](#).*
- `#define hipStreamNonBlocking 0x01`
- `#define hipEventDefault 0x0`  
*Flags that can be used with [hipEventCreateWithFlags](#):*
- `#define hipEventBlockingSync 0x1`  
*Waiting will yield CPU. Power-friendly and usage-friendly but may increase latency.*
- `#define hipEventDisableTiming 0x2`  
*Disable event's capability to record timing information. May improve performance.*
- `#define hipEventInterprocess 0x4`  
*Event can support IPC.*
- `#define hipEventReleaseToDevice 0x40000000`
- `#define hipEventReleaseToSystem 0x80000000`
- `#define hipHostMallocDefault 0x0`  
*Flags that can be used with [hipHostMalloc](#).*
- `#define hipHostMallocPortable 0x1`  
*Memory is considered allocated by all contexts.*
- `#define hipHostMallocMapped 0x2`  
*can be obtained with [hipHostGetDevicePointer](#).*
- `#define hipHostMallocWriteCombined 0x4`

- #define [hipHostMallocNumaUser](#) 0x20000000  
*Host memory allocation will follow numa policy set by user.*
- #define [hipHostMallocCoherent](#) 0x40000000  
*allocation.*
- #define [hipHostMallocNonCoherent](#) 0x80000000  
*allocation.*
- #define [hipMemAttachGlobal](#) 0x01  
*Memory can be accessed by any stream on any device.*
- #define [hipMemAttachHost](#) 0x02  
*Memory cannot be accessed by any stream on any device.*
- #define [hipMemAttachSingle](#) 0x04  
*the associated device*
- #define [hipDeviceMallocDefault](#) 0x0
- #define [hipDeviceMallocFinegrained](#) 0x1  
*Memory is allocated in fine grained region of device.*
- #define [hipMallocSignalMemory](#) 0x2
- #define [hipHostRegisterDefault](#) 0x0  
*Flags that can be used with hipHostRegister.*
- #define [hipHostRegisterPortable](#) 0x1  
*Memory is considered registered by all contexts.*
- #define [hipHostRegisterMapped](#) 0x2  
*can be obtained with [hipHostGetDevicePointer](#).*
- #define [hipHostRegisterIoMemory](#) 0x4  
*Not supported.*
- #define [hipExtHostRegisterCoarseGrained](#) 0x8  
*Coarse Grained host memory lock.*
- #define [hipDeviceScheduleAuto](#) 0x0  
*Automatically select between Spin and Yield.*
- #define [hipDeviceScheduleSpin](#) 0x1  
*may consume more power.*
- #define [hipDeviceScheduleYield](#) 0x2  
*power and is friendlier to other threads in the system.*
- #define [hipDeviceScheduleBlockingSync](#) 0x4
- #define [hipDeviceScheduleMask](#) 0x7
- #define [hipDeviceMapHost](#) 0x8
- #define [hipDeviceLmemResizeToMax](#) 0x16
- #define [hipArrayDefault](#) 0x00  
*Default HIP array allocation flag.*
- #define [hipArrayLayered](#) 0x01
- #define [hipArraySurfaceLoadStore](#) 0x02
- #define [hipArrayCubemap](#) 0x04
- #define [hipArrayTextureGather](#) 0x08
- #define [hipOccupancyDefault](#) 0x00
- #define [hipCooperativeLaunchMultiDeviceNoPreSync](#) 0x01
- #define [hipCooperativeLaunchMultiDeviceNoPostSync](#) 0x02
- #define [hipCpuDeviceld](#) ((int)-1)
- #define [hipInvalidDeviceld](#) ((int)-2)
- #define [hipExtAnyOrderLaunch](#) 0x01  
*AnyOrderLaunch of kernels.*
- #define [hipStreamWaitValueGte](#) 0x0
- #define [hipStreamWaitValueEq](#) 0x1
- #define [hipStreamWaitValueAnd](#) 0x2
- #define [hipStreamWaitValueNor](#) 0x3
- #define [hipStreamPerThread](#) ((hipStream\_t)2)  
*Implicit stream per application thread.*

## Typedefs

- typedef enum \_\_HIP\_NODISCARD hipError\_t **hipError\_t**
- typedef enum hipDeviceAttribute\_t **hipDeviceAttribute\_t**
- typedef enum hipMemoryAdvise **hipMemoryAdvise**
- typedef enum hipMemRangeCoherencyMode **hipMemRangeCoherencyMode**
- typedef enum hipMemRangeAttribute **hipMemRangeAttribute**
- typedef enum hipJitOption **hipJitOption**
- typedef enum hipFuncAttribute **hipFuncAttribute**
- typedef enum hipFuncCache\_t **hipFuncCache\_t**
- typedef enum hipSharedMemConfig **hipSharedMemConfig**
- typedef struct dim3 **dim3**
- typedef struct hipLaunchParams\_t **hipLaunchParams**
- typedef enum hipExternalMemoryHandleType\_enum **hipExternalMemoryHandleType**
- typedef struct hipExternalMemoryHandleDesc\_st **hipExternalMemoryHandleDesc**
- typedef struct hipExternalMemoryBufferDesc\_st **hipExternalMemoryBufferDesc**
- typedef void \* **hipExternalMemory\_t**
- typedef enum hipExternalSemaphoreHandleType\_enum **hipExternalSemaphoreHandleType**
- typedef struct hipExternalSemaphoreHandleDesc\_st **hipExternalSemaphoreHandleDesc**
- typedef void \* **hipExternalSemaphore\_t**
- typedef struct hipExternalSemaphoreSignalParams\_st **hipExternalSemaphoreSignalParams**
- typedef struct hipExternalSemaphoreWaitParams\_st **hipExternalSemaphoreWaitParams**
- typedef enum hipGLDeviceList **hipGLDeviceList**
- typedef enum hipGraphicsRegisterFlags **hipGraphicsRegisterFlags**
- typedef struct \_hipGraphicsResource **hipGraphicsResource**
- typedef hipGraphicsResource \* **hipGraphicsResource\_t**

## Enumerations

- enum hipDeviceAttribute\_t {  
hipDeviceAttributeMaxThreadsPerBlock, hipDeviceAttributeMaxBlockDimX, hipDeviceAttributeMaxBlockDimY,  
hipDeviceAttributeMaxBlockDimZ,  
hipDeviceAttributeMaxGridDimX, hipDeviceAttributeMaxGridDimY, hipDeviceAttributeMaxGridDimZ,  
hipDeviceAttributeMaxSharedMemoryPerBlock,  
hipDeviceAttributeTotalConstantMemory, hipDeviceAttributeWarpSize, hipDeviceAttributeMaxRegistersPerBlock,  
hipDeviceAttributeClockRate,  
hipDeviceAttributeMemoryClockRate, hipDeviceAttributeMemoryBusWidth, hipDeviceAttributeMultiprocessorCount,  
hipDeviceAttributeComputeMode,  
hipDeviceAttributeL2CacheSize, hipDeviceAttributeMaxThreadsPerMultiProcessor, hipDeviceAttributeComputeCapabilityMajor,  
hipDeviceAttributeComputeCapabilityMinor,  
hipDeviceAttributeConcurrentKernels, hipDeviceAttributePciBusId, hipDeviceAttributePciDeviceId, hipDeviceAttributeMaxShare,  
hipDeviceAttributeIsMultiGpuBoard, hipDeviceAttributeIntegrated, hipDeviceAttributeCooperativeLaunch,  
hipDeviceAttributeCooperativeMultiDeviceLaunch,  
hipDeviceAttributeMaxTexture1DWidth, hipDeviceAttributeMaxTexture2DWidth, hipDeviceAttributeMaxTexture2DHeight,  
hipDeviceAttributeMaxTexture3DWidth,  
hipDeviceAttributeMaxTexture3DHeight, hipDeviceAttributeMaxTexture3DDepth, hipDeviceAttributeHdpMemFlushCntl,  
hipDeviceAttributeHdpRegFlushCntl,  
hipDeviceAttributeMaxPitch, hipDeviceAttributeTextureAlignment, hipDeviceAttributeTexturePitchAlignment,  
hipDeviceAttributeKernelExecTimeout,  
hipDeviceAttributeCanMapHostMemory, hipDeviceAttributeEccEnabled, hipDeviceAttributeCooperativeMultiDeviceUnmatched,  
hipDeviceAttributeCooperativeMultiDeviceUnmatchedGridDim,  
hipDeviceAttributeCooperativeMultiDeviceUnmatchedBlockDim, hipDeviceAttributeCooperativeMultiDeviceUnmatchedSharedM,  
hipDeviceAttributeAsicRevision, hipDeviceAttributeManagedMemory,  
hipDeviceAttributeDirectManagedMemAccessFromHost, hipDeviceAttributeConcurrentManagedAccess,  
hipDeviceAttributePageableMemoryAccess, hipDeviceAttributePageableMemoryAccessUsesHostPageTables,  
hipDeviceAttributeCanUseStreamWaitValue }

- enum **hipComputeMode** { **hipComputeModeDefault** = 0, **hipComputeModeExclusive** = 1, **hipComputeModeProhibited** = 2, **hipComputeModeExclusiveProcess** = 3 }
- enum **hipMemoryAdvise** { **hipMemAdviseSetReadMostly** = 1, **hipMemAdviseUnsetReadMostly** = 2, **hipMemAdviseSetPreferredLocation** = 3, **hipMemAdviseUnsetPreferredLocation** = 4, **hipMemAdviseSetAccessedBy** = 5, **hipMemAdviseUnsetAccessedBy** = 6, **hipMemAdviseSetCoarseGrain** = 100, **hipMemAdviseUnsetCoarseGrain** = 101, **hipMemAdviseSetReadMostly**, **hipMemAdviseUnsetReadMostly**, **hipMemAdviseSetPreferredLocation**, **hipMemAdviseUnsetPreferredLocation**, **hipMemAdviseSetAccessedBy**, **hipMemAdviseUnsetAccessedBy**, **hipMemAdviseSetReadMostly**, **hipMemAdviseUnsetReadMostly**, **hipMemAdviseSetPreferredLocation**, **hipMemAdviseUnsetPreferredLocation**, **hipMemAdviseSetAccessedBy**, **hipMemAdviseUnsetAccessedBy** }
- enum **hipMemRangeCoherencyMode** { **hipMemRangeCoherencyModeFineGrain** = 0, **hipMemRangeCoherencyModeCoarseGrain** = 1, **hipMemRangeCoherencyModeIndeterminate** = 2 }
- enum **hipMemRangeAttribute** { **hipMemRangeAttributeReadMostly** = 1, **hipMemRangeAttributePreferredLocation** = 2, **hipMemRangeAttributeAccessedBy** = 3, **hipMemRangeAttributeLastPrefetchLocation** = 4, **hipMemRangeAttributeCoherencyMode** = 100 }
- enum **hipJitOption** { **hipJitOptionMaxRegisters** = 0, **hipJitOptionThreadsPerBlock**, **hipJitOptionWallTime**, **hipJitOptionInfoLogBuffer**, **hipJitOptionInfoLogBufferSizeBytes**, **hipJitOptionErrorLogBuffer**, **hipJitOptionErrorLogBufferSizeBytes**, **hipJitOptionOptimizationLevel**, **hipJitOptionTargetFromContext**, **hipJitOptionTarget**, **hipJitOptionFallbackStrategy**, **hipJitOptionGenerateDebugInfo**, **hipJitOptionLogVerbose**, **hipJitOptionGenerateLineInfo**, **hipJitOptionCacheMode**, **hipJitOptionSm3xOpt**, **hipJitOptionFastCompile**, **hipJitOptionNumOptions** }
- enum **hipFuncAttribute** { **hipFuncAttributeMaxDynamicSharedMemorySize** = 8, **hipFuncAttributePreferredSharedMemoryCarveout** = 9, **hipFuncAttributeMax** }
- enum **hipFuncCache\_t** { **hipFuncCachePreferNone**, **hipFuncCachePreferShared**, **hipFuncCachePreferL1**, **hipFuncCachePreferEqual** }
- enum **hipSharedMemConfig** { **hipSharedMemBankSizeDefault**, **hipSharedMemBankSizeFourByte**, **hipSharedMemBankSizeEightByte** }
- enum **hipExternalMemoryHandleType\_enum** { **hipExternalMemoryHandleTypeOpaqueFd** = 1, **hipExternalMemoryHandleTypeOpaqueWin32** = 2, **hipExternalMemoryHandleTypeOpaqueWin32Kmt** = 3, **hipExternalMemoryHandleTypeD3D12Heap** = 4, **hipExternalMemoryHandleTypeD3D12Resource** = 5, **hipExternalMemoryHandleTypeD3D11Resource** = 6, **hipExternalMemoryHandleTypeD3D11ResourceKmt** = 7 }
- enum **hipExternalSemaphoreHandleType\_enum** { **hipExternalSemaphoreHandleTypeOpaqueFd** = 1, **hipExternalSemaphoreHandleTypeOpaqueWin32** = 2, **hipExternalSemaphoreHandleTypeOpaqueWin32Kmt** = 3, **hipExternalSemaphoreHandleTypeD3D12Fence** = 4 }
- enum **hipGLDeviceList** { **hipGLDeviceListAll** = 1, **hipGLDeviceListCurrentFrame** = 2, **hipGLDeviceListNextFrame** = 3 }
- enum **hipGraphicsRegisterFlags** { **hipGraphicsRegisterFlagsNone** = 0, **hipGraphicsRegisterFlagsReadOnly** = 1, **hipGraphicsRegisterFlagsWriteDiscard**, **hipGraphicsRegisterFlagsSurfaceLoadStore** = 4, **hipGraphicsRegisterFlagsTextureGather** }

#### 4.1.1 Detailed Description

#### 4.1.2 Macro Definition Documentation

#### 4.1.2.1 hipDeviceScheduleSpin

```
#define hipDeviceScheduleSpin 0x1
```

may consume more power.

Dedicate a CPU core to spin-wait. Provides lowest latency, but burns a CPU core and

#### 4.1.2.2 hipDeviceScheduleYield

```
#define hipDeviceScheduleYield 0x2
```

power and is friendlier to other threads in the system.

Yield the CPU to the operating system when waiting. May increase latency, but lowers

#### 4.1.2.3 hipEventDefault

```
#define hipEventDefault 0x0
```

Flags that can be used with `hipEventCreateWithFlags`:

Default flags

#### 4.1.2.4 hipEventInterprocess

```
#define hipEventInterprocess 0x4
```

Event can support IPC.

#### Warning

- not supported in HIP.

#### 4.1.2.5 hipEventReleaseToSystem

```
#define hipEventReleaseToSystem 0x80000000
```

< Use a device-scope release when recording this event. This flag is useful to obtain more precise timings of commands between events. The flag is a no-op on CUDA platforms.

#### 4.1.2.6 hipHostMallocCoherent

```
#define hipHostMallocCoherent 0x40000000
```

allocation.

Allocate coherent memory. Overrides `HIP_COHERENT_HOST_ALLOC` for specific

#### 4.1.2.7 hipHostMallocDefault

```
#define hipHostMallocDefault 0x0
```

Flags that can be used with hipHostMalloc.

< Use a system-scope release when recording this event. This flag is useful to make non-coherent host memory visible to the host. The flag is a no-op on CUDA platforms.

#### 4.1.2.8 hipHostMallocMapped

```
#define hipHostMallocMapped 0x2
```

can be obtained with [hipHostGetDevicePointer](#).

Map the allocation into the address space for the current device. The device pointer

#### 4.1.2.9 hipHostMallocNonCoherent

```
#define hipHostMallocNonCoherent 0x80000000
```

allocation.

Allocate non-coherent memory. Overrides HIP\_COHERENT\_HOST\_ALLOC for specific

#### 4.1.2.10 hipHostRegisterDefault

```
#define hipHostRegisterDefault 0x0
```

Flags that can be used with hipHostRegister.

Memory is Mapped and Portable

#### 4.1.2.11 hipHostRegisterMapped

```
#define hipHostRegisterMapped 0x2
```

can be obtained with [hipHostGetDevicePointer](#).

Map the allocation into the address space for the current device. The device pointer

#### 4.1.2.12 hipMallocSignalMemory

```
#define hipMallocSignalMemory 0x2
```

Memory represents a HSA signal.



#### 4.1.2.13 hipMemAttachSingle

```
#define hipMemAttachSingle 0x04
```

the associated device

Memory can only be accessed by a single stream on

#### 4.1.2.14 hipStreamDefault

```
#define hipStreamDefault 0x00
```

Flags that can be used with `hipStreamCreateWithFlags`.

Default stream creation flags. These are used with [hipStreamCreate\(\)](#).

#### 4.1.2.15 hipStreamNonBlocking

```
#define hipStreamNonBlocking 0x01
```

Stream does not implicitly synchronize with null stream

### 4.1.3 Typedef Documentation

#### 4.1.3.1 dim3

```
typedef struct dim3 dim3
```

Struct for data in 3D

#### 4.1.3.2 hipExternalSemaphoreWaitParams

```
typedef struct hipExternalSemaphoreWaitParams_st hipExternalSemaphoreWaitParams
```

External semaphore wait parameters, compatible with driver type

#### 4.1.3.3 hipFuncAttribute

```
typedef enum hipFuncAttribute hipFuncAttribute
```

#### Warning

On AMD devices and some Nvidia devices, these hints and controls are ignored.

#### 4.1.3.4 hipFuncCache\_t

```
typedef enum hipFuncCache_t hipFuncCache_t
```

##### Warning

On AMD devices and some Nvidia devices, these hints and controls are ignored.

#### 4.1.3.5 hipSharedMemConfig

```
typedef enum hipSharedMemConfig hipSharedMemConfig
```

##### Warning

On AMD devices and some Nvidia devices, these hints and controls are ignored.

### 4.1.4 Enumeration Type Documentation

#### 4.1.4.1 hipDeviceAttribute\_t

```
enum hipDeviceAttribute_t
```

##### Enumerator

hipDeviceAttributeMaxThreadsPerBlock	Maximum number of threads per block.
hipDeviceAttributeMaxBlockDimX	Maximum x-dimension of a block.
hipDeviceAttributeMaxBlockDimY	Maximum y-dimension of a block.
hipDeviceAttributeMaxBlockDimZ	Maximum z-dimension of a block.
hipDeviceAttributeMaxGridDimX	Maximum x-dimension of a grid.
hipDeviceAttributeMaxGridDimY	Maximum y-dimension of a grid.
hipDeviceAttributeMaxGridDimZ	Maximum z-dimension of a grid.
hipDeviceAttributeMaxSharedMemoryPerBlock	Maximum shared memory available per block in bytes.
hipDeviceAttributeTotalConstantMemory	Constant memory size in bytes.
hipDeviceAttributeWarpSize	Warp size in threads.
hipDeviceAttributeMaxRegistersPerBlock	Maximum number of 32-bit registers available to a thread block. This number is shared by all thread blocks simultaneously resident on a multiprocessor.
hipDeviceAttributeClockRate	Peak clock frequency in kilohertz.
hipDeviceAttributeMemoryClockRate	Peak memory clock frequency in kilohertz.
hipDeviceAttributeMemoryBusWidth	Global memory bus width in bits.
hipDeviceAttributeMultiprocessorCount	Number of multiprocessors on the device.
hipDeviceAttributeComputeMode	Compute mode that device is currently in.
hipDeviceAttributeL2CacheSize	Size of L2 cache in bytes. 0 if the device doesn't have L2 cache.

## Enumerator

hipDeviceAttributeMaxThreadsPerMultiProcessor	Maximum resident threads per multiprocessor.
hipDeviceAttributeComputeCapabilityMajor	Major compute capability version number.
hipDeviceAttributeComputeCapabilityMinor	Minor compute capability version number.
hipDeviceAttributeConcurrentKernels	Device can possibly execute multiple kernels concurrently.
hipDeviceAttributePciBusId	PCI Bus ID.
hipDeviceAttributePciDeviceId	PCI Device ID.
hipDeviceAttributeMaxSharedMemoryPerMultiProcessor	Maximum Shared Memory Per Multiprocessor.
hipDeviceAttributeIsMultiGpuBoard	Multiple GPU devices.
hipDeviceAttributeIntegrated	iGPU
hipDeviceAttributeCooperativeLaunch	Support cooperative launch.
hipDeviceAttributeCooperativeMultiDeviceLaunch	Support cooperative launch on multiple devices.
hipDeviceAttributeMaxTexture1DWidth	Maximum number of elements in 1D images.
hipDeviceAttributeMaxTexture2DWidth	Maximum dimension width of 2D images in image elements.
hipDeviceAttributeMaxTexture2DHeight	Maximum dimension height of 2D images in image elements.
hipDeviceAttributeMaxTexture3DWidth	Maximum dimension width of 3D images in image elements.
hipDeviceAttributeMaxTexture3DHeight	Maximum dimensions height of 3D images in image elements.
hipDeviceAttributeMaxTexture3DDepth	Maximum dimensions depth of 3D images in image elements.
hipDeviceAttributeHdpMemFlushCntl	Address of the HDP_MEM_COHERENCY_FLUSH_CNTL register.
hipDeviceAttributeHdpRegFlushCntl	Address of the HDP_REG_COHERENCY_FLUSH_CNTL register.
hipDeviceAttributeMaxPitch	Maximum pitch in bytes allowed by memory copies.
hipDeviceAttributeTextureAlignment	Alignment requirement for textures.
hipDeviceAttributeTexturePitchAlignment	Pitch alignment requirement for 2D texture references bound to pitched memory;.
hipDeviceAttributeKernelExecTimeout	Run time limit for kernels executed on the device.
hipDeviceAttributeCanMapHostMemory	Device can map host memory into device address space.
hipDeviceAttributeEccEnabled	Device has ECC support enabled.
hipDeviceAttributeCooperativeMultiDeviceUnmatchedFunc	Supports cooperative launch on multiple devices with unmatched functions
hipDeviceAttributeCooperativeMultiDeviceUnmatchedGridDim	Supports cooperative launch on multiple devices with unmatched grid dimensions
hipDeviceAttributeCooperativeMultiDeviceUnmatchedBlockDim	Supports cooperative launch on multiple devices with unmatched block dimensions
hipDeviceAttributeCooperativeMultiDeviceUnmatchedSharedMem	Supports cooperative launch on multiple devices with unmatched shared memories
hipDeviceAttributeAsicRevision	Revision of the GPU in this device.
hipDeviceAttributeManagedMemory	Device supports allocating managed memory on this system.
hipDeviceAttributeDirectManagedMemAccessFromHost	Host can directly access managed memory on the device without migration
hipDeviceAttributeConcurrentManagedAccess	Device can coherently access managed memory concurrently with the CPU

## Enumerator

hipDeviceAttributePageableMemoryAccess	Device supports coherently accessing pageable memory without calling hipHostRegister on it
hipDeviceAttributePageableMemoryAccessUsesHostPageTables	Device accesses pageable memory via the host's page tables
hipDeviceAttributeCanUseStreamWaitValue	'1' if Device supports <a href="#">hipStreamWaitValue32()</a> and <a href="#">hipStreamWaitValue64()</a> , '0' otherwise.

## 4.1.4.2 hipFuncAttribute

```
enum hipFuncAttribute
```

## Warning

On AMD devices and some Nvidia devices, these hints and controls are ignored.

## 4.1.4.3 hipFuncCache\_t

```
enum hipFuncCache_t
```

## Warning

On AMD devices and some Nvidia devices, these hints and controls are ignored.

## Enumerator

hipFuncCachePreferNone	no preference for shared memory or L1 (default)
hipFuncCachePreferShared	prefer larger shared memory and smaller L1 cache
hipFuncCachePreferL1	prefer larger L1 cache and smaller shared memory
hipFuncCachePreferEqual	prefer equal size L1 cache and shared memory

## 4.1.4.4 hipGLDeviceList

```
enum hipGLDeviceList
```

## Enumerator

hipGLDeviceListAll	All hip devices used by current OpenGL context.
hipGLDeviceListCurrentFrame	frame Hip devices used by current OpenGL context in current
hipGLDeviceListNextFrame	frame. Hip devices used by current OpenGL context in next

#### 4.1.4.5 hipGraphicsRegisterFlags

enum `hipGraphicsRegisterFlags`

##### Enumerator

<code>hipGraphicsRegisterFlagsReadOnly</code>	HIP will not write to this registered resource.
<code>hipGraphicsRegisterFlagsWriteDiscard</code>	HIP will only write and will not read from this registered resource.
<code>hipGraphicsRegisterFlagsSurfaceLoadStore</code>	HIP will bind this resource to a surface.
<code>hipGraphicsRegisterFlagsTextureGather</code>	HIP will perform texture gather operations on this registered resource.

#### 4.1.4.6 hipMemoryAdvise

enum `hipMemoryAdvise`

##### Enumerator

<code>hipMemAdviseSetReadMostly</code>	Data will mostly be read and only occasionally be written to
<code>hipMemAdviseUnsetReadMostly</code>	Undo the effect of <code>hipMemAdviseSetReadMostly</code> .
<code>hipMemAdviseSetPreferredLocation</code>	Set the preferred location for the data as the specified device
<code>hipMemAdviseUnsetPreferredLocation</code>	Clear the preferred location for the data.
<code>hipMemAdviseSetAccessedBy</code>	Data will be accessed by the specified device, so prevent page faults as much as possible
<code>hipMemAdviseUnsetAccessedBy</code>	Let HIP to decide on the page faulting policy for the specified device
<code>hipMemAdviseSetCoarseGrain</code>	The default memory model is fine-grain. That allows coherent operations between host and device, while executing kernels. The coarse-grain can be used for data that only needs to be coherent at dispatch boundaries for better performance
<code>hipMemAdviseUnsetCoarseGrain</code>	Restores cache coherency policy back to fine-grain.

#### 4.1.4.7 hipMemRangeAttribute

enum `hipMemRangeAttribute`

##### Enumerator

<code>hipMemRangeAttributeReadMostly</code>	Whether the range will mostly be read and only occasionally be written to
<code>hipMemRangeAttributePreferredLocation</code>	The preferred location of the range.

## Enumerator

hipMemRangeAttributeAccessedBy	Memory range has hipMemAdviseSetAccessedBy set for the specified device
hipMemRangeAttributeLastPrefetchLocation	prefetched The last location to where the range was
hipMemRangeAttributeCoherencyMode	Returns coherency mode <a href="#">hipMemRangeCoherencyMode</a> for the range

## 4.1.4.8 hipMemRangeCoherencyMode

enum [hipMemRangeCoherencyMode](#)

## Enumerator

hipMemRangeCoherencyModeFineGrain	Updates to memory with this attribute can be done coherently from all devices
hipMemRangeCoherencyModeCoarseGrain	Writes to memory with this attribute can be performed by a single device at a time
hipMemRangeCoherencyModeIndeterminate	Memory region queried contains subregions with both <a href="#">hipMemRangeCoherencyModeFineGrain</a> and <a href="#">hipMemRangeCoherencyModeCoarseGrain</a> attributes

## 4.1.4.9 hipSharedMemConfig

enum [hipSharedMemConfig](#)

## Warning

On AMD devices and some Nvidia devices, these hints and controls are ignored.

## Enumerator

hipSharedMemBankSizeDefault	The compiler selects a device-specific value for the banking.
hipSharedMemBankSizeFourByte	Shared mem is banked at 4-bytes intervals and performs best when adjacent threads access data 4 bytes apart.
hipSharedMemBankSizeEightByte	Shared mem is banked at 8-byte intervals and performs best when adjacent threads access data 4 bytes apart.

## 4.2 HIP API

### Modules

- [Initialization and Version](#)
- [Device Management](#)
- [Execution Control](#)
- [Error Handling](#)
- [Stream Management](#)
- [Event Management](#)
- [Memory Management](#)
- [PeerToPeer Device Memory Access](#)
- [Context Management](#)
- [Module Management](#)
- [Occupancy](#)
- [Profiler Control\[Deprecated\]](#)
- [Launch API to support the triple-chevron syntax](#)
- [Texture Management](#)
- [Runtime Compilation](#)
- [Callback Activity APIs](#)
- [Graph Management](#)
- [Interop](#)

### 4.2.1 Detailed Description

Defines the HIP API. See the individual sections for more information.

## 4.3 Initialization and Version

### Functions

- hipError\_t [hipInit](#) (unsigned int flags)  
*Explicitly initializes the HIP runtime.*
- hipError\_t [hipDriverGetVersion](#) (int \*driverVersion)  
*Returns the approximate HIP driver version.*
- hipError\_t [hipRuntimeGetVersion](#) (int \*runtimeVersion)  
*Returns the approximate HIP Runtime version.*
- hipError\_t [hipDeviceGet](#) (hipDevice\_t \*device, int ordinal)  
*Returns a handle to a compute device.*
- hipError\_t [hipDeviceComputeCapability](#) (int \*major, int \*minor, hipDevice\_t device)  
*Returns the compute capability of the device.*
- hipError\_t [hipDeviceGetName](#) (char \*name, int len, hipDevice\_t device)  
*Returns an identifier string for the device.*
- hipError\_t [hipDeviceGetP2PAttribute](#) (int \*value, hipDeviceP2PAttr attr, int srcDevice, int dstDevice)  
*Returns a value for attr of link between two devices.*
- hipError\_t [hipDeviceGetPCIBusId](#) (char \*pciBusId, int len, int device)  
*Returns a PCI Bus Id string for the device, overloaded to take int device ID.*
- hipError\_t [hipDeviceGetByPCIBusId](#) (int \*device, const char \*pciBusId)  
*Returns a handle to a compute device.*
- hipError\_t [hipDeviceTotalMem](#) (size\_t \*bytes, hipDevice\_t device)  
*Returns the total amount of memory on the device.*

### 4.3.1 Detailed Description

This section describes the initialization and version functions of HIP runtime API.

### 4.3.2 Function Documentation

#### 4.3.2.1 hipDeviceComputeCapability()

```
hipError_t hipDeviceComputeCapability (
    int * major,
    int * minor,
    hipDevice_t device )
```

Returns the compute capability of the device.

#### Parameters

out	<i>major</i>	
out	<i>minor</i>	
in	<i>device</i>	



**Returns**

#hipSuccess, #hipErrorInavlidDevice

**4.3.2.2 hipDeviceGet()**

```
hipError_t hipDeviceGet (
    hipDevice_t * device,
    int ordinal )
```

Returns a handle to a compute device.

**Parameters**

out	<i>device</i>	
in	<i>ordinal</i>	

**Returns**

#hipSuccess, #hipErrorInavlidDevice

**4.3.2.3 hipDeviceGetByPCIBusId()**

```
hipError_t hipDeviceGetByPCIBusId (
    int * device,
    const char * pciBusId )
```

Returns a handle to a compute device.

**Parameters**

out	<i>device</i>	handle
in	<i>PCI</i>	Bus ID

**Returns**

#hipSuccess, #hipErrorInavlidDevice, #hipErrorInvalidValue

**4.3.2.4 hipDeviceGetName()**

```
hipError_t hipDeviceGetName (
    char * name,
```

```
int len,  
hipDevice_t device )
```

Returns an identifier string for the device.

**Parameters**

out	<i>name</i>	
in	<i>len</i>	
in	<i>device</i>	

**Returns**

#hipSuccess, #hipErrorInvalidDevice

**4.3.2.5 hipDeviceGetP2PAttribute()**

```
hipError_t hipDeviceGetP2PAttribute (
    int * value,
    hipDeviceP2PAttr attr,
    int srcDevice,
    int dstDevice )
```

Returns a value for attr of link between two devices.

**Parameters**

out	<i>value</i>	
in	<i>attr</i>	
in	<i>srcDevice</i>	
in	<i>dstDevice</i>	

**Returns**

#hipSuccess, #hipErrorInvalidDevice

**4.3.2.6 hipDeviceGetPCIBusId()**

```
hipError_t hipDeviceGetPCIBusId (
    char * pciBusId,
    int len,
    int device )
```

Returns a PCI Bus Id string for the device, overloaded to take int device ID.

**Parameters**

out	<i>pci↔ BusId</i>	
in	<i>len</i>	
in	<i>device</i>	

**Returns**

#hipSuccess, #hipErrorInvalidDevice

**4.3.2.7 hipDeviceTotalMem()**

```
hipError_t hipDeviceTotalMem (
    size_t * bytes,
    hipDevice_t device )
```

Returns the total amount of memory on the device.

**Parameters**

out	<i>bytes</i>	
in	<i>device</i>	

**Returns**

#hipSuccess, #hipErrorInvalidDevice

**4.3.2.8 hipDriverGetVersion()**

```
hipError_t hipDriverGetVersion (
    int * driverVersion )
```

Returns the approximate HIP driver version.

**Parameters**

out	<i>driverVersion</i>	
-----	----------------------	--

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

The HIP feature set does not correspond to an exact CUDA SDK driver revision. This function always set \*driverVersion to 4 as an approximation though HIP supports some features which were introduced in later CUDA SDK revisions. HIP apps code should not rely on the driver revision number here and should use arch feature flags to test device capabilities or conditional compilation.

**See also**

[hipRuntimeGetVersion](#)

#### 4.3.2.9 hipInit()

```
hipError_t hipInit (
    unsigned int flags )
```

Explicitly initializes the HIP runtime.

Most HIP APIs implicitly initialize the HIP runtime. This API provides control over the timing of the initialization.

#### 4.3.2.10 hipRuntimeGetVersion()

```
hipError_t hipRuntimeGetVersion (
    int * runtimeVersion )
```

Returns the approximate HIP Runtime version.

##### Parameters

out	<i>runtimeVersion</i>	
-----	-----------------------	--

##### Returns

#hipSuccess, #hipErrorInvalidValue

##### Warning

The version definition of HIP runtime is different from CUDA. On AMD platform, the function returns HIP runtime version, while on NVIDIA platform, it returns CUDA runtime version. And there is no mapping/correlation between HIP version and CUDA version.

##### See also

[hipDriverGetVersion](#)

## 4.4 Device Management

### Functions

- `hipError_t hipDeviceSynchronize` (void)  
*Waits on all active streams on current device.*
- `hipError_t hipDeviceReset` (void)  
*The state of current device is discarded and updated to a fresh state.*
- `hipError_t hipSetDevice` (int deviceId)  
*Set default device to be used for subsequent hip API calls from this thread.*
- `hipError_t hipGetDevice` (int \*deviceId)  
*Return the default device id for the calling host thread.*
- `hipError_t hipGetDeviceCount` (int \*count)  
*Return number of compute-capable devices.*
- `hipError_t hipDeviceGetAttribute` (int \*pi, `hipDeviceAttribute_t` attr, int deviceId)  
*Query for a specific device attribute.*
- `hipError_t hipGetDeviceProperties` (`hipDeviceProp_t` \*prop, int deviceId)  
*Returns device properties.*
- `hipError_t hipDeviceSetCacheConfig` (`hipFuncCache_t` cacheConfig)  
*Set L1/Shared cache partition.*
- `hipError_t hipDeviceGetCacheConfig` (`hipFuncCache_t` \*cacheConfig)  
*Set Cache configuration for a specific function.*
- `hipError_t hipDeviceGetLimit` (size\_t \*pValue, enum `hipLimit_t` limit)  
*Get Resource limits of current device.*
- `hipError_t hipDeviceGetSharedMemConfig` (`hipSharedMemConfig` \*pConfig)  
*Returns bank width of shared memory for current device.*
- `hipError_t hipGetDeviceFlags` (unsigned int \*flags)  
*Gets the flags set for current device.*
- `hipError_t hipDeviceSetSharedMemConfig` (`hipSharedMemConfig` config)  
*The bank width of shared memory on current device is set.*
- `hipError_t hipSetDeviceFlags` (unsigned flags)  
*The current device behavior is changed according the flags passed.*
- `hipError_t hipChooseDevice` (int \*device, const `hipDeviceProp_t` \*prop)  
*Device which matches `hipDeviceProp_t` is returned.*
- `hipError_t hipExtGetLinkTypeAndHopCount` (int device1, int device2, uint32\_t \*linktype, uint32\_t \*hopcount)  
*Returns the link type and hop count between two devices.*
- `hipError_t hipIpcGetMemHandle` (`hipIpcMemHandle_t` \*handle, void \*devPtr)  
*Gets an interprocess memory handle for an existing device memory allocation.*
- `hipError_t hipIpcOpenMemHandle` (void \*\*devPtr, `hipIpcMemHandle_t` handle, unsigned int flags)  
*Opens an interprocess memory handle exported from another process and returns a device pointer usable in the local process.*
- `hipError_t hipIpcCloseMemHandle` (void \*devPtr)  
*Close memory mapped with `hipIpcOpenMemHandle`.*
- `hipError_t hipIpcGetEventHandle` (`hipIpcEventHandle_t` \*handle, `hipEvent_t` event)  
*Gets an opaque interprocess handle for an event.*
- `hipError_t hipIpcOpenEventHandle` (`hipEvent_t` \*event, `hipIpcEventHandle_t` handle)  
*Opens an interprocess event handles.*

### 4.4.1 Detailed Description

This section describes the device management functions of HIP runtime API.

## 4.4.2 Function Documentation

### 4.4.2.1 hipChooseDevice()

```
hipError_t hipChooseDevice (
    int * device,
    const hipDeviceProp_t * prop )
```

Device which matches `hipDeviceProp_t` is returned.

#### Parameters

out	<i>device</i>	ID
in	<i>device</i>	properties pointer

#### Returns

#hipSuccess, #hipErrorInvalidValue

### 4.4.2.2 hipDeviceGetAttribute()

```
hipError_t hipDeviceGetAttribute (
    int * pi,
    hipDeviceAttribute_t attr,
    int deviceId )
```

Query for a specific device attribute.

#### Parameters

out	<i>pi</i>	pointer to value to return
in	<i>attr</i>	attribute to query
in	<i>deviceId</i>	which device to query for information

#### Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

### 4.4.2.3 hipDeviceGetCacheConfig()

```
hipError_t hipDeviceGetCacheConfig (
    hipFuncCache_t * cacheConfig )
```

Set Cache configuration for a specific function.



## Parameters

in	<i>cacheConfig</i>	
----	--------------------	--

## Returns

#hipSuccess, #hipErrorNotInitialized Note: AMD devices and some Nvidia GPUS do not support reconfigurable cache. This hint is ignored on those architectures.

## 4.4.2.4 hipDeviceGetLimit()

```
hipError_t hipDeviceGetLimit (
    size_t * pValue,
    enum hipLimit_t limit )
```

Get Resource limits of current device.

## Parameters

out	<i>pValue</i>	
in	<i>limit</i>	

## Returns

#hipSuccess, #hipErrorUnsupportedLimit, #hipErrorInvalidValue Note: Currently, only hipLimitMallocHeapSize is available

## 4.4.2.5 hipDeviceGetSharedMemConfig()

```
hipError_t hipDeviceGetSharedMemConfig (
    hipSharedMemConfig * pConfig )
```

Returns bank width of shared memory for current device.

## Parameters

out	<i>pConfig</i>	
-----	----------------	--

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

Note: AMD devices and some Nvidia GPUS do not support shared cache banking, and the hint is ignored on those architectures.

#### 4.4.2.6 hipDeviceReset()

```
hipError_t hipDeviceReset (
    void )
```

The state of current device is discarded and updated to a fresh state.

Calling this function deletes all streams created, memory allocated, kernels running, events created. Make sure that no other thread is using the device or streams, memory, kernels, events associated with the current device.

##### Returns

#hipSuccess

##### See also

[hipDeviceSynchronize](#)

#### 4.4.2.7 hipDeviceSetCacheConfig()

```
hipError_t hipDeviceSetCacheConfig (
    hipFuncCache_t cacheConfig )
```

Set L1/Shared cache partition.

##### Parameters

in	<i>cacheConfig</i>	
----	--------------------	--

##### Returns

#hipSuccess, #hipErrorNotInitialized Note: AMD devices and some Nvidia GPUS do not support reconfigurable cache. This hint is ignored on those architectures.

#### 4.4.2.8 hipDeviceSetSharedMemConfig()

```
hipError_t hipDeviceSetSharedMemConfig (
    hipSharedMemConfig config )
```

The bank width of shared memory on current device is set.

##### Parameters

in	<i>config</i>	
----	---------------	--

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

Note: AMD devices and some Nvidia GPUS do not support shared cache banking, and the hint is ignored on those architectures.

**4.4.2.9 hipDeviceSynchronize()**

```
hipError_t hipDeviceSynchronize (
    void )
```

Waits on all active streams on current device.

When this command is invoked, the host thread gets blocked until all the commands associated with streams associated with the device. HIP does not support multiple blocking modes (yet!).

**Returns**

#hipSuccess

**See also**

[hipSetDevice](#), [hipDeviceReset](#)

**4.4.2.10 hipExtGetLinkTypeAndHopCount()**

```
hipError_t hipExtGetLinkTypeAndHopCount (
    int device1,
    int device2,
    uint32_t * linktype,
    uint32_t * hopcount )
```

Returns the link type and hop count between two devices.

**Parameters**

in	<i>device1</i>	Ordinal for device1
in	<i>device2</i>	Ordinal for device2
out	<i>linktype</i>	Returns the link type (See <code>hsa_amd_link_info_type_t</code> ) between the two devices
out	<i>hopcount</i>	Returns the hop count between the two devices

Queries and returns the HSA link type and the hop count between the two specified devices.

**Returns**

#hipSuccess, #hipInvalidDevice, #hipErrorRuntimeOther

#### 4.4.2.11 hipGetDevice()

```
hipError_t hipGetDevice (
    int * deviceId )
```

Return the default device id for the calling host thread.

##### Parameters

out	<i>device</i>	*device is written with the default device
-----	---------------	--

HIP maintains an default device for each thread using thread-local-storage. This device is used implicitly for HIP runtime APIs called by this thread. `hipGetDevice` returns in `*device` the default device for the calling host thread.

##### Returns

`#hipSuccess`, `#hipErrorInvalidDevice`, `#hipErrorInvalidValue`

##### See also

[hipSetDevice](#), `hipGetDevicesizeBytes`

#### 4.4.2.12 hipGetDeviceCount()

```
hipError_t hipGetDeviceCount (
    int * count )
```

Return number of compute-capable devices.

##### Parameters

<i>[output]</i>	count Returns number of compute-capable devices.
-----------------	--

##### Returns

`#hipSuccess`, `#hipErrorNoDevice`

Returns in `*count` the number of devices that have ability to run compute commands. If there are no such devices, then [hipGetDeviceCount](#) will return `#hipErrorNoDevice`. If 1 or more devices can be found, then `hipGetDeviceCount` returns `#hipSuccess`.

#### 4.4.2.13 hipGetDeviceFlags()

```
hipError_t hipGetDeviceFlags (
    unsigned int * flags )
```

Gets the flags set for current device.

## Parameters

out	<i>flags</i>	
-----	--------------	--

## Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

**4.4.2.14 hipGetDeviceProperties()**

```
hipError_t hipGetDeviceProperties (
    hipDeviceProp_t * prop,
    int deviceId )
```

Returns device properties.

## Parameters

out	<i>prop</i>	written with device properties
in	<i>deviceId</i>	which device to query for information

## Returns

#hipSuccess, #hipErrorInvalidDevice

Populates hipGetDeviceProperties with information for the specified device.

**4.4.2.15 hipIpcCloseMemHandle()**

```
hipError_t hipIpcCloseMemHandle (
    void * devPtr )
```

Close memory mapped with hipIpcOpenMemHandle.

Unmaps memory returned by hipIpcOpenMemHandle. The original allocation in the exporting process as well as imported mappings in other processes will be unaffected.

Any resources used to enable peer access will be freed if this is the last mapping using them.

## Parameters

<i>devPtr</i>	- Device pointer returned by hipIpcOpenMemHandle
---------------	--

**Returns**

hipSuccess, hipErrorMapFailed, hipErrorInvalidHandle,

**4.4.2.16 hipIpcGetEventHandle()**

```
hipError_t hipIpcGetEventHandle (
    hipIpcEventHandle_t * handle,
    hipEvent_t event )
```

Gets an opaque interprocess handle for an event.

This opaque handle may be copied into other processes and opened with `cudaIpcOpenEventHandle`. Then `cudaEventRecord`, `cudaEventSynchronize`, `cudaStreamWaitEvent` and `cudaEventQuery` may be used in either process. Operations on the imported event after the exported event has been freed with `hipEventDestroy` will result in undefined behavior.

**Parameters**

out	<i>handle</i>	Pointer to <code>cudaIpcEventHandle</code> to return the opaque event handle
in	<i>event</i>	Event allocated with <code>cudaEventInterprocess</code> and <code>cudaEventDisableTiming</code> flags

**Returns**

#hipSuccess, #hipErrorInvalidConfiguration, #hipErrorInvalidValue

**4.4.2.17 hipIpcGetMemHandle()**

```
hipError_t hipIpcGetMemHandle (
    hipIpcMemHandle_t * handle,
    void * devPtr )
```

Gets an interprocess memory handle for an existing device memory allocation.

Takes a pointer to the base of an existing device memory allocation created with `hipMalloc` and exports it for use in another process. This is a lightweight operation and may be called multiple times on an allocation without adverse effects.

If a region of memory is freed with `hipFree` and a subsequent call to `hipMalloc` returns memory with the same device address, `hipIpcGetMemHandle` will return a unique handle for the new memory.

**Parameters**

<i>handle</i>	- Pointer to user allocated <code>hipIpcMemHandle</code> to return the handle in.
<i>devPtr</i>	- Base pointer to previously allocated device memory

**Returns**

hipSuccess, hipErrorInvalidHandle, hipErrorOutOfMemory, hipErrorMapFailed,

**4.4.2.18 hipIpcOpenEventHandle()**

```
hipError_t hipIpcOpenEventHandle (
    hipEvent_t * event,
    hipIpcEventHandle_t handle )
```

Opens an interprocess event handles.

Opens an interprocess event handle exported from another process with `cudaIpcGetEventHandle`. The returned `hipEvent_t` behaves like a locally created event with the `hipEventDisableTiming` flag specified. This event need be freed with `hipEventDestroy`. Operations on the imported event after the exported event has been freed with `hipEventDestroy` will result in undefined behavior. If the function is called within the same process where handle is returned by `hipIpcGetEventHandle`, it will return `hipErrorInvalidContext`.

**Parameters**

out	<i>event</i>	Pointer to <code>hipEvent_t</code> to return the event
in	<i>handle</i>	The opaque interprocess handle to open

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidContext

**4.4.2.19 hipIpcOpenMemHandle()**

```
hipError_t hipIpcOpenMemHandle (
    void ** devPtr,
    hipIpcMemHandle_t handle,
    unsigned int flags )
```

Opens an interprocess memory handle exported from another process and returns a device pointer usable in the local process.

Maps memory exported from another process with `hipIpcGetMemHandle` into the current device address space. For contexts on different devices `hipIpcOpenMemHandle` can attempt to enable peer access between the devices as if the user called `hipDeviceEnablePeerAccess`. This behavior is controlled by the `hipIpcMemLazyEnablePeerAccess` flag. `hipDeviceCanAccessPeer` can determine if a mapping is possible.

Contexts that may open `hipIpcMemHandles` are restricted in the following way. `hipIpcMemHandles` from each device in a given process may only be opened by one context per device per other process.

Memory returned from `hipIpcOpenMemHandle` must be freed with `hipIpcCloseMemHandle`.

Calling `hipFree` on an exported memory region before calling `hipIpcCloseMemHandle` in the importing context will result in undefined behavior.

## Parameters

<i>devPtr</i>	- Returned device pointer
<i>handle</i>	- <code>hipIpcMemHandle</code> to open
<i>flags</i>	- Flags for this operation. Must be specified as <code>hipIpcMemLazyEnablePeerAccess</code>

## Returns

`hipSuccess`, `hipErrorMapFailed`, `hipErrorInvalidHandle`, `hipErrorTooManyPeers`

## Note

No guarantees are made about the address returned in `*devPtr`. In particular, multiple processes may not receive the same address for the same `handle`.

**4.4.2.20 `hipSetDevice()`**

```
hipError_t hipSetDevice (
    int deviceId )
```

Set default device to be used for subsequent hip API calls from this thread.

## Parameters

in	<i>deviceId</i>	Valid device in range 0... <a href="#">hipGetDeviceCount()</a> .
----	-----------------	--

Sets `device` as the default device for the calling host thread. Valid device id's are 0... ([hipGetDeviceCount\(\)](#)-1).

Many HIP APIs implicitly use the "default device" :

- Any device memory subsequently allocated from this host thread (using `hipMalloc`) will be allocated on device.
- Any streams or events created from this host thread will be associated with device.
- Any kernels launched from this host thread (using `hipLaunchKernel`) will be executed on device (unless a specific stream is specified, in which case the device associated with that stream will be used).

This function may be called from any host thread. Multiple host threads may use the same device. This function does no synchronization with the previous or new device, and has very little runtime overhead. Applications can use `hipSetDevice` to quickly switch the default device before making a HIP runtime call which uses the default device.

The default device is stored in thread-local-storage for each thread. Thread-pool implementations may inherit the default device of the previous thread. A good practice is to always call `hipSetDevice` at the start of HIP coding sequecy to establish a known standard device.

## Returns

`#hipSuccess`, `#hipErrorInvalidDevice`, `#hipErrorDeviceAlreadyInUse`

## See also

[hipGetDevice](#), [hipGetDeviceCount](#)



#### 4.4.2.21 hipSetDeviceFlags()

```
hipError_t hipSetDeviceFlags (
    unsigned flags )
```

The current device behavior is changed according the flags passed.

##### Parameters

in	flags	
		The schedule flags impact how HIP waits for the completion of a command running on a device. hipDeviceScheduleSpin : HIP runtime will actively spin in the thread which submitted the work until the command completes. This offers the lowest latency, but will consume a CPU core and may increase power. hipDeviceScheduleYield : The HIP runtime will yield the CPU to system so that other tasks can use it. This may increase latency to detect the completion but will consume less power and is friendlier to other tasks in the system. hipDeviceScheduleBlockingSync : On ROCm platform, this is a synonym for hipDeviceScheduleYield. hipDeviceScheduleAuto : Use a heuristic to select between Spin and Yield modes. If the number of HIP contexts is greater than the number of logical processors in the system, use Spin scheduling. Else use Yield scheduling.

hipDeviceMapHost : Allow mapping host memory. On ROCM, this is always allowed and the flag is ignored.   
hipDeviceLmemResizeToMax :

##### Warning

ROCM silently ignores this flag.

##### Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorSetOnActiveProcess

## 4.5 Execution Control

### Functions

- hipError\_t [hipFuncSetAttribute](#) (const void \*func, [hipFuncAttribute](#) attr, int value)  
*Set attribute for a specific function.*
- hipError\_t [hipFuncSetCacheConfig](#) (const void \*func, [hipFuncCache\\_t](#) config)  
*Set Cache configuration for a specific function.*
- hipError\_t [hipFuncSetSharedMemConfig](#) (const void \*func, [hipSharedMemConfig](#) config)  
*Set shared memory configuration for a specific function.*

### 4.5.1 Detailed Description

This section describes the execution control functions of HIP runtime API.

### 4.5.2 Function Documentation

#### 4.5.2.1 hipFuncSetAttribute()

```
hipError_t hipFuncSetAttribute (
    const void * func,
    hipFuncAttribute attr,
    int value )
```

Set attribute for a specific function.

#### Parameters

in	<i>func;</i>	
in	<i>attr;</i>	
in	<i>value;</i>	

#### Returns

#hipSuccess, #hipErrorInvalidDeviceFunction, #hipErrorInvalidValue

Note: AMD devices and some Nvidia GPUS do not support shared cache banking, and the hint is ignored on those architectures.

#### 4.5.2.2 hipFuncSetCacheConfig()

```
hipError_t hipFuncSetCacheConfig (
    const void * func,
    hipFuncCache\_t config )
```

Set Cache configuration for a specific function.

## Parameters

in	<i>config</i> ;	
----	-----------------	--

## Returns

#hipSuccess, #hipErrorNotInitialized Note: AMD devices and some Nvidia GPUS do not support reconfigurable cache. This hint is ignored on those architectures.

### 4.5.2.3 hipFuncSetSharedMemConfig()

```
hipError_t hipFuncSetSharedMemConfig (
    const void * func,
    hipSharedMemConfig config )
```

Set shared memory configuration for a specific function.

## Parameters

in	<i>func</i>	
in	<i>config</i>	

## Returns

#hipSuccess, #hipErrorInvalidDeviceFunction, #hipErrorInvalidValue

Note: AMD devices and some Nvidia GPUS do not support shared cache banking, and the hint is ignored on those architectures.

## 4.6 Error Handling

### Functions

- `hipError_t hipGetLastError` (void)  
*Return last error returned by any HIP runtime API call and resets the stored error code to #hipSuccess.*
- `hipError_t hipPeekAtLastError` (void)  
*Return last error returned by any HIP runtime API call.*
- `const char * hipGetErrorName` (hipError\_t hip\_error)  
*Return name of the specified error code in text form.*
- `const char * hipGetErrorString` (hipError\_t hipError)  
*Return handy text string message to explain the error which occurred.*

### 4.6.1 Detailed Description

---

This section describes the error handling functions of HIP runtime API.

### 4.6.2 Function Documentation

#### 4.6.2.1 hipGetErrorName()

```
const char* hipGetErrorName (
    hipError_t hip_error )
```

Return name of the specified error code in text form.

##### Parameters

<code>hip_error</code>	Error code to convert to name.
------------------------	--------------------------------

##### Returns

const char pointer to the NULL-terminated error name

##### See also

[hipGetErrorString](#), [hipGetLastError](#), [hipPeakAtLastError](#), [hipError\\_t](#)

#### 4.6.2.2 hipGetErrorString()

```
const char* hipGetErrorString (
    hipError_t hipError )
```

Return handy text string message to explain the error which occurred.

##### Parameters

<code>hipError</code>	Error code to convert to string.
-----------------------	----------------------------------

##### Returns

const char pointer to the NULL-terminated error string

**Warning**

: on HCC, this function returns the name of the error (same as `hipGetErrorName`)

**See also**

[hipGetErrorName](#), [hipGetLastError](#), [hipPeakAtLastError](#), [hipError\\_t](#)

**4.6.2.3 hipGetLastError()**

```
hipError_t hipGetLastError (
    void )
```

Return last error returned by any HIP runtime API call and resets the stored error code to `#hipSuccess`.

**Returns**

return code from last HIP called from the active host thread

Returns the last error that has been returned by any of the runtime calls in the same host thread, and then resets the saved error to `#hipSuccess`.

**See also**

[hipGetErrorString](#), [hipGetLastError](#), [hipPeakAtLastError](#), [hipError\\_t](#)

**4.6.2.4 hipPeekAtLastError()**

```
hipError_t hipPeekAtLastError (
    void )
```

Return last error returned by any HIP runtime API call.

**Returns**

`#hipSuccess`

Returns the last error that has been returned by any of the runtime calls in the same host thread. Unlike `hipGetLastError`, this function does not reset the saved error code.

**See also**

[hipGetErrorString](#), [hipGetLastError](#), [hipPeakAtLastError](#), [hipError\\_t](#)

## 4.7 Stream Management

### Typedefs

- typedef void(\* [hipStreamCallback\\_t](#)) (hipStream\_t stream, hipError\_t status, void \*userData)

### Functions

- hipError\_t [hipStreamCreate](#) (hipStream\_t \*stream)  
*Create an asynchronous stream.*
- hipError\_t [hipStreamCreateWithFlags](#) (hipStream\_t \*stream, unsigned int flags)  
*Create an asynchronous stream.*
- hipError\_t [hipStreamCreateWithPriority](#) (hipStream\_t \*stream, unsigned int flags, int priority)  
*Create an asynchronous stream with the specified priority.*
- hipError\_t [hipDeviceGetStreamPriorityRange](#) (int \*leastPriority, int \*greatestPriority)  
*Returns numerical values that correspond to the least and greatest stream priority.*
- hipError\_t [hipStreamDestroy](#) (hipStream\_t stream)  
*Destroys the specified stream.*
- hipError\_t [hipStreamQuery](#) (hipStream\_t stream)  
*Return #hipSuccess if all of the operations in the specified stream have completed, or #hipErrorNotReady if not.*
- hipError\_t [hipStreamSynchronize](#) (hipStream\_t stream)  
*Wait for all commands in stream to complete.*
- hipError\_t [hipStreamWaitEvent](#) (hipStream\_t stream, hipEvent\_t event, unsigned int flags)  
*Make the specified compute stream wait for an event.*
- hipError\_t [hipStreamGetFlags](#) (hipStream\_t stream, unsigned int \*flags)  
*Return flags associated with this stream.*
- hipError\_t [hipStreamGetPriority](#) (hipStream\_t stream, int \*priority)  
*Query the priority of a stream.*
- hipError\_t [hipExtStreamCreateWithCUMask](#) (hipStream\_t \*stream, uint32\_t cuMaskSize, const uint32\_t \*cuMask)  
*Create an asynchronous stream with the specified CU mask.*
- hipError\_t [hipExtStreamGetCUMask](#) (hipStream\_t stream, uint32\_t cuMaskSize, uint32\_t \*cuMask)  
*Get CU mask associated with an asynchronous stream.*
- hipError\_t [hipStreamAddCallback](#) (hipStream\_t stream, [hipStreamCallback\\_t](#) callback, void \*userData, unsigned int flags)  
*Adds a callback to be called on the host after all currently enqueued items in the stream have completed. For each hipStreamAddCallback call, a callback will be executed exactly once. The callback will block later work in the stream until it is finished.*
- hipError\_t [hipStreamWaitValue32](#) (hipStream\_t stream, void \*ptr, uint32\_t value, unsigned int flags, uint32\_t mask \_\_dparm(0xFFFFFFFF))  
*Enqueues a wait command to the stream.[BETA].*
- hipError\_t [hipStreamWaitValue64](#) (hipStream\_t stream, void \*ptr, uint64\_t value, unsigned int flags, uint64\_t mask \_\_dparm(0xFFFFFFFFFFFFFFFF))  
*Enqueues a wait command to the stream.[BETA].*
- hipError\_t [hipStreamWriteValue32](#) (hipStream\_t stream, void \*ptr, uint32\_t value, unsigned int flags)  
*Enqueues a write command to the stream.[BETA].*
- hipError\_t [hipStreamWriteValue64](#) (hipStream\_t stream, void \*ptr, uint64\_t value, unsigned int flags)  
*Enqueues a write command to the stream.[BETA].*

### 4.7.1 Detailed Description

This section describes the stream management functions of HIP runtime API. The following Stream APIs are not (yet) supported in HIP:

- hipStreamAttachMemAsync is a nop

This section describes Stream Memory Wait and Write functions of HIP runtime API.

## 4.7.2 Typedef Documentation

### 4.7.2.1 hipStreamCallback\_t

```
typedef void(* hipStreamCallback_t) (hipStream_t stream, hipError_t status, void *userData)
```

Stream CallBack struct

## 4.7.3 Function Documentation

### 4.7.3.1 hipDeviceGetStreamPriorityRange()

```
hipError_t hipDeviceGetStreamPriorityRange (
    int * leastPriority,
    int * greatestPriority )
```

Returns numerical values that correspond to the least and greatest stream priority.

#### Parameters

in, out	<i>leastPriority</i>	pointer in which value corresponding to least priority is returned.
in, out	<i>greatestPriority</i>	pointer in which value corresponding to greatest priority is returned.

Returns in *\*leastPriority* and *\*greatestPriority* the numerical values that correspond to the least and greatest stream priority respectively. Stream priorities follow a convention where lower numbers imply greater priorities. The range of meaningful stream priorities is given by [*\*greatestPriority*, *\*leastPriority*]. If the user attempts to create a stream with a priority value that is outside the the meaningful range as specified by this API, the priority is automatically clamped to within the valid range.

### 4.7.3.2 hipExtStreamCreateWithCUMask()

```
hipError_t hipExtStreamCreateWithCUMask (
    hipStream_t * stream,
    uint32_t cuMaskSize,
    const uint32_t * cuMask )
```

Create an asynchronous stream with the specified CU mask.

#### Parameters

in, out	<i>stream</i>	Pointer to new stream
in	<i>cuMaskSize</i>	Size of CU mask bit array passed in.
in	<i>cuMask</i>	Bit-vector representing the CU mask. Each active bit represents using one CU. The first 32 bits represent the first 32 CUs, and so on. If its size is greater than physical CU number (i.e., <code>multiProcessorCount</code> member of <a href="#">hipDeviceProp_t</a> ), the extra elements are ignored. It is user's responsibility to make sure the input is meaningful.

#### Returns

#hipSuccess, #hipErrorInvalidHandle, #hipErrorInvalidValue

Create a new asynchronous stream with the specified CU mask. *stream* returns an opaque handle that can be used to reference the newly created stream in subsequent `hipStream*` commands. The stream is allocated on the heap and will remain allocated even if the handle goes out-of-scope. To release the memory used by the stream, application must call `hipStreamDestroy`.

See also

[hipStreamCreate](#), [hipStreamSynchronize](#), [hipStreamWaitEvent](#), [hipStreamDestroy](#)

#### 4.7.3.3 hipExtStreamGetCUMask()

```
hipError_t hipExtStreamGetCUMask (
    hipStream_t stream,
    uint32_t cuMaskSize,
    uint32_t * cuMask )
```

Get CU mask associated with an asynchronous stream.

Parameters

in	<i>stream</i>	stream to be queried
in	<i>cuMaskSize</i>	number of the block of memories (uint32_t *) allocated by user
out	<i>cuMask</i>	Pointer to a pre-allocated block of memories (uint32_t *) in which the stream's CU mask is returned. The CU mask is returned in a chunk of 32 bits where each active bit represents one active CU

Returns

#hipSuccess, #hipErrorInvalidHandle, #hipErrorInvalidValue

See also

[hipStreamCreate](#), [hipStreamSynchronize](#), [hipStreamWaitEvent](#), [hipStreamDestroy](#)

#### 4.7.3.4 hipStreamAddCallback()

```
hipError_t hipStreamAddCallback (
    hipStream_t stream,
    hipStreamCallback_t callback,
    void * userData,
    unsigned int flags )
```

Adds a callback to be called on the host after all currently enqueued items in the stream have completed. For each `hipStreamAddCallback` call, a callback will be executed exactly once. The callback will block later work in the stream until it is finished.

Parameters

in	<i>stream</i>	- Stream to add callback to
in	<i>callback</i>	- The function to call once preceding stream operations are complete
in	<i>userData</i>	- User specified data to be passed to the callback function
in	<i>flags</i>	- Reserved for future use, must be 0

Returns

#hipSuccess, #hipErrorInvalidHandle, #hipErrorNotSupported

See also

[hipStreamCreate](#), [hipStreamCreateWithFlags](#), [hipStreamQuery](#), [hipStreamSynchronize](#), [hipStreamWaitEvent](#), [hipStreamDestroy](#), [hipStreamCreateWithPriority](#)



#### 4.7.3.5 hipStreamCreate()

```
hipError_t hipStreamCreate (
    hipStream_t * stream )
```

Create an asynchronous stream.

##### Parameters

in, out	<i>stream</i>	Valid pointer to hipStream_t. This function writes the memory with the newly created stream.
---------	---------------	--

##### Returns

#hipSuccess, #hipErrorInvalidValue

Create a new asynchronous stream. *stream* returns an opaque handle that can be used to reference the newly created stream in subsequent hipStream\* commands. The stream is allocated on the heap and will remain allocated even if the handle goes out-of-scope. To release the memory used by the stream, applicaiton must call hipStream↵ Destroy.

##### Returns

#hipSuccess, #hipErrorInvalidValue

##### See also

[hipStreamCreateWithFlags](#), [hipStreamCreateWithPriority](#), [hipStreamSynchronize](#), [hipStreamWaitEvent](#), [hipStreamDestroy](#)

#### 4.7.3.6 hipStreamCreateWithFlags()

```
hipError_t hipStreamCreateWithFlags (
    hipStream_t * stream,
    unsigned int flags )
```

Create an asynchronous stream.

##### Parameters

in, out	<i>stream</i>	Pointer to new stream
in	<i>flags</i>	to control stream creation.

##### Returns

#hipSuccess, #hipErrorInvalidValue

Create a new asynchronous stream. *stream* returns an opaque handle that can be used to reference the newly created stream in subsequent hipStream\* commands. The stream is allocated on the heap and will remain allocated even if the handle goes out-of-scope. To release the memory used by the stream, applicaiton must call hipStream↵ Destroy. Flags controls behavior of the stream. See [hipStreamDefault](#), [hipStreamNonBlocking](#).

##### See also

[hipStreamCreate](#), [hipStreamCreateWithPriority](#), [hipStreamSynchronize](#), [hipStreamWaitEvent](#), [hipStreamDestroy](#)

#### 4.7.3.7 hipStreamCreateWithPriority()

```
hipError_t hipStreamCreateWithPriority (
    hipStream_t * stream,
```

```
    unsigned int flags,
    int priority )
```

Create an asynchronous stream with the specified priority.

#### Parameters

<i>in, out</i>	<i>stream</i>	Pointer to new stream
<i>in</i>	<i>flags</i>	to control stream creation.
<i>in</i>	<i>priority</i>	of the stream. Lower numbers represent higher priorities.

#### Returns

#hipSuccess, #hipErrorInvalidValue

Create a new asynchronous stream with the specified priority. *stream* returns an opaque handle that can be used to reference the newly created stream in subsequent *hipStream\** commands. The stream is allocated on the heap and will remain allocated even if the handle goes out-of-scope. To release the memory used by the stream, applicaiton must call *hipStreamDestroy*. Flags controls behavior of the stream. See [hipStreamDefault](#), [hipStreamNonBlocking](#).

#### See also

[hipStreamCreate](#), [hipStreamSynchronize](#), [hipStreamWaitEvent](#), [hipStreamDestroy](#)

### 4.7.3.8 hipStreamDestroy()

```
hipError_t hipStreamDestroy (
    hipStream_t stream )
```

Destroys the specified stream.

#### Parameters

<i>in, out</i>	<i>stream</i>	Valid pointer to <i>hipStream_t</i> . This function writes the memory with the newly created stream.
----------------	---------------	--

#### Returns

#hipSuccess #hipErrorInvalidHandle

Destroys the specified stream.

If commands are still executing on the specified stream, some may complete execution before the queue is deleted. The queue may be destroyed while some commands are still inflight, or may wait for all commands queued to the stream before destroying it.

#### See also

[hipStreamCreate](#), [hipStreamCreateWithFlags](#), [hipStreamCreateWithPriority](#), [hipStreamQuery](#), [hipStreamWaitEvent](#), [hipStreamSynchronize](#)

### 4.7.3.9 hipStreamGetFlags()

```
hipError_t hipStreamGetFlags (
    hipStream_t stream,
    unsigned int * flags )
```

Return flags associated with this stream.

## Parameters

in	<i>stream</i>	stream to be queried
in, out	<i>flags</i>	Pointer to an unsigned integer in which the stream's flags are returned

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidHandle

#hipSuccess #hipErrorInvalidValue #hipErrorInvalidHandle

Return flags associated with this stream in \*flags.

## See also

[hipStreamCreateWithFlags](#)

**4.7.3.10 hipStreamGetPriority()**

```
hipError_t hipStreamGetPriority (
    hipStream_t stream,
    int * priority )
```

Query the priority of a stream.

## Parameters

in	<i>stream</i>	stream to be queried
in, out	<i>priority</i>	Pointer to an unsigned integer in which the stream's priority is returned

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidHandle

#hipSuccess #hipErrorInvalidValue #hipErrorInvalidHandle

Query the priority of a stream. The priority is returned in in priority.

## See also

[hipStreamCreateWithFlags](#)

**4.7.3.11 hipStreamQuery()**

```
hipError_t hipStreamQuery (
    hipStream_t stream )
```

Return #hipSuccess if all of the operations in the specified *stream* have completed, or #hipErrorNotReady if not.

## Parameters

in	<i>stream</i>	stream to query
----	---------------	-----------------

## Returns

#hipSuccess, #hipErrorNotReady, #hipErrorInvalidHandle

This is thread-safe and returns a snapshot of the current state of the queue. However, if other host threads are sending work to the stream, the status may change immediately after the function is called. It is typically used for debug.

See also

[hipStreamCreate](#), [hipStreamCreateWithFlags](#), [hipStreamCreateWithPriority](#), [hipStreamWaitEvent](#), [hipStreamSynchronize](#), [hipStreamDestroy](#)

#### 4.7.3.12 hipStreamSynchronize()

```
hipError_t hipStreamSynchronize (
    hipStream_t stream )
```

Wait for all commands in stream to complete.

Parameters

in	<i>stream</i>	stream identifier.
----	---------------	--------------------

Returns

#hipSuccess, #hipErrorInvalidHandle

This command is host-synchronous : the host will block until the specified stream is empty.

This command follows standard null-stream semantics. Specifically, specifying the null stream will cause the command to wait for other streams on the same device to complete all pending operations.

This command honors the `hipDeviceLaunchBlocking` flag, which controls whether the wait is active or blocking.

See also

[hipStreamCreate](#), [hipStreamCreateWithFlags](#), [hipStreamCreateWithPriority](#), [hipStreamWaitEvent](#), [hipStreamDestroy](#)

#### 4.7.3.13 hipStreamWaitEvent()

```
hipError_t hipStreamWaitEvent (
    hipStream_t stream,
    hipEvent_t event,
    unsigned int flags )
```

Make the specified compute stream wait for an event.

Parameters

in	<i>stream</i>	stream to make wait.
in	<i>event</i>	event to wait on
in	<i>flags</i>	control operation [must be 0]

Returns

#hipSuccess, #hipErrorInvalidHandle

This function inserts a wait operation into the specified stream. All future work submitted to `stream` will wait until `event` reports completion before beginning execution.

This function only waits for commands in the current stream to complete. Notably, this function does not implicitly wait for commands in the default stream to complete, even if the specified stream is created with `hipStreamNonBlocking = 0`.

See also

[hipStreamCreate](#), [hipStreamCreateWithFlags](#), [hipStreamCreateWithPriority](#), [hipStreamSynchronize](#), [hipStreamDestroy](#)

**4.7.3.14 hipStreamWaitValue32()**

```
hipError_t hipStreamWaitValue32 (
    hipStream_t stream,
    void * ptr,
    uint32_t value,
    unsigned int flags,
    uint32_t mask __dparm(0xFFFFFFFF) )
```

Enqueues a wait command to the stream.[BETA].

**Parameters**

in	<i>stream</i>	- Stream identifier
in	<i>ptr</i>	- Pointer to memory object allocated using 'hipMallocSignalMemory' flag
in	<i>value</i>	- Value to be used in compare operation
in	<i>flags</i>	- Defines the compare operation, supported values are hipStreamWaitValueGte hipStreamWaitValueEq, hipStreamWaitValueAnd and hipStreamWaitValueNor
in	<i>mask</i>	- Mask to be applied on value at memory before it is compared with value, default value is set to enable every bit

**Returns**

#hipSuccess, #hipErrorInvalidValue

Enqueues a wait command to the stream, all operations enqueued on this stream after this, will not execute until the defined wait condition is true.

hipStreamWaitValueGte: waits until \*ptr&mask >= value hipStreamWaitValueEq : waits until \*ptr&mask == value  
 hipStreamWaitValueAnd: waits until ((\*ptr&mask) & value) != 0 hipStreamWaitValueNor: waits until ~((\*ptr&mask) | (value&mask)) != 0

**Note**

when using 'hipStreamWaitValueNor', mask is applied on both 'value' and '\*ptr'.

Support for hipStreamWaitValue32 can be queried using '[hipDeviceGetAttribute\(\)](#)' and 'hipDeviceAttributeCanUseStreamWaitValue' flag.

**Warning**

This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**See also**

[hipExtMallocWithFlags](#), [hipFree](#), [hipStreamWaitValue64](#), [hipStreamWriteValue64](#), [hipStreamWriteValue32](#), [hipDeviceGetAttribute](#)

**4.7.3.15 hipStreamWaitValue64()**

```
hipError_t hipStreamWaitValue64 (
    hipStream_t stream,
    void * ptr,
    uint64_t value,
    unsigned int flags,
    uint64_t mask __dparm(0xFFFFFFFFFFFFFFFF) )
```

Enqueues a wait command to the stream.[BETA].

**Parameters**

in	<i>stream</i>	- Stream identifier
in	<i>ptr</i>	- Pointer to memory object allocated using 'hipMallocSignalMemory' flag
in	<i>value</i>	- Value to be used in compare operation
in	<i>flags</i>	- Defines the compare operation, supported values are hipStreamWaitValueGte hipStreamWaitValueEq, hipStreamWaitValueAnd and hipStreamWaitValueNor.
in	<i>mask</i>	- Mask to be applied on value at memory before it is compared with value default value is set to enable every bit

**Returns**

#hipSuccess, #hipErrorInvalidValue

Enqueues a wait command to the stream, all operations enqueued on this stream after this, will not execute until the defined wait condition is true.

hipStreamWaitValueGte: waits until  $*ptr \& mask \geq value$  hipStreamWaitValueEq : waits until  $*ptr \& mask == value$   
 hipStreamWaitValueAnd: waits until  $((*ptr \& mask) \& value) != 0$  hipStreamWaitValueNor: waits until  $\sim((*ptr \& mask) | (value \& mask)) != 0$

**Note**

when using 'hipStreamWaitValueNor', mask is applied on both 'value' and '\*ptr'.

Support for hipStreamWaitValue64 can be queried using '[hipDeviceGetAttribute\(\)](#)' and 'hipDeviceAttribute↵ CanUseStreamWaitValue' flag.

**Warning**

This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**See also**

[hipExtMallocWithFlags](#), [hipFree](#), [hipStreamWaitValue32](#), [hipStreamWriteValue64](#), [hipStreamWriteValue32](#), [hipDeviceGetAttribute](#)

**4.7.3.16 hipStreamWriteValue32()**

```
hipError_t hipStreamWriteValue32 (
    hipStream_t stream,
    void * ptr,
    uint32_t value,
    unsigned int flags )
```

Enqueues a write command to the stream.[BETA].

**Parameters**

in	<i>stream</i>	- Stream identifier
in	<i>ptr</i>	- Pointer to a GPU accessible memory object
in	<i>value</i>	- Value to be written
in	<i>flags</i>	- reserved, ignored for now, will be used in future releases

**Returns**

#hipSuccess, #hipErrorInvalidValue

Enqueues a write command to the stream, write operation is performed after all earlier commands on this stream have completed the execution.

**Warning**

This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**See also**

[hipExtMallocWithFlags](#), [hipFree](#), [hipStreamWriteValue32](#), [hipStreamWaitValue32](#), [hipStreamWaitValue64](#)

**4.7.3.17 hipStreamWriteValue64()**

```
hipError_t hipStreamWriteValue64 (
    hipStream_t stream,
    void * ptr,
    uint64_t value,
    unsigned int flags )
```

Enqueues a write command to the stream.[BETA].

**Parameters**

in	<i>stream</i>	- Stream identifier
in	<i>ptr</i>	- Pointer to a GPU accessible memory object
in	<i>value</i>	- Value to be written
in	<i>flags</i>	- reserved, ignored for now, will be used in future releases

**Returns**

#hipSuccess, #hipErrorInvalidValue

Enqueues a write command to the stream, write operation is performed after all earlier commands on this stream have completed the execution.

**Warning**

This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**See also**

[hipExtMallocWithFlags](#), [hipFree](#), [hipStreamWriteValue32](#), [hipStreamWaitValue32](#), [hipStreamWaitValue64](#)

## 4.8 Event Management

### Functions

- hipError\_t [hipEventCreateWithFlags](#) (hipEvent\_t \*event, unsigned flags)  
*Create an event with the specified flags.*
- hipError\_t [hipEventCreate](#) (hipEvent\_t \*event)
- hipError\_t [hipEventRecord](#) (hipEvent\_t event, hipStream\_t stream)  
*Record an event in the specified stream.*
- hipError\_t [hipEventDestroy](#) (hipEvent\_t event)  
*Destroy the specified event.*
- hipError\_t [hipEventSynchronize](#) (hipEvent\_t event)  
*Wait for an event to complete.*
- hipError\_t [hipEventElapsedTime](#) (float \*ms, hipEvent\_t start, hipEvent\_t stop)  
*Return the elapsed time between two events.*
- hipError\_t [hipEventQuery](#) (hipEvent\_t event)  
*Query event status.*

### 4.8.1 Detailed Description

---

This section describes the event management functions of HIP runtime API.

### 4.8.2 Function Documentation

#### 4.8.2.1 hipEventCreate()

```
hipError_t hipEventCreate (
    hipEvent_t * event )
```

Create an event

##### Parameters

in, out	<i>event</i>	Returns the newly created event.
---------	--------------	----------------------------------

##### Returns

#hipSuccess, #hipErrorNotInitialized, #hipErrorInvalidValue, #hipErrorLaunchFailure, #hipErrorOutOfMemory

##### See also

[hipEventCreateWithFlags](#), [hipEventRecord](#), [hipEventQuery](#), [hipEventSynchronize](#), [hipEventDestroy](#), [hipEventElapsedTime](#)

#### 4.8.2.2 hipEventCreateWithFlags()

```
hipError_t hipEventCreateWithFlags (
    hipEvent_t * event,
    unsigned flags )
```

Create an event with the specified flags.

##### Parameters

in, out	<i>event</i>	Returns the newly created event.
---------	--------------	----------------------------------



## Parameters

in	flags	Flags to control event behavior. Valid values are <a href="#">hipEventDefault</a> , <a href="#">hipEventBlockingSync</a> , <a href="#">hipEventDisableTiming</a> , <a href="#">hipEventInterprocess</a> <a href="#">hipEventDefault</a> : Default flag. The event will use active synchronization and will support timing. Blocking synchronization provides lowest possible latency at the expense of dedicating a CPU to poll on the event. <a href="#">hipEventBlockingSync</a> : The event will use blocking synchronization : if <a href="#">hipEventSynchronize</a> is called on this event, the thread will block until the event completes. This can increase latency for the synchroniation but can result in lower power and more resources for other CPU threads. <a href="#">hipEventDisableTiming</a> : Disable recording of timing information. Events created with this flag would not record profiling data and provide best performance if used for synchronization.
----	-------	---

## Warning

On AMD platform, [hipEventInterprocess](#) support is under development. Use of this flag will return an error.

## Returns

[#hipSuccess](#), [#hipErrorNotInitialized](#), [#hipErrorInvalidValue](#), [#hipErrorLaunchFailure](#), [#hipErrorOutOfMemory](#)

## See also

[hipEventCreate](#), [hipEventSynchronize](#), [hipEventDestroy](#), [hipEventElapsedTime](#)

4.8.2.3 [hipEventDestroy\(\)](#)

```
hipError_t hipEventDestroy (
    hipEvent_t event )
```

Destroy the specified event.

## Parameters

in	<i>event</i>	Event to destroy.
----	--------------	-------------------

## Returns

[#hipSuccess](#), [#hipErrorNotInitialized](#), [#hipErrorInvalidValue](#), [#hipErrorLaunchFailure](#)

Releases memory associated with the event. If the event is recording but has not completed recording when [hipEventDestroy\(\)](#) is called, the function will return immediately and the completion\_future resources will be released later, when the [hipDevice](#) is synchronized.

## See also

[hipEventCreate](#), [hipEventCreateWithFlags](#), [hipEventQuery](#), [hipEventSynchronize](#), [hipEventRecord](#), [hipEventElapsedTime](#)

## Returns

[#hipSuccess](#)

4.8.2.4 [hipEventElapsedTime\(\)](#)

```
hipError_t hipEventElapsedTime (
    float * ms,
```

```
hipEvent_t start,
hipEvent_t stop )
```

Return the elapsed time between two events.

#### Parameters

out	ms	: Return time between start and stop in ms.
in	start	: Start event.
in	stop	: Stop event.

#### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotReady, #hipErrorInvalidHandle, #hipErrorNotInitialized, #hipErrorLaunchFailure

Computes the elapsed time between two events. Time is computed in ms, with a resolution of approximately 1 us. Events which are recorded in a NULL stream will block until all commands on all other streams complete execution, and then record the timestamp.

Events which are recorded in a non-NULL stream will record their timestamp when they reach the head of the specified stream, after all previous commands in that stream have completed executing. Thus the time that the event recorded may be significantly after the host calls [hipEventRecord\(\)](#).

If [hipEventRecord\(\)](#) has not been called on either event, then #hipErrorInvalidHandle is returned. If [hipEventRecord\(\)](#) has been called on both events, but the timestamp has not yet been recorded on one or both events (that is, [hipEventQuery\(\)](#) would return #hipErrorNotReady on at least one of the events), then #hipErrorNotReady is returned.

Note, for HIP Events used in kernel dispatch using hipExtLaunchKernelGGL/hipExtLaunchKernel, events passed in hipExtLaunchKernelGGL/hipExtLaunchKernel are not explicitly recorded and should only be used to get elapsed time for that specific launch. In case events are used across multiple dispatches, for example, start and stop events from different hipExtLaunchKernelGGL/hipExtLaunchKernel calls, they will be treated as invalid unrecorded events, HIP will throw error "hipErrorInvalidHandle" from hipEventElapsedTime.

#### See also

[hipEventCreate](#), [hipEventCreateWithFlags](#), [hipEventQuery](#), [hipEventDestroy](#), [hipEventRecord](#), [hipEventSynchronize](#)

### 4.8.2.5 hipEventQuery()

```
hipError_t hipEventQuery (
    hipEvent_t event )
```

Query event status.

#### Parameters

in	event	Event to query.
----	-------	-----------------

#### Returns

#hipSuccess, #hipErrorNotReady, #hipErrorInvalidHandle, #hipErrorInvalidValue, #hipErrorNotInitialized, #hipErrorLaunchFailure

Query the status of the specified event. This function will return #hipErrorNotReady if all commands in the appropriate stream (specified to [hipEventRecord\(\)](#)) have completed. If that work has not completed, or if [hipEventRecord\(\)](#) was not called on the event, then #hipSuccess is returned.

#### See also

[hipEventCreate](#), [hipEventCreateWithFlags](#), [hipEventRecord](#), [hipEventDestroy](#), [hipEventSynchronize](#), [hipEventElapsedTime](#)

#### 4.8.2.6 hipEventRecord()

```
hipError_t hipEventRecord (
    hipEvent_t event,
    hipStream_t stream )
```

Record an event in the specified stream.

##### Parameters

in	<i>event</i>	event to record.
in	<i>stream</i>	stream in which to record event.

##### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized, #hipErrorInvalidHandle, #hipErrorLaunchFailure

[hipEventQuery\(\)](#) or [hipEventSynchronize\(\)](#) must be used to determine when the event transitions from "recording" (after [hipEventRecord\(\)](#) is called) to "recorded" (when timestamps are set, if requested).

Events which are recorded in a non-NULL stream will transition to from recording to "recorded" state when they reach the head of the specified stream, after all previous commands in that stream have completed executing.

If [hipEventRecord\(\)](#) has been previously called on this event, then this call will overwrite any existing state in event. If this function is called on an event that is currently being recorded, results are undefined

- either outstanding recording may save state into the event, and the order is not guaranteed.

##### See also

[hipEventCreate](#), [hipEventCreateWithFlags](#), [hipEventQuery](#), [hipEventSynchronize](#), [hipEventDestroy](#), [hipEventElapsedTime](#)

#### 4.8.2.7 hipEventSynchronize()

```
hipError_t hipEventSynchronize (
    hipEvent_t event )
```

Wait for an event to complete.

This function will block until the event is ready, waiting for all previous work in the stream specified when event was recorded with [hipEventRecord\(\)](#).

If [hipEventRecord\(\)](#) has not been called on *event*, this function returns immediately.

TODO-hip- This function needs to support [hipEventBlockingSync](#) parameter.

##### Parameters

in	<i>event</i>	Event on which to wait.
----	--------------	-------------------------

##### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized, #hipErrorInvalidHandle, #hipErrorLaunchFailure

##### See also

[hipEventCreate](#), [hipEventCreateWithFlags](#), [hipEventQuery](#), [hipEventDestroy](#), [hipEventRecord](#), [hipEventElapsedTime](#)

## 4.9 Memory Management

### Functions

- `hipError_t hipPointerGetAttributes` (`hipPointerAttribute_t *attributes`, `const void *ptr`)  
*Return attributes for the specified pointer.*
- `hipError_t hipImportExternalSemaphore` (`hipExternalSemaphore_t *extSem_out`, `const hipExternalSemaphoreHandleDesc *semHandleDesc`)  
*Imports an external semaphore.*
- `hipError_t hipSignalExternalSemaphoresAsync` (`const hipExternalSemaphore_t *extSemArray`, `const hipExternalSemaphoreSignalParams *paramsArray`, `unsigned int numExtSems`, `hipStream_t stream`)  
*Signals a set of external semaphore objects.*
- `hipError_t hipWaitExternalSemaphoresAsync` (`const hipExternalSemaphore_t *extSemArray`, `const hipExternalSemaphoreWaitParams *paramsArray`, `unsigned int numExtSems`, `hipStream_t stream`)  
*Waits on a set of external semaphore objects.*
- `hipError_t hipDestroyExternalSemaphore` (`hipExternalSemaphore_t extSem`)  
*Destroys an external semaphore object and releases any references to the underlying resource. Any outstanding signals or waits must have completed before the semaphore is destroyed.*
- `hipError_t hipImportExternalMemory` (`hipExternalMemory_t *extMem_out`, `const hipExternalMemoryHandleDesc *memHandleDesc`)  
*Imports an external memory object.*
- `hipError_t hipExternalMemoryGetMappedBuffer` (`void **devPtr`, `hipExternalMemory_t extMem`, `const hipExternalMemoryBufferDesc *bufferDesc`)  
*Maps a buffer onto an imported memory object.*
- `hipError_t hipDestroyExternalMemory` (`hipExternalMemory_t extMem`)  
*Destroys an external memory object.*
- `hipError_t hipMalloc` (`void **ptr`, `size_t size`)  
*Allocate memory on the default accelerator.*
- `hipError_t hipExtMallocWithFlags` (`void **ptr`, `size_t sizeBytes`, `unsigned int flags`)  
*Allocate memory on the default accelerator.*
- `hipError_t hipMallocHost` (`void **ptr`, `size_t size`)  
*Allocate pinned host memory [Deprecated].*
- `hipError_t hipMemAllocHost` (`void **ptr`, `size_t size`)  
*Allocate pinned host memory [Deprecated].*
- `hipError_t hipHostMalloc` (`void **ptr`, `size_t size`, `unsigned int flags`)  
*Allocate device accessible page locked host memory.*
- `hipError_t hipMallocManaged` (`void **dev_ptr`, `size_t size`, `unsigned int flags` `__dparm(hipMemAttachGlobal)`)  
*Allocates memory that will be automatically managed by HIP.*
- `hipError_t hipMemPrefetchAsync` (`const void *dev_ptr`, `size_t count`, `int device`, `hipStream_t stream` `__↔__dparm(0)`)  
*Prefetches memory to the specified destination device using HIP.*
- `hipError_t hipMemAdvise` (`const void *dev_ptr`, `size_t count`, `hipMemoryAdvise` `advice`, `int device`)  
*Advise about the usage of a given memory range to HIP.*
- `hipError_t hipMemRangeGetAttribute` (`void *data`, `size_t data_size`, `hipMemRangeAttribute` `attribute`, `const void *dev_ptr`, `size_t count`)  
*Query an attribute of a given memory range in HIP.*
- `hipError_t hipMemRangeGetAttributes` (`void **data`, `size_t *data_sizes`, `hipMemRangeAttribute *attributes`, `size_t num_attributes`, `const void *dev_ptr`, `size_t count`)  
*Query attributes of a given memory range in HIP.*
- `hipError_t hipStreamAttachMemAsync` (`hipStream_t stream`, `void *dev_ptr`, `size_t length` `__dparm(0)`, `unsigned int flags` `__dparm(hipMemAttachSingle)`)  
*Attach memory to a stream asynchronously in HIP.*
- `hipError_t hipHostAlloc` (`void **ptr`, `size_t size`, `unsigned int flags`)

- Allocate device accessible page locked host memory [Deprecated].*

  - `hipError_t hipHostGetDevicePointer` (void \*\*devPtr, void \*hstPtr, unsigned int flags)

*Get Device pointer from Host Pointer allocated through hipHostMalloc.*
- `hipError_t hipHostGetFlags` (unsigned int \*flagsPtr, void \*hostPtr)

*Return flags associated with host pointer.*
- `hipError_t hipHostRegister` (void \*hostPtr, size\_t sizeBytes, unsigned int flags)

*Register host memory so it can be accessed from the current device.*
- `hipError_t hipHostUnregister` (void \*hostPtr)

*Un-register host pointer.*
- `hipError_t hipMallocPitch` (void \*\*ptr, size\_t \*pitch, size\_t width, size\_t height)
- `hipError_t hipMemAllocPitch` (hipDeviceptr\_t \*dptr, size\_t \*pitch, size\_t widthInBytes, size\_t height, unsigned int elementSizeBytes)
- `hipError_t hipFree` (void \*ptr)

*Free memory allocated by the hcc hip memory allocation API. This API performs an implicit `hipDeviceSynchronize()` call. If pointer is NULL, the hip runtime is initialized and `hipSuccess` is returned.*
- `hipError_t hipFreeHost` (void \*ptr)

*Free memory allocated by the hcc hip host memory allocation API. [Deprecated].*
- `hipError_t hipHostFree` (void \*ptr)

*Free memory allocated by the hcc hip host memory allocation API This API performs an implicit `hipDeviceSynchronize()` call. If pointer is NULL, the hip runtime is initialized and `hipSuccess` is returned.*
- `hipError_t hipMemcpy` (void \*dst, const void \*src, size\_t sizeBytes, hipMemcpyKind kind)

*Copy data from src to dst.*
- `hipError_t hipMemcpyWithStream` (void \*dst, const void \*src, size\_t sizeBytes, hipMemcpyKind kind, hipStream\_t stream)
- `hipError_t hipMemcpyHtoD` (hipDeviceptr\_t dst, void \*src, size\_t sizeBytes)

*Copy data from Host to Device.*
- `hipError_t hipMemcpyDtoH` (void \*dst, hipDeviceptr\_t src, size\_t sizeBytes)

*Copy data from Device to Host.*
- `hipError_t hipMemcpyDtoD` (hipDeviceptr\_t dst, hipDeviceptr\_t src, size\_t sizeBytes)

*Copy data from Device to Device.*
- `hipError_t hipMemcpyHtoDAsync` (hipDeviceptr\_t dst, void \*src, size\_t sizeBytes, hipStream\_t stream)

*Copy data from Host to Device asynchronously.*
- `hipError_t hipMemcpyDtoHAsync` (void \*dst, hipDeviceptr\_t src, size\_t sizeBytes, hipStream\_t stream)

*Copy data from Device to Host asynchronously.*
- `hipError_t hipMemcpyDtoDAsync` (hipDeviceptr\_t dst, hipDeviceptr\_t src, size\_t sizeBytes, hipStream\_t stream)

*Copy data from Device to Device asynchronously.*
- `hipError_t hipModuleGetGlobal` (hipDeviceptr\_t \*dptr, size\_t \*bytes, hipModule\_t hmod, const char \*name)

*Returns a global pointer from a module. Returns in \*dptr and \*bytes the pointer and size of the global of name name located in module hmod. If no variable of that name exists, it returns `hipErrorNotFound`. Both parameters dptr and bytes are optional. If one of them is NULL, it is ignored and `hipSuccess` is returned.*
- `hipError_t hipGetSymbolAddress` (void \*\*devPtr, const void \*symbol)
- `hipError_t hipGetSymbolSize` (size\_t \*size, const void \*symbol)
- `hipError_t hipMemcpyToSymbol` (const void \*symbol, const void \*src, size\_t sizeBytes, size\_t offset \_\_dparm(0), hipMemcpyKind kind \_\_dparm(hipMemcpyHostToDevice))
- `hipError_t hipMemcpyToSymbolAsync` (const void \*symbol, const void \*src, size\_t sizeBytes, size\_t offset, hipMemcpyKind kind, hipStream\_t stream \_\_dparm(0))
- `hipError_t hipMemcpyFromSymbol` (void \*dst, const void \*symbol, size\_t sizeBytes, size\_t offset \_\_dparm(0), hipMemcpyKind kind \_\_dparm(hipMemcpyDeviceToHost))
- `hipError_t hipMemcpyFromSymbolAsync` (void \*dst, const void \*symbol, size\_t sizeBytes, size\_t offset, hipMemcpyKind kind, hipStream\_t stream \_\_dparm(0))
- `hipError_t hipMemcpyAsync` (void \*dst, const void \*src, size\_t sizeBytes, hipMemcpyKind kind, hipStream\_t stream \_\_dparm(0))

- Copy data from src to dst asynchronously.*
- `hipError_t hipMemset (void *dst, int value, size_t sizeBytes)`  
*Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant byte value value.*
  - `hipError_t hipMemsetD8 (hipDeviceptr_t dest, unsigned char value, size_t count)`  
*Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant byte value value.*
  - `hipError_t hipMemsetD8Async (hipDeviceptr_t dest, unsigned char value, size_t count, hipStream_t stream __dparm(0))`  
*Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant byte value value.*
  - `hipError_t hipMemsetD16 (hipDeviceptr_t dest, unsigned short value, size_t count)`  
*Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant short value value.*
  - `hipError_t hipMemsetD16Async (hipDeviceptr_t dest, unsigned short value, size_t count, hipStream_t stream __dparm(0))`  
*Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant short value value.*
  - `hipError_t hipMemsetD32 (hipDeviceptr_t dest, int value, size_t count)`  
*Fills the memory area pointed to by dest with the constant integer value for specified number of times.*
  - `hipError_t hipMemsetAsync (void *dst, int value, size_t sizeBytes, hipStream_t stream __dparm(0))`  
*Fills the first sizeBytes bytes of the memory area pointed to by dev with the constant byte value value.*
  - `hipError_t hipMemsetD32Async (hipDeviceptr_t dst, int value, size_t count, hipStream_t stream __dparm(0))`  
*Fills the memory area pointed to by dev with the constant integer value for specified number of times.*
  - `hipError_t hipMemset2D (void *dst, size_t pitch, int value, size_t width, size_t height)`  
*Fills the memory area pointed to by dst with the constant value.*
  - `hipError_t hipMemset2DAsync (void *dst, size_t pitch, int value, size_t width, size_t height, hipStream_t stream __dparm(0))`  
*Fills asynchronously the memory area pointed to by dst with the constant value.*
  - `hipError_t hipMemset3D (hipPitchedPtr pitchedDevPtr, int value, hipExtent extent)`  
*Fills synchronously the memory area pointed to by pitchedDevPtr with the constant value.*
  - `hipError_t hipMemset3DAsync (hipPitchedPtr pitchedDevPtr, int value, hipExtent extent, hipStream_t stream __dparm(0))`  
*Fills asynchronously the memory area pointed to by pitchedDevPtr with the constant value.*
  - `hipError_t hipMemGetInfo (size_t *free, size_t *total)`  
*Query memory info. Return snapshot of free memory, and total allocatable memory on the device.*
  - `hipError_t hipMemPtrGetInfo (void *ptr, size_t *size)`
  - `hipError_t hipMallocArray (hipArray **array, const hipChannelFormatDesc *desc, size_t width, size_t height __dparm(0), unsigned int flags __dparm(hipArrayDefault))`  
*Allocate an array on the device.*
  - `hipError_t hipArrayCreate (hipArray **pHandle, const HIP_ARRAY_DESCRIPTOR *pAllocateArray)`
  - `hipError_t hipArrayDestroy (hipArray *array)`
  - `hipError_t hipArray3DCreate (hipArray **array, const HIP_ARRAY3D_DESCRIPTOR *pAllocateArray)`
  - `hipError_t hipMalloc3D (hipPitchedPtr *pitchedDevPtr, hipExtent extent)`
  - `hipError_t hipFreeArray (hipArray *array)`  
*Frees an array on the device.*
  - `hipError_t hipFreeMipmappedArray (hipMipmappedArray_t mipmappedArray)`  
*Frees a mipmapped array on the device.*
  - `hipError_t hipMalloc3DArray (hipArray **array, const struct hipChannelFormatDesc *desc, struct hipExtent extent, unsigned int flags)`  
*Allocate an array on the device.*
  - `hipError_t hipMallocMipmappedArray (hipMipmappedArray_t *mipmappedArray, const struct hipChannelFormatDesc *desc, struct hipExtent extent, unsigned int numLevels, unsigned int flags __dparm(0))`  
*Allocate a mipmapped array on the device.*
  - `hipError_t hipGetMipmappedArrayLevel (hipArray_t *levelArray, hipMipmappedArray_const_t mipmappedArray, unsigned int level)`  
*Gets a mipmap level of a HIP mipmapped array.*

- `hipError_t hipMemcpy2D` (void \*dst, size\_t dpitch, const void \*src, size\_t spitch, size\_t width, size\_t height, hipMemcpyKind kind)  
*Copies data between host and device.*
- `hipError_t hipMemcpyParam2D` (const `hip_Memcpy2D` \*pCopy)  
*Copies memory for 2D arrays.*
- `hipError_t hipMemcpyParam2DAsync` (const `hip_Memcpy2D` \*pCopy, hipStream\_t stream \_\_dparm(0))  
*Copies memory for 2D arrays.*
- `hipError_t hipMemcpy2DAsync` (void \*dst, size\_t dpitch, const void \*src, size\_t spitch, size\_t width, size\_t height, hipMemcpyKind kind, hipStream\_t stream \_\_dparm(0))  
*Copies data between host and device.*
- `hipError_t hipMemcpy2DToArray` (`hipArray` \*dst, size\_t wOffset, size\_t hOffset, const void \*src, size\_t spitch, size\_t width, size\_t height, hipMemcpyKind kind)  
*Copies data between host and device.*
- `hipError_t hipMemcpy2DToArrayAsync` (`hipArray` \*dst, size\_t wOffset, size\_t hOffset, const void \*src, size\_t spitch, size\_t width, size\_t height, hipMemcpyKind kind, hipStream\_t stream \_\_dparm(0))  
*Copies data between host and device.*
- `hipError_t hipMemcpyToArray` (`hipArray` \*dst, size\_t wOffset, size\_t hOffset, const void \*src, size\_t count, hipMemcpyKind kind)  
*Copies data between host and device.*
- `hipError_t hipMemcpyFromArray` (void \*dst, `hipArray_const_t` srcArray, size\_t wOffset, size\_t hOffset, size\_t count, hipMemcpyKind kind)  
*Copies data between host and device.*
- `hipError_t hipMemcpy2DFromArray` (void \*dst, size\_t dpitch, `hipArray_const_t` src, size\_t wOffset, size\_t hOffset, size\_t width, size\_t height, hipMemcpyKind kind)  
*Copies data between host and device.*
- `hipError_t hipMemcpy2DFromArrayAsync` (void \*dst, size\_t dpitch, `hipArray_const_t` src, size\_t wOffset, size\_t hOffset, size\_t width, size\_t height, hipMemcpyKind kind, hipStream\_t stream \_\_dparm(0))  
*Copies data between host and device asynchronously.*
- `hipError_t hipMemcpyAtoH` (void \*dst, `hipArray` \*srcArray, size\_t srcOffset, size\_t count)  
*Copies data between host and device.*
- `hipError_t hipMemcpyHtoA` (`hipArray` \*dstArray, size\_t dstOffset, const void \*srcHost, size\_t count)  
*Copies data between host and device.*
- `hipError_t hipMemcpy3D` (const struct `hipMemcpy3DParms` \*p)  
*Copies data between host and device.*
- `hipError_t hipMemcpy3DAsync` (const struct `hipMemcpy3DParms` \*p, hipStream\_t stream \_\_dparm(0))  
*Copies data between host and device asynchronously.*
- `hipError_t hipDrvMemcpy3D` (const `HIP_MEMCPY3D` \*pCopy)  
*Copies data between host and device.*
- `hipError_t hipDrvMemcpy3DAsync` (const `HIP_MEMCPY3D` \*pCopy, hipStream\_t stream)  
*Copies data between host and device asynchronously.*

### 4.9.1 Detailed Description

This section describes the memory management functions of HIP runtime API. The following CUDA APIs are not currently supported:

- `cudaMalloc3D`
- `cudaMalloc3DArray`
- TODO - more 2D, 3D, array APIs here.

---

This section describes the managed memory management functions of HIP runtime API.

---

## 4.9.2 Function Documentation

### 4.9.2.1 hipDestroyExternalMemory()

```
hipError_t hipDestroyExternalMemory (
    hipExternalMemory_t extMem )
```

Destroys an external memory object.

#### Parameters

in	<i>extMem</i>	External memory object to be destroyed
----	---------------	--

#### Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

#### See also

### 4.9.2.2 hipDestroyExternalSemaphore()

```
hipError_t hipDestroyExternalSemaphore (
    hipExternalSemaphore_t extSem )
```

Destroys an external semaphore object and releases any references to the underlying resource. Any outstanding signals or waits must have completed before the semaphore is destroyed.

#### Parameters

in	<i>extSem</i>	handle to an external memory object
----	---------------	-------------------------------------

#### Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

#### See also

### 4.9.2.3 hipDrvMemcpy3D()

```
hipError_t hipDrvMemcpy3D (
    const HIP_MEMCPY3D * pCopy )
```

Copies data between host and device.

#### Parameters

in	<i>pCopy</i>	3D memory copy parameters
----	--------------	---------------------------

#### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection



See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

#### 4.9.2.4 hipDrvMemcpy3DAsync()

```
hipError_t hipDrvMemcpy3DAsync (
    const HIP_MEMCPY3D * pCopy,
    hipStream_t stream )
```

Copies data between host and device asynchronously.

Parameters

in	<i>pCopy</i>	3D memory copy parameters
in	<i>stream</i>	Stream to use

Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

#### 4.9.2.5 hipExternalMemoryGetMappedBuffer()

```
hipError_t hipExternalMemoryGetMappedBuffer (
    void ** devPtr,
    hipExternalMemory_t extMem,
    const hipExternalMemoryBufferDesc * bufferDesc )
```

Maps a buffer onto an imported memory object.

Parameters

out	<i>devPtr</i>	Returned device pointer to buffer
in	<i>extMem</i>	Handle to external memory object
in	<i>bufferDesc</i>	Buffer descriptor

Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

See also

#### 4.9.2.6 hipExtMallocWithFlags()

```
hipError_t hipExtMallocWithFlags (
    void ** ptr,
    size_t sizeBytes,
    unsigned int flags )
```

Allocate memory on the default accelerator.

**Parameters**

out	<i>ptr</i>	Pointer to the allocated memory
in	<i>size</i>	Requested memory size
in	<i>flags</i>	Type of memory allocation

If size is 0, no memory is allocated, \*ptr returns nullptr, and hipSuccess is returned.

**Returns**

#hipSuccess, #hipErrorOutOfMemory, #hipErrorInvalidValue (bad context, null \*ptr)

**See also**

[hipMallocPitch](#), [hipFree](#), [hipMallocArray](#), [hipFreeArray](#), [hipMalloc3D](#), [hipMalloc3DArray](#), [hipHostFree](#), [hipHostMalloc](#)

**4.9.2.7 hipFree()**

```
hipError_t hipFree (
    void * ptr )
```

Free memory allocated by the hcc hip memory allocation API. This API performs an implicit [hipDeviceSynchronize\(\)](#) call. If pointer is NULL, the hip runtime is initialized and hipSuccess is returned.

**Parameters**

in	<i>ptr</i>	Pointer to memory to be freed
----	------------	-------------------------------

**Returns**

#hipSuccess  
#hipErrorInvalidDevicePointer (if pointer is invalid, including host pointers allocated with hipHostMalloc)

**See also**

[hipMalloc](#), [hipMallocPitch](#), [hipMallocArray](#), [hipFreeArray](#), [hipHostFree](#), [hipMalloc3D](#), [hipMalloc3DArray](#), [hipHostMalloc](#)

**4.9.2.8 hipFreeArray()**

```
hipError_t hipFreeArray (
    hipArray * array )
```

Frees an array on the device.

**Parameters**

in	<i>array</i>	Pointer to array to free
----	--------------	--------------------------

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

See also

[hipMalloc](#), [hipMallocPitch](#), [hipFree](#), [hipMallocArray](#), [hipHostMalloc](#), [hipHostFree](#)

#### 4.9.2.9 hipFreeHost()

```
static hipError_t hipFreeHost (
    void * ptr ) [inline]
```

Free memory allocated by the hcc hip host memory allocation API. [Deprecated].

Parameters

in	<i>ptr</i>	Pointer to memory to be freed
----	------------	-------------------------------

Returns

#hipSuccess, #hipErrorInvalidValue (if pointer is invalid, including device pointers allocated with hipMalloc)

#### 4.9.2.10 hipFreeMipmappedArray()

```
hipError_t hipFreeMipmappedArray (
    hipMipmappedArray_t mipmappedArray )
```

Frees a mipmapped array on the device.

Parameters

in	<i>mipmappedArray</i>	- Pointer to mipmapped array to free
----	-----------------------	--------------------------------------

Returns

#hipSuccess, #hipErrorInvalidValue

#### 4.9.2.11 hipGetMipmappedArrayLevel()

```
hipError_t hipGetMipmappedArrayLevel (
    hipArray_t * levelArray,
    hipMipmappedArray_const_t mipmappedArray,
    unsigned int level )
```

Gets a mipmap level of a HIP mipmapped array.

Parameters

out	<i>levelArray</i>	- Returned mipmap level HIP array
in	<i>mipmappedArray</i>	- HIP mipmapped array
in	<i>level</i>	- Mipmap level

**Returns**

#hipSuccess, #hipErrorInvalidValue

**4.9.2.12 hipHostAlloc()**

```
static hipError_t hipHostAlloc (
    void ** ptr,
    size_t size,
    unsigned int flags ) [inline]
```

Allocate device accessible page locked host memory [Deprecated].

**Parameters**

out	<i>ptr</i>	Pointer to the allocated host pinned memory
in	<i>size</i>	Requested memory size
in	<i>flags</i>	Type of host memory allocation

If size is 0, no memory is allocated, \*ptr returns nullptr, and hipSuccess is returned.

**Returns**

#hipSuccess, #hipErrorOutOfMemory

**4.9.2.13 hipHostFree()**

```
hipError_t hipHostFree (
    void * ptr )
```

Free memory allocated by the hcc hip host memory allocation API This API performs an implicit [hipDeviceSynchronize\(\)](#) call. If pointer is NULL, the hip runtime is initialized and hipSuccess is returned.

**Parameters**

in	<i>ptr</i>	Pointer to memory to be freed
----	------------	-------------------------------

**Returns**

#hipSuccess, #hipErrorInvalidValue (if pointer is invalid, including device pointers allocated with hipMalloc)

**See also**

[hipMalloc](#), [hipMallocPitch](#), [hipFree](#), [hipMallocArray](#), [hipFreeArray](#), [hipMalloc3D](#), [hipMalloc3DArray](#), [hipHostMalloc](#)

**4.9.2.14 hipHostGetDevicePointer()**

```
hipError_t hipHostGetDevicePointer (
    void ** devPtr,
    void * hstPtr,
    unsigned int flags )
```

Get Device pointer from Host Pointer allocated through hipHostMalloc.

**Parameters**

out	<i>dstPtr</i>	Device Pointer mapped to passed host pointer
-----	---------------	--

## Parameters

in	<i>hstPtr</i>	Host Pointer allocated through hipHostMalloc
in	<i>flags</i>	Flags to be passed for extension

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorOutOfMemory

## See also

[hipSetDeviceFlags](#), [hipHostMalloc](#)

**4.9.2.15 hipHostGetFlags()**

```
hipError_t hipHostGetFlags (
    unsigned int * flagsPtr,
    void * hostPtr )
```

Return flags associated with host pointer.

## Parameters

out	<i>flagsPtr</i>	Memory location to store flags
in	<i>hostPtr</i>	Host Pointer allocated through hipHostMalloc

## Returns

#hipSuccess, #hipErrorInvalidValue

## See also

[hipHostMalloc](#)

**4.9.2.16 hipHostMalloc()**

```
hipError_t hipHostMalloc (
    void ** ptr,
    size_t size,
    unsigned int flags )
```

Allocate device accessible page locked host memory.

## Parameters

out	<i>ptr</i>	Pointer to the allocated host pinned memory
in	<i>size</i>	Requested memory size
in	<i>flags</i>	Type of host memory allocation

If size is 0, no memory is allocated, \*ptr returns nullptr, and hipSuccess is returned.

## Returns

#hipSuccess, #hipErrorOutOfMemory

See also

[hipSetDeviceFlags](#), [hipHostFree](#)

#### 4.9.2.17 hipHostRegister()

```
hipError_t hipHostRegister (
    void * hostPtr,
    size_t sizeBytes,
    unsigned int flags )
```

Register host memory so it can be accessed from the current device.

##### Parameters

out	<i>hostPtr</i>	Pointer to host memory to be registered.
in	<i>sizeBytes</i>	size of the host memory
in	<i>flags</i> .	See below.

Flags:

- [hipHostRegisterDefault](#) Memory is Mapped and Portable
- [hipHostRegisterPortable](#) Memory is considered registered by all contexts. HIP only supports one context so this is always assumed true.
- [hipHostRegisterMapped](#) Map the allocation into the address space for the current device. The device pointer can be obtained with [hipHostGetDevicePointer](#).

After registering the memory, use [hipHostGetDevicePointer](#) to obtain the mapped device pointer. On many systems, the mapped device pointer will have a different value than the mapped host pointer. Applications must use the device pointer in device code, and the host pointer in device code.

On some systems, registered memory is pinned. On some systems, registered memory may not be actually be pinned but uses OS or hardware facilities to all GPU access to the host memory.

Developers are strongly encouraged to register memory blocks which are aligned to the host cache-line size. (typically 64-bytes but can be obtains from the CPUID instruction).

If registering non-aligned pointers, the application must take care when register pointers from the same cache line on different devices. HIP's coarse-grained synchronization model does not guarantee correct results if different devices write to different parts of the same cache block - typically one of the writes will "win" and overwrite data from the other registered memory region.

Returns

#hipSuccess, #hipErrorOutOfMemory

See also

[hipHostUnregister](#), [hipHostGetFlags](#), [hipHostGetDevicePointer](#)

#### 4.9.2.18 hipHostUnregister()

```
hipError_t hipHostUnregister (
    void * hostPtr )
```

Un-register host pointer.

##### Parameters

in	<i>hostPtr</i>	Host pointer previously registered with <a href="#">hipHostRegister</a>
----	----------------	---

**Returns**

Error code

**See also**

[hipHostRegister](#)

**4.9.2.19 hipImportExternalMemory()**

```
hipError_t hipImportExternalMemory (
    hipExternalMemory_t * extMem_out,
    const hipExternalMemoryHandleDesc * memHandleDesc )
```

Imports an external memory object.

**Parameters**

out	<i>extMem_out</i>	Returned handle to an external memory object
in	<i>memHandleDesc</i>	Memory import handle descriptor

**Returns**

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

**See also****4.9.2.20 hipImportExternalSemaphore()**

```
hipError_t hipImportExternalSemaphore (
    hipExternalSemaphore_t * extSem_out,
    const hipExternalSemaphoreHandleDesc * semHandleDesc )
```

Imports an external semaphore.

**Parameters**

out	<i>extSem_out</i>	External semaphores to be waited on
in	<i>semHandleDesc</i>	Semaphore import handle descriptor

**Returns**

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

**See also****4.9.2.21 hipMalloc()**

```
hipError_t hipMalloc (
    void ** ptr,
    size_t size )
```

Allocate memory on the default accelerator.

**Parameters**

out	<i>ptr</i>	Pointer to the allocated memory
in	<i>size</i>	Requested memory size

If size is 0, no memory is allocated, \*ptr returns nullptr, and hipSuccess is returned.

**Returns**

#hipSuccess, #hipErrorOutOfMemory, #hipErrorInvalidValue (bad context, null \*ptr)

**See also**

[hipMallocPitch](#), [hipFree](#), [hipMallocArray](#), [hipFreeArray](#), [hipMalloc3D](#), [hipMalloc3DArray](#), [hipHostFree](#), [hipHostMalloc](#)

**4.9.2.22 hipMalloc3DArray()**

```
hipError_t hipMalloc3DArray (
    hipArray ** array,
    const struct hipChannelFormatDesc * desc,
    struct hipExtent extent,
    unsigned int flags )
```

Allocate an array on the device.

**Parameters**

out	<i>array</i>	Pointer to allocated array in device memory
in	<i>desc</i>	Requested channel format
in	<i>extent</i>	Requested array allocation width, height and depth
in	<i>flags</i>	Requested properties of allocated array

**Returns**

#hipSuccess, #hipErrorOutOfMemory

**See also**

[hipMalloc](#), [hipMallocPitch](#), [hipFree](#), [hipFreeArray](#), [hipHostMalloc](#), [hipHostFree](#)

**4.9.2.23 hipMallocArray()**

```
hipError_t hipMallocArray (
    hipArray ** array,
    const hipChannelFormatDesc * desc,
    size_t width,
    size_t height __dparm0,
    unsigned int flags __dparmhipArrayDefault )
```

Allocate an array on the device.

**Parameters**

out	<i>array</i>	Pointer to allocated array in device memory
in	<i>desc</i>	Requested channel format
in	<i>width</i>	Requested array allocation width



## Parameters

in	<i>height</i>	Requested array allocation height
in	<i>flags</i>	Requested properties of allocated array

## Returns

#hipSuccess, #hipErrorOutOfMemory

## See also

[hipMalloc](#), [hipMallocPitch](#), [hipFree](#), [hipFreeArray](#), [hipHostMalloc](#), [hipHostFree](#)

**4.9.2.24 hipMallocHost()**

```
static hipError_t hipMallocHost (
    void ** ptr,
    size_t size ) [inline]
```

Allocate pinned host memory [Deprecated].

## Parameters

out	<i>ptr</i>	Pointer to the allocated host pinned memory
in	<i>size</i>	Requested memory size

If size is 0, no memory is allocated, \*ptr returns nullptr, and hipSuccess is returned.

## Returns

#hipSuccess, #hipErrorOutOfMemory

**4.9.2.25 hipMallocManaged()**

```
hipError_t hipMallocManaged (
    void ** dev_ptr,
    size_t size,
    unsigned int flags __dparmhipMemAttachGlobal )
```

Allocates memory that will be automatically managed by HIP.

## Parameters

out	<i>dev_ptr</i>	- pointer to allocated device memory
in	<i>size</i>	- requested allocation size in bytes
in	<i>flags</i>	- must be either hipMemAttachGlobal or hipMemAttachHost (defaults to hipMemAttachGlobal)

## Returns

#hipSuccess, #hipErrorMemoryAllocation, #hipErrorNotSupported, #hipErrorInvalidValue

**4.9.2.26 hipMallocMipmappedArray()**

```
hipError_t hipMallocMipmappedArray (
    hipMipmappedArray_t * mipmappedArray,
```

```
const struct hipChannelFormatDesc * desc,
struct hipExtent extent,
unsigned int numLevels,
unsigned int flags __dparm0 )
```

Allocate a mipmapped array on the device.

#### Parameters

out	<i>mipmappedArray</i>	- Pointer to allocated mipmapped array in device memory
in	<i>desc</i>	- Requested channel format
in	<i>extent</i>	- Requested allocation size (width field in elements)
in	<i>numLevels</i>	- Number of mipmap levels to allocate
in	<i>flags</i>	- Flags for extensions

#### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryAllocation

#### 4.9.2.27 hipMallocPitch()

```
hipError_t hipMallocPitch (
    void ** ptr,
    size_t * pitch,
    size_t width,
    size_t height )
```

Allocates at least width (in bytes) \* height bytes of linear memory Padding may occur to ensure alignment requirements are met for the given row The change in width size due to padding will be returned in \*pitch. Currently the alignment is set to 128 bytes

#### Parameters

out	<i>ptr</i>	Pointer to the allocated device memory
out	<i>pitch</i>	Pitch for allocation (in bytes)
in	<i>width</i>	Requested pitched allocation width (in bytes)
in	<i>height</i>	Requested pitched allocation height

If size is 0, no memory is allocated, \*ptr returns nullptr, and hipSuccess is returned.

#### Returns

Error code

#### See also

[hipMalloc](#), [hipFree](#), [hipMallocArray](#), [hipFreeArray](#), [hipHostFree](#), [hipMalloc3D](#), [hipMalloc3DArray](#), [hipHostMalloc](#)

#### 4.9.2.28 hipMemAdvise()

```
hipError_t hipMemAdvise (
    const void * dev_ptr,
    size_t count,
    hipMemoryAdvise advice,
    int device )
```

Advise about the usage of a given memory range to HIP.

## Parameters

in	<i>dev_ptr</i>	pointer to memory to set the advice for
in	<i>count</i>	size in bytes of the memory range
in	<i>advice</i>	advice to be applied for the specified memory range
in	<i>device</i>	device to apply the advice for

## Returns

#hipSuccess, #hipErrorInvalidValue

## 4.9.2.29 hipMemAllocHost()

```
static hipError_t hipMemAllocHost (
    void ** ptr,
    size_t size ) [inline]
```

Allocate pinned host memory [Deprecated].

## Parameters

out	<i>ptr</i>	Pointer to the allocated host pinned memory
in	<i>size</i>	Requested memory size

If size is 0, no memory is allocated, \*ptr returns nullptr, and hipSuccess is returned.

## Returns

#hipSuccess, #hipErrorOutOfMemory

## 4.9.2.30 hipMemAllocPitch()

```
hipError_t hipMemAllocPitch (
    hipDeviceptr_t * dptr,
    size_t * pitch,
    size_t widthInBytes,
    size_t height,
    unsigned int elementSizeBytes )
```

Allocates at least width (in bytes) \* height bytes of linear memory. Padding may occur to ensure alignment requirements are met for the given row. The change in width size due to padding will be returned in \*pitch. Currently the alignment is set to 128 bytes.

## Parameters

out	<i>dptr</i>	Pointer to the allocated device memory
out	<i>pitch</i>	Pitch for allocation (in bytes)
in	<i>width</i>	Requested pitched allocation width (in bytes)
in	<i>height</i>	Requested pitched allocation height

If size is 0, no memory is allocated, *ptr* returns nullptr, and *hipSuccess* is returned. The intended usage of *pitch* is as a separate parameter of the allocation, used to compute addresses within the 2D array. Given the row and column of an array element of type *T*, the address is computed as:  $T \text{ pElement} = (T*)((\text{char}*)\text{BaseAddress} + \text{Row} * \text{Pitch}) + \text{Column};$

**Returns**

Error code

**See also**

[hipMalloc](#), [hipFree](#), [hipMallocArray](#), [hipFreeArray](#), [hipHostFree](#), [hipMalloc3D](#), [hipMalloc3DArray](#), [hipHostMalloc](#)

**4.9.2.31 hipMemcpy()**

```
hipError_t hipMemcpy (
    void * dst,
    const void * src,
    size_t sizeBytes,
    hipMemcpyKind kind )
```

Copy data from src to dst.

It supports memory from host to device, device to host, device to device and host to host. The src and dst must not overlap.

For hipMemcpy, the copy is always performed by the current device (set by hipSetDevice). For multi-gpu or peer-to-peer configurations, it is recommended to set the current device to the device where the src data is physically located. For optimal peer-to-peer copies, the copy device must be able to access the src and dst pointers (by calling hipDeviceEnablePeerAccess with copy agent as the current device and src/dest as the peerDevice argument). If this is not done, the hipMemcpy will still work, but will perform the copy using a staging buffer on the host. Calling hipMemcpy with dst and src pointers that do not match the hipMemcpyKind results in undefined behavior.

**Parameters**

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes
in	<i>copyType</i>	Memory copy type

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree, #hipErrorUnknown

**See also**

[hipArrayCreate](#), [hipArrayDestroy](#), [hipArrayGetDescriptor](#), [hipMemAlloc](#), [hipMemAllocHost](#), [hipMemAllocPitch](#), [hipMemcpy2D](#), [hipMemcpy2DAsync](#), [hipMemcpy2DUnaligned](#), [hipMemcpyAtoA](#), [hipMemcpyAtoD](#), [hipMemcpyAtoH](#), [hipMemcpyAtoHAsync](#), [hipMemcpyDtoA](#), [hipMemcpyDtoD](#), [hipMemcpyDtoDAsync](#), [hipMemcpyDtoH](#), [hipMemcpyDtoHAsync](#), [hipMemcpyHtoA](#), [hipMemcpyHtoAAsync](#), [hipMemcpyHtoDAsync](#), [hipMemFree](#), [hipMemFreeHost](#), [hipMemGetAddressRange](#), [hipMemGetInfo](#), [hipMemHostAlloc](#), [hipMemHostGetDevicePointer](#)

**4.9.2.32 hipMemcpy2D()**

```
hipError_t hipMemcpy2D (
    void * dst,
    size_t dpitch,
    const void * src,
    size_t spitch,
    size_t width,
    size_t height,
    hipMemcpyKind kind )
```

Copies data between host and device.

## Parameters

in	<i>dst</i>	Destination memory address
in	<i>dpitch</i>	Pitch of destination memory
in	<i>src</i>	Source memory address
in	<i>spitch</i>	Pitch of source memory
in	<i>width</i>	Width of matrix transfer (columns in bytes)
in	<i>height</i>	Height of matrix transfer (rows)
in	<i>kind</i>	Type of transfer

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

## See also

[hipMemcpy](#), [hipMemcpyToArray](#), [hipMemcpy2DToArray](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

## 4.9.2.33 hipMemcpy2DAsync()

```
hipError_t hipMemcpy2DAsync (
    void * dst,
    size_t dpitch,
    const void * src,
    size_t spitch,
    size_t width,
    size_t height,
    hipMemcpyKind kind,
    hipStream_t stream __dparm0 )
```

Copies data between host and device.

## Parameters

in	<i>dst</i>	Destination memory address
in	<i>dpitch</i>	Pitch of destination memory
in	<i>src</i>	Source memory address
in	<i>spitch</i>	Pitch of source memory
in	<i>width</i>	Width of matrix transfer (columns in bytes)
in	<i>height</i>	Height of matrix transfer (rows)
in	<i>kind</i>	Type of transfer
in	<i>stream</i>	Stream to use

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

## See also

[hipMemcpy](#), [hipMemcpyToArray](#), [hipMemcpy2DToArray](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

#### 4.9.2.34 hipMemcpy2DFromArray()

```
hipError_t hipMemcpy2DFromArray (
    void * dst,
    size_t dpitch,
    hipArray_const_t src,
    size_t wOffset,
    size_t hOffset,
    size_t width,
    size_t height,
    hipMemcpyKind kind )
```

Copies data between host and device.

##### Parameters

in	<i>dst</i>	Destination memory address
in	<i>dpitch</i>	Pitch of destination memory
in	<i>src</i>	Source memory address
in	<i>wOffset</i>	Source starting X offset
in	<i>hOffset</i>	Source starting Y offset
in	<i>width</i>	Width of matrix transfer (columns in bytes)
in	<i>height</i>	Height of matrix transfer (rows)
in	<i>kind</i>	Type of transfer

##### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

##### See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

#### 4.9.2.35 hipMemcpy2DFromArrayAsync()

```
hipError_t hipMemcpy2DFromArrayAsync (
    void * dst,
    size_t dpitch,
    hipArray_const_t src,
    size_t wOffset,
    size_t hOffset,
    size_t width,
    size_t height,
    hipMemcpyKind kind,
    hipStream_t stream __dparm0 )
```

Copies data between host and device asynchronously.

##### Parameters

in	<i>dst</i>	Destination memory address
in	<i>dpitch</i>	Pitch of destination memory
in	<i>src</i>	Source memory address
in	<i>wOffset</i>	Source starting X offset
in	<i>hOffset</i>	Source starting Y offset
in	<i>width</i>	Width of matrix transfer (columns in bytes)

## Parameters

in	<i>height</i>	Height of matrix transfer (rows)
in	<i>kind</i>	Type of transfer
in	<i>stream</i>	Accelerator view which the copy is being enqueued

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

## See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

## 4.9.2.36 hipMemcpy2DToArray()

```
hipError_t hipMemcpy2DToArray (
    hipArray * dst,
    size_t wOffset,
    size_t hOffset,
    const void * src,
    size_t spitch,
    size_t width,
    size_t height,
    hipMemcpyKind kind )
```

Copies data between host and device.

## Parameters

in	<i>dst</i>	Destination memory address
in	<i>wOffset</i>	Destination starting X offset
in	<i>hOffset</i>	Destination starting Y offset
in	<i>src</i>	Source memory address
in	<i>spitch</i>	Pitch of source memory
in	<i>width</i>	Width of matrix transfer (columns in bytes)
in	<i>height</i>	Height of matrix transfer (rows)
in	<i>kind</i>	Type of transfer

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

## See also

[hipMemcpy](#), [hipMemcpyToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

## 4.9.2.37 hipMemcpy2DToArrayAsync()

```
hipError_t hipMemcpy2DToArrayAsync (
    hipArray * dst,
    size_t wOffset,
```

```

size_t hOffset,
const void * src,
size_t spitch,
size_t width,
size_t height,
hipMemcpyKind kind,
hipStream_t stream __dparm0 )

```

Copies data between host and device.

#### Parameters

in	<i>dst</i>	Destination memory address
in	<i>wOffset</i>	Destination starting X offset
in	<i>hOffset</i>	Destination starting Y offset
in	<i>src</i>	Source memory address
in	<i>spitch</i>	Pitch of source memory
in	<i>width</i>	Width of matrix transfer (columns in bytes)
in	<i>height</i>	Height of matrix transfer (rows)
in	<i>kind</i>	Type of transfer
in	<i>stream</i>	Accelerator view which the copy is being enqueued

#### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipError↵  
InvalidMemcpyDirection

#### See also

[hipMemcpy](#), [hipMemcpyToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

### 4.9.2.38 hipMemcpy3D()

```

hipError_t hipMemcpy3D (
    const struct hipMemcpy3DParms * p )

```

Copies data between host and device.

#### Parameters

in	<i>p</i>	3D memory copy parameters
----	----------	---------------------------

#### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipError↵  
InvalidMemcpyDirection

#### See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

### 4.9.2.39 hipMemcpy3DAsync()

```

hipError_t hipMemcpy3DAsync (
    const struct hipMemcpy3DParms * p,
    hipStream_t stream __dparm0 )

```



Copies data between host and device asynchronously.

## Parameters

in	<i>p</i>	3D memory copy parameters
in	<i>stream</i>	Stream to use

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

## See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

## 4.9.2.40 hipMemcpyAsync()

```
hipError_t hipMemcpyAsync (
    void * dst,
    const void * src,
    size_t sizeBytes,
    hipMemcpyKind kind,
    hipStream_t stream __dparm0 )
```

Copy data from src to dst asynchronously.

## Warning

If host or dest are not pinned, the memory copy will be performed synchronously. For best performance, use `hipHostMalloc` to allocate host memory that is transferred asynchronously.

on HCC `hipMemcpyAsync` does not support overlapped H2D and D2H copies. For `hipMemcpy`, the copy is always performed by the device associated with the specified stream.

For multi-gpu or peer-to-peer configurations, it is recommended to use a stream which is attached to the device where the src data is physically located. For optimal peer-to-peer copies, the copy device must be able to access the src and dst pointers (by calling `hipDeviceEnablePeerAccess` with copy agent as the current device and src/dest as the peerDevice argument. if this is not done, the `hipMemcpy` will still work, but will perform the copy using a staging buffer on the host.

## Parameters

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes
in	<i>accelerator_view</i>	Accelerator view which the copy is being enqueued

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree, #hipErrorUnknown

## See also

[hipMemcpy](#), [hipMemcpy2D](#), [hipMemcpyToArray](#), [hipMemcpy2DToArray](#), [hipMemcpyFromArray](#), [hipMemcpy2DFromArray](#), [hipMemcpyArrayToArray](#), [hipMemcpy2DArrayToArray](#), [hipMemcpyToSymbol](#), [hipMemcpyFromSymbol](#), [hipMemcpy2DAsync](#), [hipMemcpyToArrayAsync](#), [hipMemcpy2DToArrayAsync](#), [hipMemcpyFromArrayAsync](#), [hipMemcpy2DFromArrayAsync](#), [hipMemcpyToSymbolAsync](#), [hipMemcpyFromSymbolAsync](#)

**4.9.2.41 hipMemcpyAtoH()**

```
hipError_t hipMemcpyAtoH (
    void * dst,
    hipArray * srcArray,
    size_t srcOffset,
    size_t count )
```

Copies data between host and device.

**Parameters**

in	<i>dst</i>	Destination memory address
in	<i>srcArray</i>	Source array
in	<i>srcOffset</i>	Offset in bytes of source array
in	<i>count</i>	Size of memory copy in bytes

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

**See also**

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

**4.9.2.42 hipMemcpyDtoD()**

```
hipError_t hipMemcpyDtoD (
    hipDeviceptr_t dst,
    hipDeviceptr_t src,
    size_t sizeBytes )
```

Copy data from Device to Device.

**Parameters**

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes

**Returns**

#hipSuccess, #hipErrorDeinitialized, #hipErrorNotInitialized, #hipErrorInvalidContext, #hipErrorInvalidValue

**See also**

[hipArrayCreate](#), [hipArrayDestroy](#), [hipArrayGetDescriptor](#), [hipMemAlloc](#), [hipMemAllocHost](#), [hipMemAllocPitch](#), [hipMemcpy2D](#), [hipMemcpy2DAsync](#), [hipMemcpy2DUnaligned](#), [hipMemcpyAtoA](#), [hipMemcpyAtoD](#), [hipMemcpyAtoH](#), [hipMemcpyAtoHAsync](#), [hipMemcpyDtoA](#), [hipMemcpyDtoD](#), [hipMemcpyDtoDAsync](#), [hipMemcpyDtoH](#), [hipMemcpyDtoHAsync](#), [hipMemcpyHtoA](#), [hipMemcpyHtoAAsync](#), [hipMemcpyHtoDAsync](#), [hipMemFree](#), [hipMemFreeHost](#), [hipMemGetAddressRange](#), [hipMemGetInfo](#), [hipMemHostAlloc](#), [hipMemHostGetDevicePointer](#)

**4.9.2.43 hipMemcpyDtoDAsync()**

```
hipError_t hipMemcpyDtoDAsync (
    hipDeviceptr_t dst,
```

```
hipDeviceptr_t src,
size_t sizeBytes,
hipStream_t stream )
```

Copy data from Device to Device asynchronously.

#### Parameters

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes

#### Returns

#hipSuccess, #hipErrorDeinitialized, #hipErrorNotInitialized, #hipErrorInvalidContext, #hipErrorInvalidValue

#### See also

hipArrayCreate, hipArrayDestroy, hipArrayGetDescriptor, hipMemAlloc, [hipMemAllocHost](#), [hipMemAllocPitch](#), [hipMemcpy2D](#), [hipMemcpy2DAsync](#), hipMemcpy2DUnaligned, hipMemcpyAtoA, hipMemcpyAtoD, [hipMemcpyAtoH](#), hipMemcpyAtoHAsync, hipMemcpyDtoA, [hipMemcpyDtoD](#), [hipMemcpyDtoDAsync](#), [hipMemcpyDtoH](#), [hipMemcpyDtoHAsync](#), [hipMemcpyHtoA](#), hipMemcpyHtoAAsync, [hipMemcpyHtoDAsync](#), hipMemFree, hipMemFreeHost, [hipMemGetAddressRange](#), [hipMemGetInfo](#), hipMemHostAlloc, hipMem↔HostGetDevicePointer

#### 4.9.2.44 hipMemcpyDtoH()

```
hipError_t hipMemcpyDtoH (
    void * dst,
    hipDeviceptr_t src,
    size_t sizeBytes )
```

Copy data from Device to Host.

#### Parameters

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes

#### Returns

#hipSuccess, #hipErrorDeinitialized, #hipErrorNotInitialized, #hipErrorInvalidContext, #hipErrorInvalidValue

#### See also

hipArrayCreate, hipArrayDestroy, hipArrayGetDescriptor, hipMemAlloc, [hipMemAllocHost](#), [hipMemAllocPitch](#), [hipMemcpy2D](#), [hipMemcpy2DAsync](#), hipMemcpy2DUnaligned, hipMemcpyAtoA, hipMemcpyAtoD, [hipMemcpyAtoH](#), hipMemcpyAtoHAsync, hipMemcpyDtoA, [hipMemcpyDtoD](#), [hipMemcpyDtoDAsync](#), [hipMemcpyDtoH](#), [hipMemcpyDtoHAsync](#), [hipMemcpyHtoA](#), hipMemcpyHtoAAsync, [hipMemcpyHtoDAsync](#), hipMemFree, hipMemFreeHost, [hipMemGetAddressRange](#), [hipMemGetInfo](#), hipMemHostAlloc, hipMem↔HostGetDevicePointer

#### 4.9.2.45 hipMemcpyDtoHAsync()

```
hipError_t hipMemcpyDtoHAsync (
    void * dst,
```

```
hipDeviceptr_t src,
size_t sizeBytes,
hipStream_t stream )
```

Copy data from Device to Host asynchronously.

#### Parameters

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes

#### Returns

#hipSuccess, #hipErrorDeinitialized, #hipErrorNotInitialized, #hipErrorInvalidContext, #hipErrorInvalidValue

#### See also

hipArrayCreate, hipArrayDestroy, hipArrayGetDescriptor, hipMemAlloc, [hipMemAllocHost](#), [hipMemAllocPitch](#), [hipMemcpy2D](#), [hipMemcpy2DAsync](#), hipMemcpy2DUnaligned, hipMemcpyAtoA, hipMemcpyAtoD, [hipMemcpyAtoH](#), hipMemcpyAtoHAsync, hipMemcpyDtoA, [hipMemcpyDtoD](#), [hipMemcpyDtoDAsync](#), [hipMemcpyDtoH](#), [hipMemcpyDtoHAsync](#), [hipMemcpyHtoA](#), hipMemcpyHtoAAsync, [hipMemcpyHtoDAsync](#), hipMemFree, hipMemFreeHost, [hipMemGetAddressRange](#), [hipMemGetInfo](#), hipMemHostAlloc, hipMem↔HostGetDevicePointer

#### 4.9.2.46 hipMemcpyFromArray()

```
hipError_t hipMemcpyFromArray (
    void * dst,
    hipArray_const_t srcArray,
    size_t wOffset,
    size_t hOffset,
    size_t count,
    hipMemcpyKind kind )
```

Copies data between host and device.

#### Parameters

in	<i>dst</i>	Destination memory address
in	<i>srcArray</i>	Source memory address
in	<i>wOffset</i>	Source starting X offset
in	<i>hOffset</i>	Source starting Y offset
in	<i>count</i>	Size in bytes to copy
in	<i>kind</i>	Type of transfer

#### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipError↔InvalidMemcpyDirection

#### See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

#### 4.9.2.47 hipMemcpyHtoA()

```
hipError_t hipMemcpyHtoA (
    hipArray * dstArray,
    size_t dstOffset,
    const void * srcHost,
    size_t count )
```

Copies data between host and device.

##### Parameters

in	<i>dstArray</i>	Destination memory address
in	<i>dstOffset</i>	Offset in bytes of destination array
in	<i>srcHost</i>	Source host pointer
in	<i>count</i>	Size of memory copy in bytes

##### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

##### See also

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

#### 4.9.2.48 hipMemcpyHtoD()

```
hipError_t hipMemcpyHtoD (
    hipDeviceptr_t dst,
    void * src,
    size_t sizeBytes )
```

Copy data from Host to Device.

##### Parameters

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes

##### Returns

#hipSuccess, #hipErrorDeinitialized, #hipErrorNotInitialized, #hipErrorInvalidContext, #hipErrorInvalidValue

##### See also

[hipArrayCreate](#), [hipArrayDestroy](#), [hipArrayGetDescriptor](#), [hipMemAlloc](#), [hipMemAllocHost](#), [hipMemAllocPitch](#), [hipMemcpy2D](#), [hipMemcpy2DAsync](#), [hipMemcpy2DUnaligned](#), [hipMemcpyAtoA](#), [hipMemcpyAtoD](#), [hipMemcpyAtoH](#), [hipMemcpyAtoHAsync](#), [hipMemcpyDtoA](#), [hipMemcpyDtoD](#), [hipMemcpyDtoDAsync](#), [hipMemcpyDtoH](#), [hipMemcpyDtoHAsync](#), [hipMemcpyHtoA](#), [hipMemcpyHtoAAsync](#), [hipMemcpyHtoDAsync](#), [hipMemFree](#), [hipMemFreeHost](#), [hipMemGetAddressRange](#), [hipMemGetInfo](#), [hipMemHostAlloc](#), [hipMemHostGetDevicePointer](#)

#### 4.9.2.49 hipMemcpyHtoDAsync()

```
hipError_t hipMemcpyHtoDAsync (
    hipDeviceptr_t dst,
```

```
void * src,
size_t sizeBytes,
hipStream_t stream )
```

Copy data from Host to Device asynchronously.

#### Parameters

out	<i>dst</i>	Data being copy to
in	<i>src</i>	Data being copy from
in	<i>sizeBytes</i>	Data size in bytes

#### Returns

#hipSuccess, #hipErrorDeinitialized, #hipErrorNotInitialized, #hipErrorInvalidContext, #hipErrorInvalidValue

#### See also

hipArrayCreate, hipArrayDestroy, hipArrayGetDescriptor, hipMemAlloc, [hipMemAllocHost](#), [hipMemAllocPitch](#), [hipMemcpy2D](#), [hipMemcpy2DAsync](#), hipMemcpy2DUnaligned, hipMemcpyAtoA, hipMemcpyAtoD, [hipMemcpyAtoH](#), hipMemcpyAtoHAsync, hipMemcpyDtoA, [hipMemcpyDtoD](#), [hipMemcpyDtoDAsync](#), [hipMemcpyDtoH](#), [hipMemcpyDtoHAsync](#), [hipMemcpyHtoA](#), hipMemcpyHtoAAsync, [hipMemcpyHtoDAsync](#), hipMemFree, hipMemFreeHost, [hipMemGetAddressRange](#), [hipMemGetInfo](#), hipMemHostAlloc, hipMem↔HostGetDevicePointer

#### 4.9.2.50 hipMemcpyParam2D()

```
hipError_t hipMemcpyParam2D (
    const hip\_Memcpy2D * pCopy )
```

Copies memory for 2D arrays.

#### Parameters

in	<i>pCopy</i>	Parameters for the memory copy
----	--------------	--------------------------------

#### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipError↔InvalidMemcpyDirection

#### See also

[hipMemcpy](#), [hipMemcpy2D](#), [hipMemcpyToArray](#), [hipMemcpy2DToArray](#), [hipMemcpyFromArray](#), [hipMemcpy↔ToSymbol](#), [hipMemcpyAsync](#)

#### 4.9.2.51 hipMemcpyParam2DAsync()

```
hipError_t hipMemcpyParam2DAsync (
    const hip\_Memcpy2D * pCopy,
    hipStream_t stream __dparm0 )
```

Copies memory for 2D arrays.

#### Parameters

in	<i>pCopy</i>	Parameters for the memory copy
in	<i>stream</i>	Stream to use

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

**See also**

[hipMemcpy](#), [hipMemcpy2D](#), [hipMemcpyToArray](#), [hipMemcpy2DToArray](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

**4.9.2.52 hipMemcpyToArray()**

```
hipError_t hipMemcpyToArray (
    hipArray * dst,
    size_t wOffset,
    size_t hOffset,
    const void * src,
    size_t count,
    hipMemcpyKind kind )
```

Copies data between host and device.

**Parameters**

in	<i>dst</i>	Destination memory address
in	<i>wOffset</i>	Destination starting X offset
in	<i>hOffset</i>	Destination starting Y offset
in	<i>src</i>	Source memory address
in	<i>count</i>	size in bytes to copy
in	<i>kind</i>	Type of transfer

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidPitchValue, #hipErrorInvalidDevicePointer, #hipErrorInvalidMemcpyDirection

**See also**

[hipMemcpy](#), [hipMemcpy2DToArray](#), [hipMemcpy2D](#), [hipMemcpyFromArray](#), [hipMemcpyToSymbol](#), [hipMemcpyAsync](#)

**4.9.2.53 hipMemGetInfo()**

```
hipError_t hipMemGetInfo (
    size_t * free,
    size_t * total )
```

Query memory info. Return snapshot of free memory, and total allocatable memory on the device.

Returns in \*free a snapshot of the current free memory.

**Returns**

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

**Warning**

On HCC, the free memory only accounts for memory allocated by this process and may be optimistic.



**4.9.2.54 hipMemPrefetchAsync()**

```
hipError_t hipMemPrefetchAsync (
    const void * dev_ptr,
    size_t count,
    int device,
    hipStream_t stream __dparm0 )
```

Prefetches memory to the specified destination device using HIP.

**Parameters**

in	<i>dev_ptr</i>	pointer to be prefetched
in	<i>count</i>	size in bytes for prefetching
in	<i>device</i>	destination device to prefetch to
in	<i>stream</i>	stream to enqueue prefetch operation

**Returns**

#hipSuccess, #hipErrorInvalidValue

**4.9.2.55 hipMemRangeGetAttribute()**

```
hipError_t hipMemRangeGetAttribute (
    void * data,
    size_t data_size,
    hipMemRangeAttribute attribute,
    const void * dev_ptr,
    size_t count )
```

Query an attribute of a given memory range in HIP.

**Parameters**

in, out	<i>data</i>	a pointer to a memory location where the result of each attribute query will be written to
in	<i>data_size</i>	the size of data
in	<i>attribute</i>	the attribute to query
in	<i>dev_ptr</i>	start of the range to query
in	<i>count</i>	size of the range to query

**Returns**

#hipSuccess, #hipErrorInvalidValue

**4.9.2.56 hipMemRangeGetAttributes()**

```
hipError_t hipMemRangeGetAttributes (
    void ** data,
    size_t * data_sizes,
    hipMemRangeAttribute * attributes,
    size_t num_attributes,
    const void * dev_ptr,
    size_t count )
```

Query attributes of a given memory range in HIP.

**Parameters**

in, out	<i>data</i>	a two-dimensional array containing pointers to memory locations where the result of each attribute query will be written to
in	<i>data_sizes</i>	an array, containing the sizes of each result
in	<i>attributes</i>	the attribute to query
in	<i>num_attributes</i>	an array of attributes to query (numAttributes and the number of attributes in this array should match)
in	<i>dev_ptr</i>	start of the range to query
in	<i>count</i>	size of the range to query

**Returns**

#hipSuccess, #hipErrorInvalidValue

**4.9.2.57 hipMemset()**

```
hipError_t hipMemset (
    void * dst,
    int value,
    size_t sizeBytes )
```

Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant byte value value.

**Parameters**

out	<i>dst</i>	Data being filled
in	<i>constant</i>	value to be set
in	<i>sizeBytes</i>	Data size in bytes

**Returns**

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

**4.9.2.58 hipMemset2D()**

```
hipError_t hipMemset2D (
    void * dst,
    size_t pitch,
    int value,
    size_t width,
    size_t height )
```

Fills the memory area pointed to by dst with the constant value.

**Parameters**

out	<i>dst</i>	Pointer to device memory
in	<i>pitch</i>	- data size in bytes
in	<i>value</i>	- constant value to be set
in	<i>width</i>	
in	<i>height</i>	

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree

## 4.9.2.59 hipMemset2DAsync()

```
hipError_t hipMemset2DAsync (
    void * dst,
    size_t pitch,
    int value,
    size_t width,
    size_t height,
    hipStream_t stream __dparm0 )
```

Fills asynchronously the memory area pointed to by dst with the constant value.

## Parameters

in	<i>dst</i>	Pointer to device memory
in	<i>pitch</i>	- data size in bytes
in	<i>value</i>	- constant value to be set
in	<i>width</i>	
in	<i>height</i>	
in	<i>stream</i>	

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree

## 4.9.2.60 hipMemset3D()

```
hipError_t hipMemset3D (
    hipPitchedPtr pitchedDevPtr,
    int value,
    hipExtent extent )
```

Fills synchronously the memory area pointed to by pitchedDevPtr with the constant value.

## Parameters

in	<i>pitchedDevPtr</i>	
in	<i>value</i>	- constant value to be set
in	<i>extent</i>	

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree

## 4.9.2.61 hipMemset3DAsync()

```
hipError_t hipMemset3DAsync (
    hipPitchedPtr pitchedDevPtr,
    int value,
    hipExtent extent,
    hipStream_t stream __dparm0 )
```

Fills asynchronously the memory area pointed to by `pitchedDevPtr` with the constant value.

## Parameters

in	<i>pitchedDevPtr</i>	
in	<i>value</i>	- constant value to be set
in	<i>extent</i>	
in	<i>stream</i>	

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree

## 4.9.2.62 hipMemsetAsync()

```
hipError_t hipMemsetAsync (
    void * dst,
    int value,
    size_t sizeBytes,
    hipStream_t stream __dparm0 )
```

Fills the first sizeBytes bytes of the memory area pointed to by dev with the constant byte value value.

[hipMemsetAsync\(\)](#) is asynchronous with respect to the host, so the call may return before the memset is complete. The operation can optionally be associated to a stream by passing a non-zero stream argument. If stream is non-zero, the operation may overlap with operations in other streams.

## Parameters

out	<i>dst</i>	Pointer to device memory
in	<i>value</i>	- Value to set for each byte of specified memory
in	<i>sizeBytes</i>	- Size in bytes to set
in	<i>stream</i>	- Stream identifier

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree

## 4.9.2.63 hipMemsetD16()

```
hipError_t hipMemsetD16 (
    hipDeviceptr_t dest,
    unsigned short value,
    size_t count )
```

Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant short value value.

## Parameters

out	<i>dst</i>	Data ptr to be filled
in	<i>constant</i>	value to be set
in	<i>number</i>	of values to be set

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

#### 4.9.2.64 hipMemsetD16Async()

```
hipError_t hipMemsetD16Async (
    hipDeviceptr_t dest,
    unsigned short value,
    size_t count,
    hipStream_t stream __dparm0 )
```

Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant short value value.

[hipMemsetD16Async\(\)](#) is asynchronous with respect to the host, so the call may return before the memset is complete. The operation can optionally be associated to a stream by passing a non-zero stream argument. If stream is non-zero, the operation may overlap with operations in other streams.

##### Parameters

out	<i>dst</i>	Data ptr to be filled
in	<i>constant</i>	value to be set
in	<i>number</i>	of values to be set
in	<i>stream</i>	- Stream identifier

##### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

#### 4.9.2.65 hipMemsetD32()

```
hipError_t hipMemsetD32 (
    hipDeviceptr_t dest,
    int value,
    size_t count )
```

Fills the memory area pointed to by dest with the constant integer value for specified number of times.

##### Parameters

out	<i>dst</i>	Data being filled
in	<i>constant</i>	value to be set
in	<i>number</i>	of values to be set

##### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

#### 4.9.2.66 hipMemsetD32Async()

```
hipError_t hipMemsetD32Async (
    hipDeviceptr_t dst,
    int value,
    size_t count,
    hipStream_t stream __dparm0 )
```

Fills the memory area pointed to by dev with the constant integer value for specified number of times.

[hipMemsetD32Async\(\)](#) is asynchronous with respect to the host, so the call may return before the memset is complete. The operation can optionally be associated to a stream by passing a non-zero stream argument. If stream is non-zero, the operation may overlap with operations in other streams.

## Parameters

out	<i>dst</i>	Pointer to device memory
in	<i>value</i>	- Value to set for each byte of specified memory
in	<i>count</i>	- number of values to be set
in	<i>stream</i>	- Stream identifier

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorMemoryFree

## 4.9.2.67 hipMemsetD8()

```
hipError_t hipMemsetD8 (
    hipDeviceptr_t dest,
    unsigned char value,
    size_t count )
```

Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant byte value value.

## Parameters

out	<i>dst</i>	Data ptr to be filled
in	<i>constant</i>	value to be set
in	<i>number</i>	of values to be set

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

## 4.9.2.68 hipMemsetD8Async()

```
hipError_t hipMemsetD8Async (
    hipDeviceptr_t dest,
    unsigned char value,
    size_t count,
    hipStream_t stream __dparm0 )
```

Fills the first sizeBytes bytes of the memory area pointed to by dest with the constant byte value value.

[hipMemsetD8Async\(\)](#) is asynchronous with respect to the host, so the call may return before the memset is complete. The operation can optionally be associated to a stream by passing a non-zero stream argument. If stream is non-zero, the operation may overlap with operations in other streams.

## Parameters

out	<i>dst</i>	Data ptr to be filled
in	<i>constant</i>	value to be set
in	<i>number</i>	of values to be set
in	<i>stream</i>	- Stream identifier

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotInitialized

#### 4.9.2.69 hipModuleGetGlobal()

```
hipError_t hipModuleGetGlobal (
    hipDeviceptr_t * dptr,
    size_t * bytes,
    hipModule_t hmod,
    const char * name )
```

Returns a global pointer from a module. Returns in \*dptr and \*bytes the pointer and size of the global of name name located in module hmod. If no variable of that name exists, it returns hipErrorNotFound. Both parameters dptr and bytes are optional. If one of them is NULL, it is ignored and hipSuccess is returned.

##### Parameters

out	<i>dptr</i>	Returned global device pointer
out	<i>bytes</i>	Returned global size in bytes
in	<i>hmod</i>	Module to retrieve global from
in	<i>name</i>	Name of global to retrieve

##### Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorNotFound, #hipErrorInvalidContext

#### 4.9.2.70 hipPointerGetAttributes()

```
hipError_t hipPointerGetAttributes (
    hipPointerAttribute_t * attributes,
    const void * ptr )
```

Return attributes for the specified pointer.

##### Parameters

out	<i>attributes</i>	for the specified pointer
in	<i>pointer</i>	to get attributes for

##### Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

##### See also

[hipGetDeviceCount](#), [hipGetDevice](#), [hipSetDevice](#), [hipChooseDevice](#)

#### 4.9.2.71 hipSignalExternalSemaphoresAsync()

```
hipError_t hipSignalExternalSemaphoresAsync (
    const hipExternalSemaphore_t * extSemArray,
    const hipExternalSemaphoreSignalParams * paramsArray,
    unsigned int numExtSems,
    hipStream_t stream )
```

Signals a set of external semaphore objects.

##### Parameters

in	<i>extSem_out</i>	External semaphores to be waited on
----	-------------------	-------------------------------------



## Parameters

in	<i>paramsArray</i>	Array of semaphore parameters
in	<i>numExtSems</i>	Number of semaphores to wait on
in	<i>stream</i>	Stream to enqueue the wait operations in

## Returns

#hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue

## See also

**4.9.2.72 hipStreamAttachMemAsync()**

```
hipError_t hipStreamAttachMemAsync (
    hipStream_t stream,
    void * dev_ptr,
    size_t length __dparm0,
    unsigned int flags __dparmhipMemAttachSingle )
```

Attach memory to a stream asynchronously in HIP.

## Parameters

in	<i>stream</i>	- stream in which to enqueue the attach operation
in	<i>dev_ptr</i>	- pointer to memory (must be a pointer to managed memory or to a valid host-accessible region of system-allocated memory)
in	<i>length</i>	- length of memory (defaults to zero)
in	<i>flags</i>	- must be one of hipMemAttachGlobal, hipMemAttachHost or hipMemAttachSingle (defaults to hipMemAttachSingle)

## Returns

#hipSuccess, #hipErrorInvalidValue

**4.9.2.73 hipWaitExternalSemaphoresAsync()**

```
hipError_t hipWaitExternalSemaphoresAsync (
    const hipExternalSemaphore_t * extSemArray,
    const hipExternalSemaphoreWaitParams * paramsArray,
    unsigned int numExtSems,
    hipStream_t stream )
```

Waits on a set of external semaphore objects.

## Parameters

in	<i>extSem_out</i>	External semaphores to be waited on
in	<i>paramsArray</i>	Array of semaphore parameters
in	<i>numExtSems</i>	Number of semaphores to wait on
in	<i>stream</i>	Stream to enqueue the wait operations in

**Returns**

`#hipSuccess`, `#hipErrorInvalidDevice`, `#hipErrorInvalidValue`

**See also**

## 4.10 PeerToPeer Device Memory Access

### Macros

- `#define USE_PEER_NON_UNIFIED 1`

### Functions

- `hipError_t hipDeviceCanAccessPeer` (int \*canAccessPeer, int deviceId, int peerDeviceId)  
*Determine if a device can access a peer's memory.*
- `hipError_t hipDeviceEnablePeerAccess` (int peerDeviceId, unsigned int flags)  
*Enable direct access from current device's virtual address space to memory allocations physically located on a peer device.*
- `hipError_t hipDeviceDisablePeerAccess` (int peerDeviceId)  
*Disable direct access from current device's virtual address space to memory allocations physically located on a peer device.*
- `hipError_t hipMemGetAddressRange` (hipDeviceptr\_t \*pbase, size\_t \*psize, hipDeviceptr\_t dptr)  
*Get information on memory allocations.*
- `hipError_t hipMemcpyPeer` (void \*dst, int dstDeviceId, const void \*src, int srcDeviceId, size\_t sizeBytes)  
*Copies memory from one device to memory on another device.*
- `hipError_t hipMemcpyPeerAsync` (void \*dst, int dstDeviceId, const void \*src, int srcDevice, size\_t sizeBytes, hipStream\_t stream \_\_dparm(0))  
*Copies memory from one device to memory on another device.*

#### 4.10.1 Detailed Description

##### Warning

PeerToPeer support is experimental. This section describes the PeerToPeer device memory access functions of HIP runtime API.

#### 4.10.2 Function Documentation

##### 4.10.2.1 hipDeviceCanAccessPeer()

```
hipError_t hipDeviceCanAccessPeer (
    int * canAccessPeer,
    int deviceId,
    int peerDeviceId )
```

Determine if a device can access a peer's memory.

##### Parameters

out	<i>canAccessPeer</i>	Returns the peer access capability (0 or 1)
in	<i>device</i>	- device from where memory may be accessed.
in	<i>peerDevice</i>	- device where memory is physically located

Returns "1" in *canAccessPeer* if the specified *device* is capable of directly accessing memory physically located on *peerDevice* , or "0" if not.

Returns "0" in *canAccessPeer* if *deviceId == peerDeviceId*, and both are valid devices : a device is not a peer of itself.

##### Returns

- `#hipSuccess`,
- `#hipErrorInvalidDevice` if *deviceId* or *peerDeviceId* are not valid devices

#### 4.10.2.2 hipDeviceDisablePeerAccess()

```
hipError_t hipDeviceDisablePeerAccess (
    int peerDeviceId )
```

Disable direct access from current device's virtual address space to memory allocations physically located on a peer device.

Returns hipErrorPeerAccessNotEnabled if direct access to memory on peerDevice has not yet been enabled from the current device.

##### Parameters

in	<i>peer↔ DeviceId</i>	
----	---------------------------	--

##### Returns

#hipSuccess, #hipErrorPeerAccessNotEnabled

#### 4.10.2.3 hipDeviceEnablePeerAccess()

```
hipError_t hipDeviceEnablePeerAccess (
    int peerDeviceId,
    unsigned int flags )
```

Enable direct access from current device's virtual address space to memory allocations physically located on a peer device.

Memory which already allocated on peer device will be mapped into the address space of the current device. In addition, all future memory allocations on peerDeviceId will be mapped into the address space of the current device when the memory is allocated. The peer memory remains accessible from the current device until a call to hip↔DeviceDisablePeerAccess or hipDeviceReset.

##### Parameters

in	<i>peer↔ DeviceId</i>	
in	<i>flags</i>	Returns #hipSuccess, #hipErrorInvalidDevice, #hipErrorInvalidValue,

##### Returns

#hipErrorPeerAccessAlreadyEnabled if peer access is already enabled for this device.

#### 4.10.2.4 hipMemcpyPeer()

```
hipError_t hipMemcpyPeer (
    void * dst,
    int dstDeviceId,
    const void * src,
    int srcDeviceId,
    size_t sizeBytes )
```

Copies memory from one device to memory on another device.

##### Parameters

out	<i>dst</i>	- Destination device pointer.
-----	------------	-------------------------------

## Parameters

in	<i>dst↔ DeviceId</i>	- Destination device
in	<i>src</i>	- Source device pointer
in	<i>src↔ DeviceId</i>	- Source device
in	<i>sizeBytes</i>	- Size of memory copy in bytes

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidDevice

## 4.10.2.5 hipMemcpyPeerAsync()

```
hipError_t hipMemcpyPeerAsync (
    void * dst,
    int dstDeviceId,
    const void * src,
    int srcDevice,
    size_t sizeBytes,
    hipStream_t stream __dparm0 )
```

Copies memory from one device to memory on another device.

## Parameters

out	<i>dst</i>	- Destination device pointer.
in	<i>dstDevice</i>	- Destination device
in	<i>src</i>	- Source device pointer
in	<i>srcDevice</i>	- Source device
in	<i>sizeBytes</i>	- Size of memory copy in bytes
in	<i>stream</i>	- Stream identifier

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidDevice

## 4.10.2.6 hipMemGetAddressRange()

```
hipError_t hipMemGetAddressRange (
    hipDeviceptr_t * pbase,
    size_t * psize,
    hipDeviceptr_t dptr )
```

Get information on memory allocations.

## Parameters

out	<i>pbase</i>	- BAsE pointer address
out	<i>psize</i>	- Size of allocation
in	<i>dptr-</i>	Device Pointer

**Returns**

`#hipSuccess`, `#hipErrorInvalidDevicePointer`

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

## 4.11 Context Management

### Modules

- [Context Management \[Deprecated\]](#)

### Functions

- `hipError_t hipDevicePrimaryCtxGetState` (`hipDevice_t dev`, `unsigned int *flags`, `int *active`)  
*Get the state of the primary context.*
- `hipError_t hipDevicePrimaryCtxRelease` (`hipDevice_t dev`)  
*Release the primary context on the GPU.*
- `hipError_t hipDevicePrimaryCtxRetain` (`hipCtx_t *pctx`, `hipDevice_t dev`)  
*Retain the primary context on the GPU.*
- `hipError_t hipDevicePrimaryCtxReset` (`hipDevice_t dev`)  
*Resets the primary context on the GPU.*
- `hipError_t hipDevicePrimaryCtxSetFlags` (`hipDevice_t dev`, `unsigned int flags`)  
*Set flags for the primary context.*

#### 4.11.1 Detailed Description

---

This section describes the context management functions of HIP runtime API.

#### 4.11.2 Function Documentation

##### 4.11.2.1 `hipDevicePrimaryCtxGetState()`

```
hipError_t hipDevicePrimaryCtxGetState (
    hipDevice_t dev,
    unsigned int * flags,
    int * active )
```

Get the state of the primary context.

##### Parameters

in	<i>Device</i>	to get primary context flags for
out	<i>Pointer</i>	to store flags
out	<i>Pointer</i>	to store context state; 0 = inactive, 1 = active

##### Returns

`#hipSuccess`

##### See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

##### 4.11.2.2 `hipDevicePrimaryCtxRelease()`

```
hipError_t hipDevicePrimaryCtxRelease (
    hipDevice_t dev )
```

Release the primary context on the GPU.

**Parameters**

in	<i>Device</i>	which primary context is released
----	---------------	-----------------------------------

**Returns**

#hipSuccess

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**Warning**

This function return #hipSuccess though doesn't release the primaryCtx by design on HIP/HCC path.

**4.11.2.3 hipDevicePrimaryCtxReset()**

```
hipError_t hipDevicePrimaryCtxReset (
    hipDevice_t dev )
```

Resets the primary context on the GPU.

**Parameters**

in	<i>Device</i>	which primary context is reset
----	---------------	--------------------------------

**Returns**

#hipSuccess

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.11.2.4 hipDevicePrimaryCtxRetain()**

```
hipError_t hipDevicePrimaryCtxRetain (
    hipCtx_t * pctx,
    hipDevice_t dev )
```

Retain the primary context on the GPU.

**Parameters**

out	<i>Returned</i>	context handle of the new context
in	<i>Device</i>	which primary context is released

**Returns**

#hipSuccess



See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

#### 4.11.2.5 hipDevicePrimaryCtxSetFlags()

```
hipError_t hipDevicePrimaryCtxSetFlags (
    hipDevice_t dev,
    unsigned int flags )
```

Set flags for the primary context.

Parameters

in	<i>Device</i>	for which the primary context flags are set
in	<i>New</i>	flags for the device

Returns

#hipSuccess, #hipErrorContextAlreadyInUse

See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

## 4.12 Module Management

### Functions

- HIP\_PUBLIC\_API hipError\_t [hipExtModuleLaunchKernel](#) (hipFunction\_t f, uint32\_t globalWorkSizeX, uint32\_t globalWorkSizeY, uint32\_t globalWorkSizeZ, uint32\_t localWorkSizeX, uint32\_t localWorkSizeY, uint32\_t localWorkSizeZ, size\_t sharedMemBytes, hipStream\_t hStream, void \*\*kernelParams, void \*\*extra, hipEvent\_t startEvent=nullptr, hipEvent\_t stopEvent=nullptr, uint32\_t flags=0)  
*launches kernel f with launch parameters and shared memory on stream with arguments passed to kernelparams or extra*
- HIP\_PUBLIC\_API hipError\_t **hipHccModuleLaunchKernel** (hipFunction\_t f, uint32\_t globalWorkSizeX, uint32\_t globalWorkSizeY, uint32\_t globalWorkSizeZ, uint32\_t localWorkSizeX, uint32\_t localWorkSizeY, uint32\_t localWorkSizeZ, size\_t sharedMemBytes, hipStream\_t hStream, void \*\*kernelParams, void \*\*extra, hipEvent\_t startEvent=nullptr, hipEvent\_t stopEvent=nullptr) `__attribute__((deprecated("use hipExtModuleLaunchKernel instead")))`
- hipError\_t [hipModuleLoad](#) (hipModule\_t \*module, const char \*fname)  
*Loads code object from file into a hipModule\_t.*
- hipError\_t [hipModuleUnload](#) (hipModule\_t module)  
*Frees the module.*
- hipError\_t [hipModuleGetFunction](#) (hipFunction\_t \*function, hipModule\_t module, const char \*kname)  
*Function with kname will be extracted if present in module.*
- hipError\_t [hipFuncGetAttributes](#) (struct [hipFuncAttributes](#) \*attr, const void \*func)  
*Find out attributes for a given function.*
- hipError\_t [hipFuncGetAttribute](#) (int \*value, hipFunction\_attribute attrib, hipFunction\_t hfunc)  
*Find out a specific attribute for a given function.*
- hipError\_t [hipModuleGetTexRef](#) ([textureReference](#) \*\*texRef, hipModule\_t hmod, const char \*name)  
*returns the handle of the texture reference with the name from the module.*
- hipError\_t [hipModuleLoadData](#) (hipModule\_t \*module, const void \*image)  
*builds module from code object which resides in host memory. Image is pointer to that location.*
- hipError\_t [hipModuleLoadDataEx](#) (hipModule\_t \*module, const void \*image, unsigned int numOptions, hipJitOption \*options, void \*\*optionValues)  
*builds module from code object which resides in host memory. Image is pointer to that location. Options are not used. hipModuleLoadData is called.*
- hipError\_t [hipModuleLaunchKernel](#) (hipFunction\_t f, unsigned int gridDimX, unsigned int gridDimY, unsigned int gridDimZ, unsigned int blockDimX, unsigned int blockDimY, unsigned int blockDimZ, unsigned int sharedMemBytes, hipStream\_t stream, void \*\*kernelParams, void \*\*extra)  
*launches kernel f with launch parameters and shared memory on stream with arguments passed to kernelparams or extra*
- hipError\_t [hipLaunchCooperativeKernel](#) (const void \*f, [dim3](#) gridDim, [dim3](#) blockDimX, void \*\*kernelParams, unsigned int sharedMemBytes, hipStream\_t stream)  
*launches kernel f with launch parameters and shared memory on stream with arguments passed to kernelparams or extra, where thread blocks can cooperate and synchronize as they execute*
- hipError\_t [hipLaunchCooperativeKernelMultiDevice](#) ([hipLaunchParams](#) \*launchParamsList, int numDevices, unsigned int flags)  
*Launches kernels on multiple devices where thread blocks can cooperate and synchronize as they execute.*
- hipError\_t [hipExtLaunchMultiKernelMultiDevice](#) ([hipLaunchParams](#) \*launchParamsList, int numDevices, unsigned int flags)  
*Launches kernels on multiple devices and guarantees all specified kernels are dispatched on respective streams before enqueueing any other work on the specified streams from any other threads.*

### 4.12.1 Detailed Description

This section describes the module management functions of HIP runtime API.

## 4.12.2 Function Documentation

### 4.12.2.1 hipExtLaunchMultiKernelMultiDevice()

```
hipError_t hipExtLaunchMultiKernelMultiDevice (
    hipLaunchParams * launchParamsList,
    int numDevices,
    unsigned int flags )
```

Launches kernels on multiple devices and guarantees all specified kernels are dispatched on respective streams before enqueueing any other work on the specified streams from any other threads.

#### Parameters

in	<i>hipLaunchParams</i>	List of launch parameters, one per device.
in	<i>numDevices</i>	Size of the launchParamsList array.
in	<i>flags</i>	Flags to control launch behavior.

#### Returns

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

### 4.12.2.2 hipExtModuleLaunchKernel()

```
HIP_PUBLIC_API hipError_t hipExtModuleLaunchKernel (
    hipFunction_t f,
    uint32_t globalWorkSizeX,
    uint32_t globalWorkSizeY,
    uint32_t globalWorkSizeZ,
    uint32_t localWorkSizeX,
    uint32_t localWorkSizeY,
    uint32_t localWorkSizeZ,
    size_t sharedMemBytes,
    hipStream_t hStream,
    void ** kernelParams,
    void ** extra,
    hipEvent_t startEvent = nullptr,
    hipEvent_t stopEvent = nullptr,
    uint32_t flags = 0 )
```

launches kernel f with launch parameters and shared memory on stream with arguments passed to kernelparams or extra

#### Parameters

	<i>[in]</i>	f Kernel to launch.
in	<i>gridDimX</i>	X grid dimension specified in work-items
in	<i>gridDimY</i>	Y grid dimension specified in work-items
in	<i>gridDimZ</i>	Z grid dimension specified in work-items
in	<i>blockDimX</i>	X block dimensions specified in work-items
in	<i>blockDimY</i>	Y grid dimension specified in work-items
in	<i>blockDimZ</i>	Z grid dimension specified in work-items
in	<i>sharedMemBytes</i>	Amount of dynamic shared memory to allocate for this kernel. The HIP-Clang compiler provides support for extern shared declarations
in	<i>stream</i>	Stream where the kernel should be dispatched. May be 0, in which case the default stream is used with associated synchronization rules.

**Parameters**

in	<i>kernelParams</i>	
in	<i>extra</i>	Pointer to kernel arguments. These are passed directly to the kernel and must be in the memory layout and alignment expected by the kernel.
in	<i>startEvent</i>	If non-null, specified event will be updated to track the start time of the kernel launch. The event must be created before calling this API.
in	<i>stopEvent</i>	If non-null, specified event will be updated to track the stop time of the kernel launch. The event must be created before calling this API.

**Returns**

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

**Warning**

kernelParams argument is not yet implemented in HIP. Please use extra instead. Please refer to [hip\\_porting\\_driver\\_api.md](#) for sample usage. HIP/ROCm actually updates the start event when the associated kernel completes. Currently, timing between startEvent and stopEvent does not include the time it takes to perform a system scope release / cache flush - only the time it takes to issues writes to cache.

**4.12.2.3 hipFuncGetAttribute()**

```
hipError_t hipFuncGetAttribute (
    int * value,
    hipFunction_attribute attrib,
    hipFunction_t hfunc )
```

Find out a specific attribute for a given function.

**Parameters**

out	<i>value</i>	
in	<i>attrib</i>	
in	<i>hfunc</i>	

**Returns**

hipSuccess, hipErrorInvalidValue, hipErrorInvalidDeviceFunction

**4.12.2.4 hipFuncGetAttributes()**

```
hipError_t hipFuncGetAttributes (
    struct hipFuncAttributes * attr,
    const void * func )
```

Find out attributes for a given function.

**Parameters**

out	<i>attr</i>	
in	<i>func</i>	

## Returns

hipSuccess, hipErrorInvalidValue, hipErrorInvalidDeviceFunction

## 4.12.2.5 hipLaunchCooperativeKernel()

```
hipError_t hipLaunchCooperativeKernel (
    const void * f,
    dim3 gridDim,
    dim3 blockDimX,
    void ** kernelParams,
    unsigned int sharedMemBytes,
    hipStream_t stream )
```

launches kernel *f* with launch parameters and shared memory on stream with arguments passed to *kernelParams* or extra, where thread blocks can cooperate and synchronize as they execute

## Parameters

in	<i>f</i>	Kernel to launch.
in	<i>gridDim</i>	Grid dimensions specified as multiple of <i>blockDim</i> .
in	<i>blockDim</i>	Block dimensions specified in work-items
in	<i>kernelParams</i>	A list of kernel arguments
in	<i>sharedMemBytes</i>	Amount of dynamic shared memory to allocate for this kernel. The HIP-Clang compiler provides support for extern shared declarations.
in	<i>stream</i>	Stream where the kernel should be dispatched. May be 0, in which case the default stream is used with associated synchronization rules.

## Returns

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue, hipErrorCooperativeLaunchToo↵  
Large

## 4.12.2.6 hipLaunchCooperativeKernelMultiDevice()

```
hipError_t hipLaunchCooperativeKernelMultiDevice (
    hipLaunchParams * launchParamsList,
    int numDevices,
    unsigned int flags )
```

Launches kernels on multiple devices where thread blocks can cooperate and synchronize as they execute.

## Parameters

in	<i>launchParamsList</i>	List of launch parameters, one per device.
in	<i>numDevices</i>	Size of the <i>launchParamsList</i> array.
in	<i>flags</i>	Flags to control launch behavior.

**Returns**

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue, hipErrorCooperativeLaunchToo↵  
Large

**4.12.2.7 hipModuleGetFunction()**

```
hipError_t hipModuleGetFunction (
    hipFunction_t * function,
    hipModule_t module,
    const char * kname )
```

Function with kname will be extracted if present in module.

**Parameters**

in	<i>module</i>	
in	<i>kname</i>	
out	<i>function</i>	

**Returns**

hipSuccess, hipErrorInvalidValue, hipErrorInvalidContext, hipErrorNotInitialized, hipErrorNotFound,

**4.12.2.8 hipModuleGetTexRef()**

```
hipError_t hipModuleGetTexRef (
    textureReference ** texRef,
    hipModule_t hmod,
    const char * name )
```

returns the handle of the texture reference with the name from the module.

**Parameters**

in	<i>hmod</i>	
in	<i>name</i>	
out	<i>texRef</i>	

**Returns**

hipSuccess, hipErrorNotInitialized, hipErrorNotFound, hipErrorInvalidValue

**4.12.2.9 hipModuleLaunchKernel()**

```
hipError_t hipModuleLaunchKernel (
    hipFunction_t f,
    unsigned int gridDimX,
    unsigned int gridDimY,
    unsigned int gridDimZ,
    unsigned int blockDimX,
    unsigned int blockDimY,
    unsigned int blockDimZ,
    unsigned int sharedMemBytes,
    hipStream_t stream,
```

```
void ** kernelParams,
void ** extra )
```

launches kernel *f* with launch parameters and shared memory on stream with arguments passed to *kernelParams* or *extra*

#### Parameters

in	<i>f</i>	Kernel to launch.
in	<i>gridDimX</i>	X grid dimension specified as multiple of <i>blockDimX</i> .
in	<i>gridDimY</i>	Y grid dimension specified as multiple of <i>blockDimY</i> .
in	<i>gridDimZ</i>	Z grid dimension specified as multiple of <i>blockDimZ</i> .
in	<i>blockDimX</i>	X block dimensions specified in work-items
in	<i>blockDimY</i>	Y grid dimension specified in work-items
in	<i>blockDimZ</i>	Z grid dimension specified in work-items
in	<i>sharedMemBytes</i>	Amount of dynamic shared memory to allocate for this kernel. The HIP-Clang compiler provides support for extern shared declarations.
in	<i>stream</i>	Stream where the kernel should be dispatched. May be 0, in which case the default stream is used with associated synchronization rules.
in	<i>kernelParams</i>	
in	<i>extra</i>	Pointer to kernel arguments. These are passed directly to the kernel and must be in the memory layout and alignment expected by the kernel.

#### Returns

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

#### Warning

*kernelParams* argument is not yet implemented in HIP. Please use *extra* instead. Please refer to [hip\\_porting↔\\_driver\\_api.md](#) for sample usage.

#### 4.12.2.10 hipModuleLoad()

```
hipError_t hipModuleLoad (
    hipModule_t * module,
    const char * fname )
```

Loads code object from file into a *hipModule\_t*.

#### Parameters

in	<i>fname</i>	
out	<i>module</i>	

#### Returns

hipSuccess, hipErrorInvalidValue, hipErrorInvalidContext, hipErrorFileNotFound, hipErrorOutOfMemory, [hip↔ErrorSharedObjectInitFailed](#), hipErrorNotInitialized

#### 4.12.2.11 hipModuleLoadData()

```
hipError_t hipModuleLoadData (
    hipModule_t * module,
    const void * image )
```

builds module from code object which resides in host memory. Image is pointer to that location.

#### Parameters

in	<i>image</i>	
out	<i>module</i>	

#### Returns

hipSuccess, hipErrorNotInitialized, hipErrorOutOfMemory, hipErrorNotInitialized

#### 4.12.2.12 hipModuleLoadDataEx()

```
hipError_t hipModuleLoadDataEx (
    hipModule_t * module,
    const void * image,
    unsigned int numOptions,
    hipJitOption * options,
    void ** optionValues )
```

builds module from code object which resides in host memory. Image is pointer to that location. Options are not used. hipModuleLoadData is called.

#### Parameters

in	<i>image</i>	
out	<i>module</i>	
in	<i>number</i>	of options
in	<i>options</i>	for JIT
in	<i>option</i>	values for JIT

#### Returns

hipSuccess, hipErrorNotInitialized, hipErrorOutOfMemory, hipErrorNotInitialized

#### 4.12.2.13 hipModuleUnload()

```
hipError_t hipModuleUnload (
    hipModule_t module )
```

Frees the module.

#### Parameters

in	<i>module</i>	
----	---------------	--

#### Returns

hipSuccess, hipInvalidValue module is freed and the code objects associated with it are destroyed



## 4.13 Occupancy

### Functions

- hipError\_t [hipModuleOccupancyMaxPotentialBlockSize](#) (int \*gridSize, int \*blockSize, hipFunction\_t f, size\_t dynSharedMemPerBlk, int blockSizeLimit)  
*determine the grid and block sizes to achieves maximum occupancy for a kernel*
- hipError\_t [hipModuleOccupancyMaxPotentialBlockSizeWithFlags](#) (int \*gridSize, int \*blockSize, hipFunction\_t f, size\_t dynSharedMemPerBlk, int blockSizeLimit, unsigned int flags)  
*determine the grid and block sizes to achieves maximum occupancy for a kernel*
- hipError\_t [hipModuleOccupancyMaxActiveBlocksPerMultiprocessor](#) (int \*numBlocks, hipFunction\_t f, int blockSize, size\_t dynSharedMemPerBlk)  
*Returns occupancy for a device function.*
- hipError\_t [hipModuleOccupancyMaxActiveBlocksPerMultiprocessorWithFlags](#) (int \*numBlocks, hipFunction\_t f, int blockSize, size\_t dynSharedMemPerBlk, unsigned int flags)  
*Returns occupancy for a device function.*
- hipError\_t [hipOccupancyMaxActiveBlocksPerMultiprocessor](#) (int \*numBlocks, const void \*f, int blockSize, size\_t dynSharedMemPerBlk)  
*Returns occupancy for a device function.*
- hipError\_t [hipOccupancyMaxActiveBlocksPerMultiprocessorWithFlags](#) (int \*numBlocks, const void \*f, int blockSize, size\_t dynSharedMemPerBlk, unsigned int flags \_\_dparm(hipOccupancyDefault))  
*Returns occupancy for a device function.*
- hipError\_t [hipOccupancyMaxPotentialBlockSize](#) (int \*gridSize, int \*blockSize, const void \*f, size\_t dynSharedMemPerBlk, int blockSizeLimit)  
*determine the grid and block sizes to achieves maximum occupancy for a kernel*

### 4.13.1 Detailed Description

This section describes the occupancy functions of HIP runtime API.

### 4.13.2 Function Documentation

#### 4.13.2.1 hipModuleOccupancyMaxActiveBlocksPerMultiprocessor()

```
hipError_t hipModuleOccupancyMaxActiveBlocksPerMultiprocessor (
    int * numBlocks,
    hipFunction_t f,
    int blockSize,
    size_t dynSharedMemPerBlk )
```

Returns occupancy for a device function.

#### Parameters

out	<i>numBlocks</i>	Returned occupancy
in	<i>func</i>	Kernel function (hipFunction) for which occupancy is calculated
in	<i>blockSize</i>	Block size the kernel is intended to be launched with
in	<i>dynSharedMemPerBlk</i>	dynamic shared memory usage (in bytes) intended for each block

#### 4.13.2.2 hipModuleOccupancyMaxActiveBlocksPerMultiprocessorWithFlags()

```
hipError_t hipModuleOccupancyMaxActiveBlocksPerMultiprocessorWithFlags (
    int * numBlocks,
```

```

hipFunction_t f,
int blockSize,
size_t dynSharedMemPerBlk,
unsigned int flags )

```

Returns occupancy for a device function.

#### Parameters

out	<i>numBlocks</i>	Returned occupancy
in	<i>f</i>	Kernel function(hipFunction_t) for which occupancy is calculated
in	<i>blockSize</i>	Block size the kernel is intended to be launched with
in	<i>dynSharedMemPerBlk</i>	dynamic shared memory usage (in bytes) intended for each block
in	<i>flags</i>	Extra flags for occupancy calculation (only default supported)

#### 4.13.2.3 hipModuleOccupancyMaxPotentialBlockSize()

```

hipError_t hipModuleOccupancyMaxPotentialBlockSize (
    int * gridSize,
    int * blockSize,
    hipFunction_t f,
    size_t dynSharedMemPerBlk,
    int blockSizeLimit )

```

determine the grid and block sizes to achieves maximum occupancy for a kernel

#### Parameters

out	<i>gridSize</i>	minimum grid size for maximum potential occupancy
out	<i>blockSize</i>	block size for maximum potential occupancy
in	<i>f</i>	kernel function for which occupancy is calculated
in	<i>dynSharedMemPerBlk</i>	dynamic shared memory usage (in bytes) intended for each block
in	<i>blockSizeLimit</i>	the maximum block size for the kernel, use 0 for no limit

#### Returns

hipSuccess, hipInvalidDevice, hipErrorInvalidValue

#### 4.13.2.4 hipModuleOccupancyMaxPotentialBlockSizeWithFlags()

```

hipError_t hipModuleOccupancyMaxPotentialBlockSizeWithFlags (
    int * gridSize,
    int * blockSize,
    hipFunction_t f,
    size_t dynSharedMemPerBlk,
    int blockSizeLimit,
    unsigned int flags )

```

determine the grid and block sizes to achieves maximum occupancy for a kernel

#### Parameters

out	<i>gridSize</i>	minimum grid size for maximum potential occupancy
out	<i>blockSize</i>	block size for maximum potential occupancy
in	<i>f</i>	kernel function for which occupancy is calculated

## Parameters

in	<i>dynSharedMemPerBlk</i>	dynamic shared memory usage (in bytes) intended for each block
in	<i>blockSizeLimit</i>	the maximum block size for the kernel, use 0 for no limit
in	<i>flags</i>	Extra flags for occupancy calculation (only default supported)

## Returns

hipSuccess, hipInvalidDevice, hipErrorInvalidValue

**4.13.2.5 hipOccupancyMaxActiveBlocksPerMultiprocessor()**

```
hipError_t hipOccupancyMaxActiveBlocksPerMultiprocessor (
    int * numBlocks,
    const void * f,
    int blockSize,
    size_t dynSharedMemPerBlk )
```

Returns occupancy for a device function.

## Parameters

out	<i>numBlocks</i>	Returned occupancy
in	<i>func</i>	Kernel function for which occupancy is calculated
in	<i>blockSize</i>	Block size the kernel is intended to be launched with
in	<i>dynSharedMemPerBlk</i>	dynamic shared memory usage (in bytes) intended for each block

**4.13.2.6 hipOccupancyMaxActiveBlocksPerMultiprocessorWithFlags()**

```
hipError_t hipOccupancyMaxActiveBlocksPerMultiprocessorWithFlags (
    int * numBlocks,
    const void * f,
    int blockSize,
    size_t dynSharedMemPerBlk,
    unsigned int flags __dparmhipOccupancyDefault )
```

Returns occupancy for a device function.

## Parameters

out	<i>numBlocks</i>	Returned occupancy
in	<i>f</i>	Kernel function for which occupancy is calculated
in	<i>blockSize</i>	Block size the kernel is intended to be launched with
in	<i>dynSharedMemPerBlk</i>	dynamic shared memory usage (in bytes) intended for each block
in	<i>flags</i>	Extra flags for occupancy calculation (currently ignored)

**4.13.2.7 hipOccupancyMaxPotentialBlockSize()**

```
hipError_t hipOccupancyMaxPotentialBlockSize (
    int * gridSize,
    int * blockSize,
    const void * f,
```

```
size_t dynSharedMemPerBlk,  
int blockSizeLimit )
```

determine the grid and block sizes to achieves maximum occupancy for a kernel

#### Parameters

out	<i>gridSize</i>	minimum grid size for maximum potential occupancy
out	<i>blockSize</i>	block size for maximum potential occupancy
in	<i>f</i>	kernel function for which occupancy is calulated
in	<i>dynSharedMemPerBlk</i>	dynamic shared memory usage (in bytes) intended for each block
in	<i>blockSizeLimit</i>	the maximum block size for the kernel, use 0 for no limit

#### Returns

hipSuccess, hipInvalidDevice, hipErrorInvalidValue

## 4.14 Profiler Control[Deprecated]

### Functions

- `hipError_t hipProfilerStart ()`  
*Start recording of profiling information When using this API, start the profiler with profiling disabled. (–startdisabled)*
- `hipError_t hipProfilerStop ()`  
*Stop recording of profiling information. When using this API, start the profiler with profiling disabled. (–startdisabled)*

### 4.14.1 Detailed Description

---

This section describes the profiler control functions of HIP runtime API.

#### Warning

The `cudaProfilerInitialize` API format for "configFile" is not supported.

### 4.14.2 Function Documentation

#### 4.14.2.1 hipProfilerStart()

```
hipError_t hipProfilerStart ( )
```

Start recording of profiling information When using this API, start the profiler with profiling disabled. (–startdisabled)

#### Warning

: `hipProfilerStart` API is under development.

#### 4.14.2.2 hipProfilerStop()

```
hipError_t hipProfilerStop ( )
```

Stop recording of profiling information. When using this API, start the profiler with profiling disabled. (–startdisabled)

#### Warning

: `hipProfilerStop` API is under development.

## 4.15 Launch API to support the triple-chevron syntax

### Functions

- `hipError_t hipConfigureCall (dim3 gridDim, dim3 blockDim, size_t sharedMem __dparm(0), hipStream_t stream __dparm(0))`  
*Configure a kernel launch.*
- `hipError_t hipSetupArgument (const void *arg, size_t size, size_t offset)`  
*Set a kernel argument.*
- `hipError_t hipLaunchByPtr (const void *func)`  
*Launch a kernel.*
- `hipError_t __hipPushCallConfiguration (dim3 gridDim, dim3 blockDim, size_t sharedMem __dparm(0), hipStream_t stream __dparm(0))`  
*Push configuration of a kernel launch.*
- `hipError_t __hipPopCallConfiguration (dim3 *gridDim, dim3 *blockDim, size_t *sharedMem, hipStream_t *stream)`  
*Pop configuration of a kernel launch.*
- `hipError_t hipLaunchKernel (const void *function_address, dim3 numBlocks, dim3 dimBlocks, void **args, size_t sharedMemBytes __dparm(0), hipStream_t stream __dparm(0))`  
*C compliant kernel launch API.*
- `hipError_t hipDrvMemcpy2DUnaligned (const hip_Memcpy2D *pCopy)`
- `hipError_t hipExtLaunchKernel (const void *function_address, dim3 numBlocks, dim3 dimBlocks, void **args, size_t sharedMemBytes, hipStream_t stream, hipEvent_t startEvent, hipEvent_t stopEvent, int flags)`

### 4.15.1 Detailed Description

This section describes the API to support the triple-chevron syntax.

### 4.15.2 Function Documentation

#### 4.15.2.1 \_\_hipPopCallConfiguration()

```
hipError_t __hipPopCallConfiguration (
    dim3 * gridDim,
    dim3 * blockDim,
    size_t * sharedMem,
    hipStream_t * stream )
```

Pop configuration of a kernel launch.

#### Parameters

out	<i>gridDim</i>	grid dimension specified as multiple of blockDim.
out	<i>blockDim</i>	block dimensions specified in work-items
out	<i>sharedMem</i>	Amount of dynamic shared memory to allocate for this kernel. The HIP-Clang compiler provides support for extern shared declarations.
out	<i>stream</i>	Stream where the kernel should be dispatched. May be 0, in which case the default stream is used with associated synchronization rules.

**Returns**

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

**4.15.2.2 \_\_hipPushCallConfiguration()**

```
hipError_t __hipPushCallConfiguration (
    dim3 gridDim,
    dim3 blockDim,
    size_t sharedMem __dparm0,
    hipStream_t stream __dparm0 )
```

Push configuration of a kernel launch.

**Parameters**

in	<i>gridDim</i>	grid dimension specified as multiple of blockDim.
in	<i>blockDim</i>	block dimensions specified in work-items
in	<i>sharedMem</i>	Amount of dynamic shared memory to allocate for this kernel. The HIP-Clang compiler provides support for extern shared declarations.
in	<i>stream</i>	Stream where the kernel should be dispatched. May be 0, in which case the default stream is used with associated synchronization rules.

**Returns**

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

**4.15.2.3 hipConfigureCall()**

```
hipError_t hipConfigureCall (
    dim3 gridDim,
    dim3 blockDim,
    size_t sharedMem __dparm0,
    hipStream_t stream __dparm0 )
```

Configure a kernel launch.

**Parameters**

in	<i>gridDim</i>	grid dimension specified as multiple of blockDim.
in	<i>blockDim</i>	block dimensions specified in work-items
in	<i>sharedMem</i>	Amount of dynamic shared memory to allocate for this kernel. The HIP-Clang compiler provides support for extern shared declarations.
in	<i>stream</i>	Stream where the kernel should be dispatched. May be 0, in which case the default stream is used with associated synchronization rules.

**Returns**

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

**4.15.2.4 hipDrvMemcpy2DUnaligned()**

```
hipError_t hipDrvMemcpy2DUnaligned (
    const hip_Memcpy2D * pCopy )
```

Copies memory for 2D arrays.



## Parameters

<i>pCopy</i>	- Parameters for the memory copy
--------------	----------------------------------

## Returns

#hipSuccess, #hipErrorInvalidValue

**4.15.2.5 hipLaunchByPtr()**

```
hipError_t hipLaunchByPtr (
    const void * func )
```

Launch a kernel.

## Parameters

in	<i>func</i>	Kernel to launch.
----	-------------	-------------------

## Returns

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

**4.15.2.6 hipLaunchKernel()**

```
hipError_t hipLaunchKernel (
    const void * function_address,
    dim3 numBlocks,
    dim3 dimBlocks,
    void ** args,
    size_t sharedMemBytes __dparm0,
    hipStream_t stream __dparm0 )
```

C compliant kernel launch API.

## Parameters

in	<i>function_address</i>	- kernel stub function pointer.
in	<i>numBlocks</i>	- number of blocks
in	<i>dimBlocks</i>	- dimension of a block
in	<i>args</i>	- kernel arguments
in	<i>sharedMemBytes</i>	- Amount of dynamic shared memory to allocate for this kernel. The HIP-Clang compiler provides support for extern shared declarations.
in	<i>stream</i>	- Stream where the kernel should be dispatched. May be 0, in which case the default stream is used with associated synchronization rules.

## Returns

#hipSuccess, #hipErrorInvalidValue, hipInvalidDevice

**4.15.2.7 hipSetupArgument()**

```
hipError_t hipSetupArgument (
    const void * arg,
```

```
size_t size,  
size_t offset )
```

Set a kernel argument.

#### Returns

hipSuccess, hipInvalidDevice, hipErrorNotInitialized, hipErrorInvalidValue

#### Parameters

in	<i>arg</i>	Pointer the argument in host memory.
in	<i>size</i>	Size of the argument.
in	<i>offset</i>	Offset of the argument on the argument stack.

## 4.16 Texture Management

### Modules

- [Texture Management \[Deprecated\]](#)
- [Texture Management \[Not supported\]](#)

### Functions

- `hipError_t hipBindTextureToMipmappedArray` (const [textureReference](#) \*tex, [hipMipmappedArray\\_const\\_t](#) mipmappedArray, const [hipChannelFormatDesc](#) \*desc)
- `hipError_t hipGetTextureReference` (const [textureReference](#) \*\*texref, const void \*symbol)
- `hipError_t hipCreateTextureObject` (hipTextureObject\_t \*pTexObject, const [hipResourceDesc](#) \*pResDesc, const [hipTextureDesc](#) \*pTexDesc, const struct [hipResourceViewDesc](#) \*pResViewDesc)
- `hipError_t hipDestroyTextureObject` (hipTextureObject\_t textureObject)
- `hipError_t hipGetChannelDesc` ([hipChannelFormatDesc](#) \*desc, [hipArray\\_const\\_t](#) array)
- `hipError_t hipGetTextureObjectResourceDesc` ([hipResourceDesc](#) \*pResDesc, hipTextureObject\_t textureObject)
- `hipError_t hipGetTextureObjectResourceViewDesc` (struct [hipResourceViewDesc](#) \*pResViewDesc, hipTextureObject\_t textureObject)
- `hipError_t hipGetTextureObjectTextureDesc` ([hipTextureDesc](#) \*pTexDesc, hipTextureObject\_t textureObject)
- `hipError_t hipTexRefSetAddressMode` ([textureReference](#) \*texRef, int dim, enum hipTextureAddressMode am)
- `hipError_t hipTexRefSetArray` ([textureReference](#) \*tex, [hipArray\\_const\\_t](#) array, unsigned int flags)
- `hipError_t hipTexRefSetFilterMode` ([textureReference](#) \*texRef, enum hipTextureFilterMode fm)
- `hipError_t hipTexRefSetFlags` ([textureReference](#) \*texRef, unsigned int Flags)
- `hipError_t hipTexRefSetFormat` ([textureReference](#) \*texRef, [hipArray\\_Format](#) fmt, int NumPackedComponents)
- `hipError_t hipTexObjectCreate` (hipTextureObject\_t \*pTexObject, const [HIP\\_RESOURCE\\_DESC](#) \*pResDesc, const [HIP\\_TEXTURE\\_DESC](#) \*pTexDesc, const [HIP\\_RESOURCE\\_VIEW\\_DESC](#) \*pResViewDesc)
- `hipError_t hipTexObjectDestroy` (hipTextureObject\_t texObject)
- `hipError_t hipTexObjectGetResourceDesc` ([HIP\\_RESOURCE\\_DESC](#) \*pResDesc, hipTextureObject\_t texObject)
- `hipError_t hipTexObjectGetResourceViewDesc` ([HIP\\_RESOURCE\\_VIEW\\_DESC](#) \*pResViewDesc, hipTextureObject\_t texObject)
- `hipError_t hipTexObjectGetTextureDesc` ([HIP\\_TEXTURE\\_DESC](#) \*pTexDesc, hipTextureObject\_t texObject)

#### 4.16.1 Detailed Description

---

This section describes the texture management functions of HIP runtime API.

## 4.17 Runtime Compilation

### Typedefs

- typedef enum hiprtcResult **hiprtcResult**
- typedef struct \_hiprtcProgram \* **hiprtcProgram**

### Enumerations

- enum **hiprtcResult** {  
 HIPRTC\_SUCCESS = 0, HIPRTC\_ERROR\_OUT\_OF\_MEMORY = 1, HIPRTC\_ERROR\_PROGRAM\_C↵  
 EATION\_FAILURE = 2, HIPRTC\_ERROR\_INVALID\_INPUT = 3,  
 HIPRTC\_ERROR\_INVALID\_PROGRAM = 4, HIPRTC\_ERROR\_INVALID\_OPTION = 5, HIPRTC\_ERRO↵  
 R\_COMPILATION = 6, HIPRTC\_ERROR\_BUILTIN\_OPERATION\_FAILURE = 7,  
 HIPRTC\_ERROR\_NO\_NAME\_EXPRESSIONS\_AFTER\_COMPILATION = 8, HIPRTC\_ERROR\_NO\_LO↵  
 WERED\_NAMES\_BEFORE\_COMPILATION = 9, HIPRTC\_ERROR\_NAME\_EXPRESSION\_NOT\_VALID =  
 10, HIPRTC\_ERROR\_INTERNAL\_ERROR = 11,  
 HIPRTC\_SUCCESS = 0, HIPRTC\_ERROR\_OUT\_OF\_MEMORY = 1, HIPRTC\_ERROR\_PROGRAM\_C↵  
 EATION\_FAILURE = 2, HIPRTC\_ERROR\_INVALID\_INPUT = 3,  
 HIPRTC\_ERROR\_INVALID\_PROGRAM = 4, HIPRTC\_ERROR\_INVALID\_OPTION = 5, HIPRTC\_ERRO↵  
 R\_COMPILATION = 6, HIPRTC\_ERROR\_BUILTIN\_OPERATION\_FAILURE = 7,  
 HIPRTC\_ERROR\_NO\_NAME\_EXPRESSIONS\_AFTER\_COMPILATION = 8, HIPRTC\_ERROR\_NO\_LO↵  
 WERED\_NAMES\_BEFORE\_COMPILATION = 9, HIPRTC\_ERROR\_NAME\_EXPRESSION\_NOT\_VALID =  
 10, HIPRTC\_ERROR\_INTERNAL\_ERROR = 11,  
 HIPRTC\_SUCCESS = 0, HIPRTC\_ERROR\_OUT\_OF\_MEMORY = 1, HIPRTC\_ERROR\_PROGRAM\_C↵  
 EATION\_FAILURE = 2, HIPRTC\_ERROR\_INVALID\_INPUT = 3,  
 HIPRTC\_ERROR\_INVALID\_PROGRAM = 4, HIPRTC\_ERROR\_INVALID\_OPTION = 5, HIPRTC\_ERRO↵  
 R\_COMPILATION = 6, HIPRTC\_ERROR\_BUILTIN\_OPERATION\_FAILURE = 7,  
 HIPRTC\_ERROR\_NO\_NAME\_EXPRESSIONS\_AFTER\_COMPILATION = 8, HIPRTC\_ERROR\_NO\_LO↵  
 WERED\_NAMES\_BEFORE\_COMPILATION = 9, HIPRTC\_ERROR\_NAME\_EXPRESSION\_NOT\_VALID =  
 10, HIPRTC\_ERROR\_INTERNAL\_ERROR = 11 }

### Functions

- const char \* [hiprtcGetErrorString](#) (hiprtcResult result)  
*Returns text string message to explain the error which occurred.*
- hiprtcResult [hiprtcVersion](#) (int \*major, int \*minor)  
*Sets the parameters as major and minor version.*
- hiprtcResult [hiprtcAddNameExpression](#) (hiprtcProgram prog, const char \*name\_expression)  
*Adds the given name exprssion to the runtime compilation program.*
- hiprtcResult [hiprtcCompileProgram](#) (hiprtcProgram prog, int numOptions, const char \*\*options)  
*Compiles the given runtime compilation program.*
- hiprtcResult [hiprtcCreateProgram](#) (hiprtcProgram \*prog, const char \*src, const char \*name, int numHeaders, const char \*\*headers, const char \*\*includeNames)  
*Creates an instance of hiprtcProgram with the given input parameters, and sets the output hiprtcProgram prog with it.*
- hiprtcResult [hiprtcDestroyProgram](#) (hiprtcProgram \*prog)  
*Destroys an instance of given hiprtcProgram.*
- hiprtcResult [hiprtcGetLoweredName](#) (hiprtcProgram prog, const char \*name\_expression, const char \*\*lowered\_name)  
*Gets the lowered (mangled) name from an instance of hiprtcProgram with the given input parameters, and sets the output lowered\_name with it.*
- hiprtcResult [hiprtcGetProgramLog](#) (hiprtcProgram prog, char \*log)  
*Gets the log generated by the runtime compilation program instance.*
- hiprtcResult [hiprtcGetProgramLogSize](#) (hiprtcProgram prog, size\_t \*logSizeRet)  
*Gets the size of log generated by the runtime compilation program instance.*
- hiprtcResult [hiprtcGetCode](#) (hiprtcProgram prog, char \*code)

*Gets the pointer of compilation binary by the runtime compilation program instance.*

- hiprtcResult [hiprtcGetCodeSize](#) (hiprtcProgram prog, size\_t \*codeSizeRet)

*Gets the size of compilation binary by the runtime compilation program instance.*

### 4.17.1 Detailed Description

This section describes the runtime compilation functions of HIP runtime API.

### 4.17.2 Function Documentation

#### 4.17.2.1 hiprtcAddNameExpression()

```
hiprtcResult hiprtcAddNameExpression (
    hiprtcProgram prog,
    const char * name_expression )
```

Adds the given name exprssion to the runtime compilation program.

##### Parameters

in	<i>prog</i>	runtime compilation program instance.
in	<i>name_expression</i>	const char pointer to the name expression.

##### Returns

HIPRTC\_SUCCESS

If const char pointer is NULL, it will return HIPRTC\_ERROR\_INVALID\_INPUT.

##### See also

hiprtcResult

#### 4.17.2.2 hiprtcCompileProgram()

```
hiprtcResult hiprtcCompileProgram (
    hiprtcProgram prog,
    int numOptions,
    const char ** options )
```

Compiles the given runtime compilation program.

##### Parameters

in	<i>prog</i>	runtime compilation program instance.
in	<i>numOptions</i>	number of compiler options.
in	<i>options</i>	compiler options as const array of strins.

##### Returns

HIPRTC\_SUCCESS

If the compiler failed to build the runtime compilation program, it will return HIPRTC\_ERROR\_COMPILATION.

##### See also

hiprtcResult

#### 4.17.2.3 hiprtcCreateProgram()

```
hiprtcResult hiprtcCreateProgram (
    hiprtcProgram * prog,
    const char * src,
    const char * name,
    int numHeaders,
    const char ** headers,
    const char ** includeNames )
```

Creates an instance of hiprtcProgram with the given input parameters, and sets the output hiprtcProgram prog with it.

##### Parameters

in, out	<i>prog</i>	runtime compilation program instance.
in	<i>src</i>	const char pointer to the program source.
in	<i>name</i>	const char pointer to the program name.
in	<i>numHeaders</i>	number of headers.
in	<i>headers</i>	array of strings pointing to headers.
in	<i>includeNames</i>	array of strings pointing to names included in program source.

##### Returns

HIPRTC\_SUCCESS

Any invalide input parameter, it will return HIPRTC\_ERROR\_INVALID\_INPUT or HIPRTC\_ERROR\_INVALID\_PROGRAM.

If failed to create the program, it will return HIPRTC\_ERROR\_PROGRAM\_CREATION\_FAILURE.

##### See also

hiprtcResult

#### 4.17.2.4 hiprtcDestroyProgram()

```
hiprtcResult hiprtcDestroyProgram (
    hiprtcProgram * prog )
```

Destroys an instance of given hiprtcProgram.

##### Parameters

in	<i>prog</i>	runtime compilation program instance.
----	-------------	---------------------------------------

##### Returns

HIPRTC\_SUCCESS

If prog is NULL, it will return HIPRTC\_ERROR\_INVALID\_INPUT.

##### See also

hiprtcResult

#### 4.17.2.5 hiprtcGetCode()

```
hiprtcResult hiprtcGetCode (
    hiprtcProgram prog,
    char * code )
```

Gets the pointer of compilation binary by the runtime compilation program instance.

#### Parameters

in	<i>prog</i>	runtime compilation program instance.
out	<i>code</i>	char pointer to binary.

#### Returns

HIPRTC\_SUCCESS

#### See also

hiprtcResult

#### 4.17.2.6 hiprtcGetCodeSize()

```
hiprtcResult hiprtcGetCodeSize (
    hiprtcProgram prog,
    size_t * codeSizeRet )
```

Gets the size of compilation binary by the runtime compilation program instance.

#### Parameters

in	<i>prog</i>	runtime compilation program instance.
out	<i>code</i>	the size of binary.

#### Returns

HIPRTC\_SUCCESS

#### See also

hiprtcResult

#### 4.17.2.7 hiprtcGetErrorString()

```
const char* hiprtcGetErrorString (
    hiprtcResult result )
```

Returns text string message to explain the error which occurred.

#### Parameters

in	<i>result</i>	code to convert to string.
----	---------------	----------------------------

#### Returns

const char pointer to the NULL-terminated error string

#### Warning

In HIP, this function returns the name of the error, if the hiprtc result is defined, it will return "Invalid HIPRTC error code"

See also

`hiprtcResult`

#### 4.17.2.8 `hiprtcGetLoweredName()`

```
hiprtcResult hiprtcGetLoweredName (
    hiprtcProgram prog,
    const char * name_expression,
    const char ** lowered_name )
```

Gets the lowered (mangled) name from an instance of `hiprtcProgram` with the given input parameters, and sets the output `lowered_name` with it.

Parameters

in	<i>prog</i>	runtime compilation program instance.
in	<i>name_expression</i>	const char pointer to the name expression.
in, out	<i>lowered_name</i>	const char array to the lowered (mangled) name.

Returns

`HIPRTC_SUCCESS`

If any invalide nullptr input parameters, it will return `HIPRTC_ERROR_INVALID_INPUT`

If `name_expression` is not found, it will return `HIPRTC_ERROR_NAME_EXPRESSION_NOT_VALID`

If failed to get `lowered_name` from the program, it will return `HIPRTC_ERROR_COMPILATION`.

See also

`hiprtcResult`

#### 4.17.2.9 `hiprtcGetProgramLog()`

```
hiprtcResult hiprtcGetProgramLog (
    hiprtcProgram prog,
    char * log )
```

Gets the log generated by the runtime compilation program instance.

Parameters

in	<i>prog</i>	runtime compilation program instance.
out	<i>log</i>	memory pointer to the generated log.

Returns

`HIPRTC_SUCCESS`

See also

`hiprtcResult`

#### 4.17.2.10 `hiprtcGetProgramLogSize()`

```
hiprtcResult hiprtcGetProgramLogSize (
    hiprtcProgram prog,
    size_t * logSizeRet )
```



Gets the size of log generated by the runtime compilation program instance.

**Parameters**

in	<i>prog</i>	runtime compilation program instance.
out	<i>logSizeRet</i>	size of generated log.

**Returns**

HIPRTC\_SUCCESS

**See also**

hiprtcResult

**4.17.2.11 hiprtcVersion()**

```
hiprtcResult hiprtcVersion (
    int * major,
    int * minor )
```

Sets the parameters as major and minor version.

**Parameters**

out	<i>major</i>	HIP Runtime Compilation major version.
out	<i>minor</i>	HIP Runtime Compilation minor version.

## 4.18 Callback Activity APIs

### Functions

- hipError\_t **hipRegisterApiCallback** (uint32\_t id, void \*fun, void \*arg)
- hipError\_t **hipRemoveApiCallback** (uint32\_t id)
- hipError\_t **hipRegisterActivityCallback** (uint32\_t id, void \*fun, void \*arg)
- hipError\_t **hipRemoveActivityCallback** (uint32\_t id)
- const char \* **hipApiName** (uint32\_t id)
- const char \* **hipKernelNameRef** (const hipFunction\_t f)
- const char \* **hipKernelNameRefByPtr** (const void \*hostFunction, hipStream\_t stream)
- int **hipGetStreamDeviceId** (hipStream\_t stream)

#### 4.18.1 Detailed Description

This section describes the callback/Activity of HIP runtime API.

## 4.19 Graph Management

### Classes

- struct [hipHostNodeParams](#)
- struct [hipKernelNodeParams](#)
- struct [hipMemsetParams](#)

### Typedefs

- typedef struct ihipGraph \* [hipGraph\\_t](#)
- typedef struct hipGraphNode \* [hipGraphNode\\_t](#)
- typedef struct hipGraphExec \* [hipGraphExec\\_t](#)
- typedef enum [hipGraphNodeType](#) **hipGraphNodeType**
- typedef void(\* [hipHostFn\\_t](#)) (void \*userData)
- typedef struct [hipHostNodeParams](#) **hipHostNodeParams**
- typedef struct [hipKernelNodeParams](#) **hipKernelNodeParams**
- typedef struct [hipMemsetParams](#) **hipMemsetParams**
- typedef enum [hipGraphExecUpdateResult](#) **hipGraphExecUpdateResult**
- typedef enum hipStreamCaptureMode **hipStreamCaptureMode**
- typedef enum [hipStreamCaptureStatus](#) **hipStreamCaptureStatus**

### Enumerations

- enum [hipGraphNodeType](#) {  
[hipGraphNodeTypeKernel](#) = 1, [hipGraphNodeTypeMemcpy](#) = 2, [hipGraphNodeTypeMemset](#) = 3,  
[hipGraphNodeTypeHost](#) = 4,  
[hipGraphNodeTypeGraph](#) = 5, [hipGraphNodeTypeEmpty](#) = 6, [hipGraphNodeTypeWaitEvent](#) = 7,  
[hipGraphNodeTypeEventRecord](#) = 8,  
[hipGraphNodeTypeMemcpy1D](#) = 9, [hipGraphNodeTypeMemcpyFromSymbol](#) = 10, [hipGraphNodeTypeMemcpyToSymbol](#)  
= 11, **hipGraphNodeTypeCount** }
- enum [hipGraphExecUpdateResult](#) {  
[hipGraphExecUpdateSuccess](#) = 0x0, [hipGraphExecUpdateError](#) = 0x1, [hipGraphExecUpdateErrorTopologyChanged](#)  
= 0x2, [hipGraphExecUpdateErrorNodeTypeChanged](#) = 0x3,  
[hipGraphExecUpdateErrorFunctionChanged](#), [hipGraphExecUpdateErrorParametersChanged](#), [hipGraphExecUpdateErrorNotSupported](#),  
**hipGraphExecUpdateErrorUnsupportedFunctionChange** = 0x7 }
- enum **hipStreamCaptureMode** { [hipStreamCaptureModeGlobal](#) = 0, [hipStreamCaptureModeThreadLocal](#), [hipStreamCaptureModeRelaxed](#) }
- enum [hipStreamCaptureStatus](#) { [hipStreamCaptureStatusNone](#) = 0, [hipStreamCaptureStatusActive](#), [hipStreamCaptureStatusInvalidated](#) }

### Functions

- hipError\_t [hipStreamBeginCapture](#) (hipStream\_t stream, hipStreamCaptureMode mode)  
*Begins graph capture on a stream.*
- hipError\_t [hipStreamEndCapture](#) (hipStream\_t stream, [hipGraph\\_t](#) \*pGraph)  
*Ends capture on a stream, returning the captured graph.*
- hipError\_t [hipGraphCreate](#) ([hipGraph\\_t](#) \*pGraph, unsigned int flags)  
*Creates a graph.*
- hipError\_t [hipGraphDestroy](#) ([hipGraph\\_t](#) graph)  
*Destroys a graph.*
- hipError\_t [hipGraphExecDestroy](#) ([hipGraphExec\\_t](#) pGraphExec)  
*Destroys an executable graph.*
- hipError\_t [hipGraphInstantiate](#) ([hipGraphExec\\_t](#) \*pGraphExec, [hipGraph\\_t](#) graph, [hipGraphNode\\_t](#) \*pErrorNode, char \*pLogBuffer, size\_t bufferSize)  
*Creates an executable graph from a graph.*

- hipError\_t [hipGraphLaunch](#) ([hipGraphExec\\_t](#) graphExec, [hipStream\\_t](#) stream)  
*launches an executable graph in a stream*
- hipError\_t [hipGraphAddKernelNode](#) ([hipGraphNode\\_t](#) \*pGraphNode, [hipGraph\\_t](#) graph, const [hipGraphNode\\_t](#) \*pDependencies, size\_t numDependencies, const [hipKernelNodeParams](#) \*pNodeParams)  
*Creates a kernel execution node and adds it to a graph.*
- hipError\_t [hipGraphAddMemcpyNode](#) ([hipGraphNode\\_t](#) \*pGraphNode, [hipGraph\\_t](#) graph, const [hipGraphNode\\_t](#) \*pDependencies, size\_t numDependencies, const [hipMemcpy3DParams](#) \*pCopyParams)  
*Creates a memcpy node and adds it to a graph.*
- hipError\_t [hipGraphAddMemcpyNode1D](#) ([hipGraphNode\\_t](#) \*pGraphNode, [hipGraph\\_t](#) graph, const [hipGraphNode\\_t](#) \*pDependencies, size\_t numDependencies, void \*dst, const void \*src, size\_t count, [hipMemcpyKind](#) kind)  
*Creates a 1D memcpy node and adds it to a graph.*
- hipError\_t [hipGraphAddMemsetNode](#) ([hipGraphNode\\_t](#) \*pGraphNode, [hipGraph\\_t](#) graph, const [hipGraphNode\\_t](#) \*pDependencies, size\_t numDependencies, const [hipMemsetParams](#) \*pMemsetParams)  
*Creates a memset node and adds it to a graph.*
- hipError\_t [hipGraphGetNodes](#) ([hipGraph\\_t](#) graph, [hipGraphNode\\_t](#) \*nodes, size\_t \*numNodes)  
*Returns graph nodes.*
- hipError\_t [hipGraphGetRootNodes](#) ([hipGraph\\_t](#) graph, [hipGraphNode\\_t](#) \*pRootNodes, size\_t \*pNumRootNodes)  
*Returns graph's root nodes.*
- hipError\_t [hipGraphKernelNodeGetParams](#) ([hipGraphNode\\_t](#) node, [hipKernelNodeParams](#) \*pNodeParams)  
*Gets kernel node's parameters.*
- hipError\_t [hipGraphKernelNodeSetParams](#) ([hipGraphNode\\_t](#) node, const [hipKernelNodeParams](#) \*pNodeParams)  
*Sets a kernel node's parameters.*
- hipError\_t [hipGraphMemcpyNodeGetParams](#) ([hipGraphNode\\_t](#) node, [hipMemcpy3DParams](#) \*pNodeParams)  
*Gets a memcpy node's parameters.*
- hipError\_t [hipGraphMemcpyNodeSetParams](#) ([hipGraphNode\\_t](#) node, const [hipMemcpy3DParams](#) \*pNodeParams)  
*Sets a memcpy node's parameters.*
- hipError\_t [hipGraphMemsetNodeGetParams](#) ([hipGraphNode\\_t](#) node, [hipMemsetParams](#) \*pNodeParams)  
*Gets a memset node's parameters.*
- hipError\_t [hipGraphMemsetNodeSetParams](#) ([hipGraphNode\\_t](#) node, const [hipMemsetParams](#) \*pNodeParams)  
*Sets a memset node's parameters.*
- hipError\_t [hipGraphExecKernelNodeSetParams](#) ([hipGraphExec\\_t](#) hGraphExec, [hipGraphNode\\_t](#) node, const [hipKernelNodeParams](#) \*pNodeParams)  
*Sets the parameters for a kernel node in the given graphExec.*
- hipError\_t [hipGraphAddDependencies](#) ([hipGraph\\_t](#) graph, const [hipGraphNode\\_t](#) \*from, const [hipGraphNode\\_t](#) \*to, size\_t numDependencies)  
*Adds dependency edges to a graph.*
- hipError\_t [hipGraphAddEmptyNode](#) ([hipGraphNode\\_t](#) \*pGraphNode, [hipGraph\\_t](#) graph, const [hipGraphNode\\_t](#) \*pDependencies, size\_t numDependencies)  
*Creates an empty node and adds it to a graph.*

#### 4.19.1 Detailed Description

This section describes the graph management types & functions of HIP runtime API.

#### 4.19.2 Typedef Documentation

**4.19.2.1 hipGraph\_t**

```
typedef struct ihipGraph* hipGraph_t
```

An opaque value that represents a hip graph

**4.19.2.2 hipGraphExec\_t**

```
typedef struct hipGraphExec* hipGraphExec_t
```

An opaque value that represents a hip graph Exec

**4.19.2.3 hipGraphNode\_t**

```
typedef struct hipGraphNode* hipGraphNode_t
```

An opaque value that represents a hip graph node

**4.19.3 Enumeration Type Documentation****4.19.3.1 hipGraphExecUpdateResult**

```
enum hipGraphExecUpdateResult
```

Enumerator

hipGraphExecUpdateSuccess	The update succeeded.
hipGraphExecUpdateError	The update failed for an unexpected reason which is described in the return value of the function
hipGraphExecUpdateErrorTopologyChanged	The update failed because the topology changed.
hipGraphExecUpdateErrorNodeTypeChanged	The update failed because a node type changed.
hipGraphExecUpdateErrorFunctionChanged	The update failed because the function of a kernel node changed.
hipGraphExecUpdateErrorParametersChanged	The update failed because the parameters changed in a way that is not supported.
hipGraphExecUpdateErrorNotSupported	The update failed because something about the node is not supported.

**4.19.3.2 hipGraphNodeType**

```
enum hipGraphNodeType
```

Enumerator

hipGraphNodeTypeKernel	GPU kernel node.
hipGraphNodeTypeMemcpy	Memcpy 3D node.
hipGraphNodeTypeMemset	Memset 1D node.
hipGraphNodeTypeHost	Host (executable) node.
hipGraphNodeTypeGraph	Node which executes an embedded graph.
hipGraphNodeTypeEmpty	Empty (no-op) node.
hipGraphNodeTypeWaitEvent	External event wait node.
hipGraphNodeTypeEventRecord	External event record node.
hipGraphNodeTypeMemcpy1D	Memcpy 1D node.
hipGraphNodeTypeMemcpyFromSymbol	MemcpyFromSymbol node.
hipGraphNodeTypeMemcpyToSymbol	MemcpyToSymbol node.

#### 4.19.3.3 hipStreamCaptureStatus

enum `hipStreamCaptureStatus`

##### Enumerator

<code>hipStreamCaptureStatusNone</code>	Stream is not capturing.
<code>hipStreamCaptureStatusActive</code>	Stream is actively capturing.
<code>hipStreamCaptureStatusInvalidated</code>	Stream is part of a capture sequence that has been invalidated, but not terminated

### 4.19.4 Function Documentation

#### 4.19.4.1 hipGraphAddDependencies()

```
hipError_t hipGraphAddDependencies (
    hipGraph_t graph,
    const hipGraphNode_t * from,
    const hipGraphNode_t * to,
    size_t numDependencies )
```

Adds dependency edges to a graph.

##### Parameters

in	<i>graph</i>	- instance of the graph to add dependencies.
in	<i>from</i>	- pointer to the graph nodes with dependencies to add from.
in	<i>to</i>	- pointer to the graph nodes to add dependencies to.
in	<i>numDependencies</i>	- the number of dependencies to add.

##### Returns

`#hipSuccess`, `#hipErrorInvalidValue`

##### Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

#### 4.19.4.2 hipGraphAddEmptyNode()

```
hipError_t hipGraphAddEmptyNode (
    hipGraphNode_t * pGraphNode,
    hipGraph_t graph,
    const hipGraphNode_t * pDependencies,
    size_t numDependencies )
```

Creates an empty node and adds it to a graph.

##### Parameters

out	<i>pGraphNode</i>	- pointer to the graph node to create and add to the graph.
in, out	<i>graph</i>	- instance of the graph the node is add to.

## Parameters

in	<i>pDependencies</i>	- const pointer to the node dependencies.
in	<i>numDependencies</i>	- the number of dependencies.

## Returns

#hipSuccess, #hipErrorInvalidValue

## Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

## 4.19.4.3 hipGraphAddKernelNode()

```
hipError_t hipGraphAddKernelNode (
    hipGraphNode_t * pGraphNode,
    hipGraph_t graph,
    const hipGraphNode_t * pDependencies,
    size_t numDependencies,
    const hipKernelNodeParams * pNodeParams )
```

Creates a kernel execution node and adds it to a graph.

## Parameters

out	<i>pGraphNode</i>	- pointer to graph node to create.
in, out	<i>graph</i>	- instance of graph to add the created node.
in	<i>pDependencies</i>	- pointer to the dependencies on the kernel execution node.
in	<i>numDependencies</i>	- the number of the dependencies.
in	<i>pNodeParams</i>	- pointer to the parameters to the kernel execution node on the GPU.

## Returns

#hipSuccess, #hipErrorInvalidValue, #hipErrorInvalidDeviceFunction

## Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

## 4.19.4.4 hipGraphAddMemcpyNode()

```
hipError_t hipGraphAddMemcpyNode (
    hipGraphNode_t * pGraphNode,
    hipGraph_t graph,
    const hipGraphNode_t * pDependencies,
    size_t numDependencies,
    const hipMemcpy3DParms * pCopyParams )
```

Creates a memcpy node and adds it to a graph.

## Parameters

out	<i>pGraphNode</i>	- pointer to graph node to create.
-----	-------------------	------------------------------------

**Parameters**

in, out	<i>graph</i>	- instance of graph to add the created node.
in	<i>pDependencies</i>	- const pointer to the dependencies on the kernel execution node.
in	<i>numDependencies</i>	- the number of the dependencies.
in	<i>pCopyParams</i>	- const pointer to the parameters for the memory copy.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.5 hipGraphAddMemcpyNode1D()**

```
hipError_t hipGraphAddMemcpyNode1D (
    hipGraphNode_t * pGraphNode,
    hipGraph_t graph,
    const hipGraphNode_t * pDependencies,
    size_t numDependencies,
    void * dst,
    const void * src,
    size_t count,
    hipMemcpyKind kind )
```

Creates a 1D memcpy node and adds it to a graph.

**Parameters**

out	<i>pGraphNode</i>	- pointer to graph node to create.
in, out	<i>graph</i>	- instance of the graph to add the created node.
in	<i>pDependencies</i>	- const pointer to the dependencies on the kernel execution node.
in	<i>numDependencies</i>	- the number of the dependencies.
in	<i>dst</i>	- pointer to memory address to the destination.
in	<i>src</i>	- pointer to memory address to the source.
in	<i>count</i>	- the size of the memory to copy.
in	<i>kind</i>	- the type of memory copy.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.6 hipGraphAddMemsetNode()**

```
hipError_t hipGraphAddMemsetNode (
    hipGraphNode_t * pGraphNode,
```



```
hipGraph_t graph,
const hipGraphNode_t * pDependencies,
size_t numDependencies,
const hipMemsetParams * pMemsetParams )
```

Creates a memset node and adds it to a graph.

#### Parameters

out	<i>pGraphNode</i>	- pointer to the graph node to create.
in, out	<i>graph</i>	- instance of the graph to add the created node.
in	<i>pDependencies</i>	- const pointer to the dependencies on the kernel execution node.
in	<i>numDependencies</i>	- the number of the dependencies.
in	<i>pMemsetParams</i>	- const pointer to the parameters for the memory set.

#### Returns

#hipSuccess, #hipErrorInvalidValue

#### Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

#### 4.19.4.7 hipGraphCreate()

```
hipError_t hipGraphCreate (
    hipGraph_t * pGraph,
    unsigned int flags )
```

Creates a graph.

#### Parameters

out	<i>pGraph</i>	- pointer to graph to create.
in	<i>flags</i>	- flags for graph creation, must be 0.

#### Returns

#hipSuccess.

#### Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

#### 4.19.4.8 hipGraphDestroy()

```
hipError_t hipGraphDestroy (
    hipGraph_t graph )
```

Destroys a graph.

#### Parameters

in	<i>graph</i>	- instance of graph to destroy.
----	--------------	---------------------------------

**Returns**

#hipSuccess.

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.9 hipGraphExecDestroy()**

```
hipError_t hipGraphExecDestroy (
    hipGraphExec_t pGraphExec )
```

Destroys an executable graph.

**Parameters**

in	<i>pGraphExec</i>	- instance of executable graph to destroy.
----	-------------------	--

**Returns**

#hipSuccess.

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.10 hipGraphExecKernelNodeSetParams()**

```
hipError_t hipGraphExecKernelNodeSetParams (
    hipGraphExec_t hGraphExec,
    hipGraphNode_t node,
    const hipKernelNodeParams * pNodeParams )
```

Sets the parameters for a kernel node in the given graphExec.

**Parameters**

in	<i>hGraphExec</i>	- instance of the executable graph with the node.
in	<i>node</i>	- instance of the node to set parameters to.
in	<i>pNodeParams</i>	- const pointer to the kernel node parameters.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.11 hipGraphGetNodes()**

```
hipError_t hipGraphGetNodes (
    hipGraph_t graph,
```

```
hipGraphNode_t * nodes,
size_t * numNodes )
```

Returns graph nodes.

#### Parameters

in	<i>graph</i>	- instance of graph to get the nodes.
out	<i>nodes</i>	- pointer to the graph nodes.
out	<i>numNodes</i>	- the number of graph nodes.

#### Returns

#hipSuccess, #hipErrorInvalidValue

#### Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

#### 4.19.4.12 hipGraphGetRootNodes()

```
hipError_t hipGraphGetRootNodes (
    hipGraph_t graph,
    hipGraphNode_t * pRootNodes,
    size_t * pNumRootNodes )
```

Returns graph's root nodes.

#### Parameters

in	<i>graph</i>	- instance of the graph to get the nodes.
out	<i>pRootNodes</i>	- pointer to the graph's root nodes.
out	<i>pNumRootNodes</i>	- the number of graph's root nodes.

#### Returns

#hipSuccess, #hipErrorInvalidValue

#### Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

#### 4.19.4.13 hipGraphInstantiate()

```
hipError_t hipGraphInstantiate (
    hipGraphExec_t * pGraphExec,
    hipGraph_t graph,
    hipGraphNode_t * pErrorNode,
    char * pLogBuffer,
    size_t bufferSize )
```

Creates an executable graph from a graph.

**Parameters**

out	<i>pGraphExec</i>	- pointer to instantiated executable graph to create.
in	<i>graph</i>	- instance of graph to instantiate.
out	<i>pErrorNode</i>	- pointer to error node in case error occurred in graph instantiation, it could modify the corresponding node.
out	<i>pLogBuffer</i>	- pointer to log buffer.
in	<i>bufferSize</i>	- the size of log buffer.

**Returns**

#hipSuccess, #hipErrorOutOfMemory.

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.14 hipGraphKernelNodeGetParams()**

```
hipError_t hipGraphKernelNodeGetParams (
    hipGraphNode_t node,
    hipKernelNodeParams * pNodeParams )
```

Gets kernel node's parameters.

**Parameters**

in	<i>node</i>	- instance of the node to get parameters from.
out	<i>pNodeParams</i>	- pointer to the parameters

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.15 hipGraphKernelNodeSetParams()**

```
hipError_t hipGraphKernelNodeSetParams (
    hipGraphNode_t node,
    const hipKernelNodeParams * pNodeParams )
```

Sets a kernel node's parameters.

**Parameters**

in	<i>node</i>	- instance of the node to set parameters to.
in	<i>pNodeParams</i>	- const pointer to the parameters.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.16 hipGraphLaunch()**

```
hipError_t hipGraphLaunch (
    hipGraphExec_t graphExec,
    hipStream_t stream )
```

launches an executable graph in a stream

**Parameters**

in	<i>graphExec</i>	- instance of executable graph to launch.
in	<i>stream</i>	- instance of stream in which to launch executable graph.

**Returns**

#hipSuccess, #hipErrorOutOfMemory, #hipErrorInvalidHandle, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.17 hipGraphMemcpyNodeGetParams()**

```
hipError_t hipGraphMemcpyNodeGetParams (
    hipGraphNode_t node,
    hipMemcpy3DParms * pNodeParams )
```

Gets a memcpy node's parameters.

**Parameters**

in	<i>node</i>	- instance of the node to get parameters from.
out	<i>pNodeParams</i>	- pointer to the parameters.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.18 hipGraphMemcpyNodeSetParams()**

```
hipError_t hipGraphMemcpyNodeSetParams (
```

```
hipGraphNode_t node,
const hipMemcpy3DParms * pNodeParams )
```

Sets a memcpy node's parameters.

#### Parameters

in	<i>node</i>	- instance of the node to set parameters to.
in	<i>pNodeParams</i>	- const pointer to the parameters.

#### Returns

#hipSuccess, #hipErrorInvalidValue

#### Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

#### 4.19.4.19 hipGraphMemsetNodeGetParams()

```
hipError_t hipGraphMemsetNodeGetParams (
    hipGraphNode_t node,
    hipMemsetParams * pNodeParams )
```

Gets a memset node's parameters.

#### Parameters

in	<i>node</i>	- instane of the node to get parameters from.
out	<i>pNodeParams</i>	- pointer to the parameters.

#### Returns

#hipSuccess, #hipErrorInvalidValue

#### Warning

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

#### 4.19.4.20 hipGraphMemsetNodeSetParams()

```
hipError_t hipGraphMemsetNodeSetParams (
    hipGraphNode_t node,
    const hipMemsetParams * pNodeParams )
```

Sets a memset node's parameters.

#### Parameters

in	<i>node</i>	- instance of the node to set parameters to.
in	<i>pNodeParams</i>	- pointer to the parameters.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.21 hipStreamBeginCapture()**

```
hipError_t hipStreamBeginCapture (
    hipStream_t stream,
    hipStreamCaptureMode mode )
```

Begins graph capture on a stream.

**Parameters**

in	<i>stream</i>	- Stream to initiate capture.
in	<i>mode</i>	- Controls the interaction of this capture sequence with other API calls that are not safe.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

**4.19.4.22 hipStreamEndCapture()**

```
hipError_t hipStreamEndCapture (
    hipStream_t stream,
    hipGraph_t * pGraph )
```

Ends capture on a stream, returning the captured graph.

**Parameters**

in	<i>stream</i>	- Stream to end capture.
out	<i>pGraph</i>	- returns the graph captured.

**Returns**

#hipSuccess, #hipErrorInvalidValue

**Warning**

: This API is marked as beta, meaning, while this is feature complete, it is still open to changes and may have outstanding issues.

## 4.20 Interop

### Typedefs

- typedef unsigned int **GLuint**

### Functions

- hipError\_t **hipGLGetDevices** (unsigned int \*pHipDeviceCount, int \*pHipDevices, unsigned int hipDeviceCount, [hipGLDeviceList](#) deviceList)
- hipError\_t **hipGraphicsGLRegisterBuffer** (hipGraphicsResource \*\*resource, GLuint buffer, unsigned int flags)
- hipError\_t **hipGraphicsMapResources** (int count, hipGraphicsResource\_t \*resources, hipStream\_t stream \_\_dparm(0))
- hipError\_t **hipGraphicsResourceGetMappedPointer** (void \*\*devPtr, size\_t \*size, hipGraphicsResource\_t resource)
- hipError\_t **hipGraphicsUnmapResources** (int count, hipGraphicsResource\_t \*resources, hipStream\_t stream \_\_dparm(0))
- hipError\_t **hipGraphicsUnregisterResource** (hipGraphicsResource\_t resource)

#### 4.20.1 Detailed Description

---

This section describes Stream Memory Wait and Write functions of HIP runtime API.



## 4.21 Context Management [Deprecated]

### Functions

- hipError\_t [hipCtxCreate](#) (hipCtx\_t \*ctx, unsigned int flags, hipDevice\_t device)  
*Create a context and set it as current/ default context.*
- hipError\_t [hipCtxDestroy](#) (hipCtx\_t ctx)  
*Destroy a HIP context.*
- hipError\_t [hipCtxPopCurrent](#) (hipCtx\_t \*ctx)  
*Pop the current/default context and return the popped context.*
- hipError\_t [hipCtxPushCurrent](#) (hipCtx\_t ctx)  
*Push the context to be set as current/ default context.*
- hipError\_t [hipCtxSetCurrent](#) (hipCtx\_t ctx)  
*Set the passed context as current/default.*
- hipError\_t [hipCtxGetCurrent](#) (hipCtx\_t \*ctx)  
*Get the handle of the current/ default context.*
- hipError\_t [hipCtxGetDevice](#) (hipDevice\_t \*device)  
*Get the handle of the device associated with current/default context.*
- hipError\_t [hipCtxGetApiVersion](#) (hipCtx\_t ctx, int \*apiVersion)  
*Returns the approximate HIP api version.*
- hipError\_t [hipCtxGetCacheConfig](#) (hipFuncCache\_t \*cacheConfig)  
*Set Cache configuration for a specific function.*
- hipError\_t [hipCtxSetCacheConfig](#) (hipFuncCache\_t cacheConfig)  
*Set L1/Shared cache partition.*
- hipError\_t [hipCtxSetSharedMemConfig](#) (hipSharedMemConfig config)  
*Set Shared memory bank configuration.*
- hipError\_t [hipCtxGetSharedMemConfig](#) (hipSharedMemConfig \*pConfig)  
*Get Shared memory bank configuration.*
- hipError\_t [hipCtxSynchronize](#) (void)  
*Blocks until the default context has completed all preceding requested tasks.*
- hipError\_t [hipCtxGetFlags](#) (unsigned int \*flags)  
*Return flags used for creating default context.*
- hipError\_t [hipCtxEnablePeerAccess](#) (hipCtx\_t peerCtx, unsigned int flags)  
*Enables direct access to memory allocations in a peer context.*
- hipError\_t [hipCtxDisablePeerAccess](#) (hipCtx\_t peerCtx)  
*Disable direct access from current context's virtual address space to memory allocations physically located on a peer context. Disables direct access to memory allocations in a peer context and unregisters any registered allocations.*

### 4.21.1 Detailed Description

This section describes the deprecated context management functions of HIP runtime API.

### 4.21.2 Function Documentation

#### 4.21.2.1 hipCtxCreate()

```
hipError_t hipCtxCreate (
    hipCtx_t * ctx,
    unsigned int flags,
    hipDevice_t device )
```

Create a context and set it as current/ default context.

**Parameters**

out	<i>ctx</i>	
in	<i>flags</i>	
in	<i>associated</i>	device handle

**Returns**

#hipSuccess

**See also**

[hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.2 hipCtxDestroy()**

```
hipError_t hipCtxDestroy (
    hipCtx_t ctx )
```

Destroy a HIP context.

**Parameters**

in	<i>ctx</i>	Context to destroy
----	------------	--------------------

**Returns**

#hipSuccess, #hipErrorInvalidValue

**See also**

[hipCtxCreate](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.3 hipCtxDisablePeerAccess()**

```
hipError_t hipCtxDisablePeerAccess (
    hipCtx_t peerCtx )
```

Disable direct access from current context's virtual address space to memory allocations physically located on a peer context. Disables direct access to memory allocations in a peer context and unregisters any registered allocations.

Returns hipErrorPeerAccessNotEnabled if direct access to memory on peerDevice has not yet been enabled from the current device.

**Parameters**

in	<i>peerCtx</i>	
----	----------------	--

**Returns**

#hipSuccess, #hipErrorPeerAccessNotEnabled

## See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

## Warning

PeerToPeer support is experimental.

**4.21.2.4 hipCtxEnablePeerAccess()**

```
hipError_t hipCtxEnablePeerAccess (
    hipCtx_t peerCtx,
    unsigned int flags )
```

Enables direct access to memory allocations in a peer context.

Memory which already allocated on peer device will be mapped into the address space of the current device. In addition, all future memory allocations on peerDeviceId will be mapped into the address space of the current device when the memory is allocated. The peer memory remains accessible from the current device until a call to `hipDeviceDisablePeerAccess` or `hipDeviceReset`.

## Parameters

in	<i>peerCtx</i>	
in	<i>flags</i>	

## Returns

`#hipSuccess`, `#hipErrorInvalidDevice`, `#hipErrorInvalidValue`, `#hipErrorPeerAccessAlreadyEnabled`

## See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

## Warning

PeerToPeer support is experimental.

**4.21.2.5 hipCtxGetApiVersion()**

```
hipError_t hipCtxGetApiVersion (
    hipCtx_t ctx,
    int * apiVersion )
```

Returns the approximate HIP api version.

## Parameters

in	<i>ctx</i>	Context to check
out	<i>apiVersion</i>	

## Returns

`#hipSuccess`

**Warning**

The HIP feature set does not correspond to an exact CUDA SDK api revision. This function always set `*apiVersion` to 4 as an approximation though HIP supports some features which were introduced in later CUDA SDK revisions. HIP apps code should not rely on the api revision number here and should use arch feature flags to test device capabilities or conditional compilation.

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetDevice](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.6 hipCtxGetCacheConfig()**

```
hipError_t hipCtxGetCacheConfig (
    hipFuncCache_t * cacheConfig )
```

Set Cache configuration for a specific function.

**Parameters**

out	<i>cacheConfiguration</i>	
-----	---------------------------	--

**Returns**

`#hipSuccess`

**Warning**

AMD devices and some Nvidia GPUS do not support reconfigurable cache. This hint is ignored on those architectures.

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.7 hipCtxGetCurrent()**

```
hipError_t hipCtxGetCurrent (
    hipCtx_t * ctx )
```

Get the handle of the current/ default context.

**Parameters**

out	<i>ctx</i>	
-----	------------	--

**Returns**

`#hipSuccess`, `#hipErrorInvalidContext`

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetDevice](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

#### 4.21.2.8 hipCtxGetDevice()

```
hipError_t hipCtxGetDevice (
    hipDevice_t * device )
```

Get the handle of the device associated with current/default context.

##### Parameters

out	<i>device</i>	
-----	---------------	--

##### Returns

#hipSuccess, #hipErrorInvalidContext

##### See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#)

#### 4.21.2.9 hipCtxGetFlags()

```
hipError_t hipCtxGetFlags (
    unsigned int * flags )
```

Return flags used for creating default context.

##### Parameters

out	<i>flags</i>	
-----	--------------	--

##### Returns

#hipSuccess

##### See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

#### 4.21.2.10 hipCtxGetSharedMemConfig()

```
hipError_t hipCtxGetSharedMemConfig (
    hipSharedMemConfig * pConfig )
```

Get Shared memory bank configuration.

##### Parameters

out	<i>sharedMemoryConfiguration</i>	
-----	----------------------------------	--

##### Returns

#hipSuccess

**Warning**

AMD devices and some Nvidia GPUS do not support shared cache banking, and the hint is ignored on those architectures.

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.11 hipCtxPopCurrent()**

```
hipError_t hipCtxPopCurrent (
    hipCtx_t * ctx )
```

Pop the current/default context and return the popped context.

**Parameters**

out	<i>ctx</i>	
-----	------------	--

**Returns**

#hipSuccess, #hipErrorInvalidContext

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxSetCurrent](#), [hipCtxGetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.12 hipCtxPushCurrent()**

```
hipError_t hipCtxPushCurrent (
    hipCtx_t ctx )
```

Push the context to be set as current/ default context.

**Parameters**

in	<i>ctx</i>	
----	------------	--

**Returns**

#hipSuccess, #hipErrorInvalidContext

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.13 hipCtxSetCacheConfig()**

```
hipError_t hipCtxSetCacheConfig (
    hipFuncCache_t cacheConfig )
```

Set L1/Shared cache partition.

## Parameters

in	<i>cacheConfiguration</i>	
----	---------------------------	--

## Returns

#hipSuccess

## Warning

AMD devices and some Nvidia GPUS do not support reconfigurable cache. This hint is ignored on those architectures.

## See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.14 hipCtxSetCurrent()**

```
hipError_t hipCtxSetCurrent (
    hipCtx_t ctx )
```

Set the passed context as current/default.

## Parameters

in	<i>ctx</i>	
----	------------	--

## Returns

#hipSuccess, #hipErrorInvalidContext

## See also

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.15 hipCtxSetSharedMemConfig()**

```
hipError_t hipCtxSetSharedMemConfig (
    hipSharedMemConfig config )
```

Set Shared memory bank configuration.

## Parameters

in	<i>sharedMemoryConfiguration</i>	
----	----------------------------------	--

## Returns

#hipSuccess

## Warning

AMD devices and some Nvidia GPUS do not support shared cache banking, and the hint is ignored on those architectures.

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxSynchronize](#), [hipCtxGetDevice](#)

**4.21.2.16 hipCtxSynchronize()**

```
hipError_t hipCtxSynchronize (  
    void )
```

Blocks until the default context has completed all preceding requested tasks.

**Returns**

#hipSuccess

**Warning**

This function waits for all streams on the default context to complete execution, and then returns.

**See also**

[hipCtxCreate](#), [hipCtxDestroy](#), [hipCtxGetFlags](#), [hipCtxPopCurrent](#), [hipCtxGetCurrent](#), [hipCtxSetCurrent](#), [hipCtxPushCurrent](#), [hipCtxSetCacheConfig](#), [hipCtxGetDevice](#)



## 4.22 Texture Management [Deprecated]

### Functions

- `hipError_t hipBindTexture` (`size_t *offset`, `const textureReference *tex`, `const void *devPtr`, `const hipChannelFormatDesc *desc`, `size_t size` `__dparm(UINT_MAX)`)
- `hipError_t hipBindTexture2D` (`size_t *offset`, `const textureReference *tex`, `const void *devPtr`, `const hipChannelFormatDesc *desc`, `size_t width`, `size_t height`, `size_t pitch`)
- `hipError_t hipBindTextureToArray` (`const textureReference *tex`, `hipArray_const_t array`, `const hipChannelFormatDesc *desc`)
- `hipError_t hipGetTextureAlignmentOffset` (`size_t *offset`, `const textureReference *texref`)
- `hipError_t hipUnbindTexture` (`const textureReference *tex`)
- `hipError_t hipTexRefGetAddress` (`hipDeviceptr_t *dev_ptr`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetAddressMode` (`enum hipTextureAddressMode *pam`, `const textureReference *texRef`, `int dim`)
- `hipError_t hipTexRefGetFilterMode` (`enum hipTextureFilterMode *pfm`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetFlags` (`unsigned int *pFlags`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetFormat` (`hipArray_Format *pFormat`, `int *pNumChannels`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetMaxAnisotropy` (`int *pmaxAnsio`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetMipmapFilterMode` (`enum hipTextureFilterMode *pfm`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetMipmapLevelBias` (`float *pbias`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetMipmapLevelClamp` (`float *pminMipmapLevelClamp`, `float *pmaxMipmapLevelClamp`, `const textureReference *texRef`)
- `hipError_t hipTexRefGetMipMappedArray` (`hipMipmappedArray_t *pArray`, `const textureReference *texRef`)
- `hipError_t hipTexRefSetAddress` (`size_t *ByteOffset`, `textureReference *texRef`, `hipDeviceptr_t dptr`, `size_t bytes`)
- `hipError_t hipTexRefSetAddress2D` (`textureReference *texRef`, `const HIP_ARRAY_DESCRIPTOR *desc`, `hipDeviceptr_t dptr`, `size_t Pitch`)
- `hipError_t hipTexRefSetMaxAnisotropy` (`textureReference *texRef`, `unsigned int maxAniso`)

### 4.22.1 Detailed Description

This section describes the deprecated texture management functions of HIP runtime API.

## 4.23 Texture Management [Not supported]

### Functions

- hipError\_t **hipTexRefSetBorderColor** ([textureReference](#) \*texRef, float \*pBorderColor)
- hipError\_t **hipTexRefSetMipmapFilterMode** ([textureReference](#) \*texRef, enum hipTextureFilterMode fm)
- hipError\_t **hipTexRefSetMipmapLevelBias** ([textureReference](#) \*texRef, float bias)
- hipError\_t **hipTexRefSetMipmapLevelClamp** ([textureReference](#) \*texRef, float minMipMapLevelClamp, float maxMipMapLevelClamp)
- hipError\_t **hipTexRefSetMipmappedArray** ([textureReference](#) \*texRef, struct [hipMipmappedArray](#) \*mipmappedArray, unsigned int Flags)
- hipError\_t **hipMipmappedArrayCreate** ([hipMipmappedArray\\_t](#) \*pHandle, [HIP\\_ARRAY3D\\_DESCRIPTOR](#) \*pMipmappedArrayDesc, unsigned int numMipmapLevels)
- hipError\_t **hipMipmappedArrayDestroy** ([hipMipmappedArray\\_t](#) hMipmappedArray)
- hipError\_t **hipMipmappedArrayGetLevel** ([hipArray\\_t](#) \*pLevelArray, [hipMipmappedArray\\_t](#) hMipMapped↔Array, unsigned int level)

### 4.23.1 Detailed Description

This section describes the texture management functions currently unsupported in HIP runtime.

## Chapter 5

# Class Documentation

### 5.1 `__half2_raw` Struct Reference

#### Public Attributes

- unsigned short `x`
- unsigned short `y`

### 5.2 `__half_raw` Struct Reference

#### Public Attributes

- unsigned short `x`

### 5.3 `__hip_enable_if< __B, __T >` Struct Template Reference

### 5.4 `__hip_enable_if< true, __T >` Struct Template Reference

#### Public Types

- typedef `__T` `type`
- typedef `__T` `type`

### 5.5 `char1` Union Reference

#### Public Attributes

- char `data`

### 5.6 `char16` Union Reference

#### Public Attributes

- char `data` [16]

### 5.7 `char2` Union Reference

#### Public Attributes

- char `data` [2]

## 5.8 char3 Union Reference

### Public Attributes

- [char4](#) `data`

## 5.9 char4 Union Reference

### Public Attributes

- `char data` [4]

## 5.10 char8 Union Reference

### Public Attributes

- `char data` [8]

## 5.11 dim3 Struct Reference

### Public Attributes

- [uint32\\_t](#) `x`  
`x`
- [uint32\\_t](#) `y`  
`y`
- [uint32\\_t](#) `z`  
`z`

### 5.11.1 Detailed Description

Struct for data in 3D

## 5.12 double1 Union Reference

### Public Attributes

- `double data`

## 5.13 double16 Union Reference

### Public Attributes

- `double data` [16]

## 5.14 double2 Union Reference

### Public Attributes

- `double data` [2]

## 5.15 double3 Union Reference

### Public Attributes

- [double4](#) data

## 5.16 double4 Union Reference

### Public Attributes

- double data [4]

## 5.17 double8 Union Reference

### Public Attributes

- double data [8]

## 5.18 float1 Union Reference

### Public Attributes

- float data

## 5.19 float16 Union Reference

### Public Attributes

- float data [16]

## 5.20 float2 Union Reference

### Public Attributes

- float data [2]

## 5.21 float3 Union Reference

### Public Attributes

- [float4](#) data

## 5.22 float4 Union Reference

### Public Attributes

- float data [4]

## 5.23 float8 Union Reference

### Public Attributes

- float data [8]

## 5.24 gl\_dim3 Struct Reference

### Public Member Functions

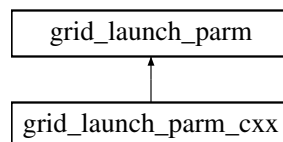
- **gl\_dim3** (uint32\_t \_x=1, uint32\_t \_y=1, uint32\_t \_z=1)
- **gl\_dim3** (uint32\_t \_x=1, uint32\_t \_y=1, uint32\_t \_z=1)

### Public Attributes

- int **x**
- int **y**
- int **z**

## 5.25 grid\_launch\_parm Struct Reference

Inheritance diagram for grid\_launch\_parm:



### Public Attributes

- [gl\\_dim3 grid\\_dim](#)  
*Grid dimensions.*
- [gl\\_dim3 group\\_dim](#)  
*Group dimensions.*
- unsigned int [dynamic\\_group\\_mem\\_bytes](#)
- enum gl\_barrier\_bit [barrier\\_bit](#)
- unsigned int [launch\\_fence](#)
- hc::accelerator\_view \* [av](#)
- hc::completion\_future \* [cf](#)

### 5.25.1 Member Data Documentation

#### 5.25.1.1 av

```
hc::accelerator_view * grid_launch_parm::av
```

Pointer to the accelerator\_view where the kernel should execute. If NULL, the default view on the default accelerator is used.

#### 5.25.1.2 barrier\_bit

```
enum gl_barrier_bit grid_launch_parm::barrier_bit
```

Control setting of barrier bit on per-packet basis: See gl\_barrier\_bit description. Placeholder, is not used to control packet dispatch yet

#### 5.25.1.3 cf

```
hc::completion_future * grid_launch_parm::cf
```

Pointer to the completion\_future used to track the status of the command. If NULL, the command does not write status. In this case, synchronization can be enforced with queue-level waits or waiting on younger commands.

#### 5.25.1.4 dynamic\_group\_mem\_bytes

unsigned int grid\_launch\_parm::dynamic\_group\_mem\_bytes

Amount of dynamic group memory to use with the kernel launch. This memory is in addition to the amount used statically in the kernel.

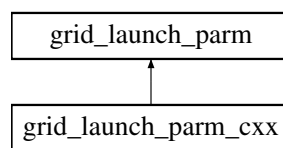
#### 5.25.1.5 launch\_fence

unsigned int grid\_launch\_parm::launch\_fence

Value of packet fences to apply to launch. The correspond to the value of bits 9:14 in the AQL packet, see HSA\_PACKET\_HEADER\_ACQUIRE\_FENCE\_SCOPE and hsa\_fence\_scope\_t.

## 5.26 grid\_launch\_parm\_cxx Class Reference

Inheritance diagram for grid\_launch\_parm\_cxx:



### Public Member Functions

- `__attribute__((annotate("serialize"))) void __cxxamp_serialize(Kalmar`
- `__attribute__((annotate("serialize"))) void __cxxamp_serialize(Kalmar`

### Additional Inherited Members

## 5.27 hip\_api\_data\_s Struct Reference

### Public Attributes

- uint64\_t **correlation\_id**
- uint32\_t **phase**
- 

```

union {
    struct {
        dim3 * gridDim
        dim3 gridDim__val
        dim3 * blockDim
        dim3 blockDim__val
        size_t * sharedMem
        size_t sharedMem__val
        hipStream_t * stream
        hipStream_t stream__val
    } __hipPopCallConfiguration
    struct {
        dim3 gridDim
        dim3 blockDim
        size_t sharedMem
        hipStream_t stream
    } __hipPushCallConfiguration
    struct {
        hipArray ** array
        hipArray * array__val
        const HIP_ARRAY3D_DESCRIPTOR * pAllocateArray
    }
  
```

```

    HIP_ARRAY3D_DESCRIPTOR pAllocateArray__val
} hipArray3DCreate
struct {
    hipArray ** pHandle
    hipArray * pHandle__val
    const HIP_ARRAY_DESCRIPTOR * pAllocateArray
    HIP_ARRAY_DESCRIPTOR pAllocateArray__val
} hipArrayCreate
struct {
    hipArray * array
    hipArray array__val
} hipArrayDestroy
struct {
    int * device
    int device__val
    const hipDeviceProp_t * prop
    hipDeviceProp_t prop__val
} hipChooseDevice
struct {
    dim3 gridDim
    dim3 blockDim
    size_t sharedMem
    hipStream_t stream
} hipConfigureCall
struct {
    hipSurfaceObject_t * pSurfObject
    hipSurfaceObject_t pSurfObject__val
    const hipResourceDesc * pResDesc
    hipResourceDesc pResDesc__val
} hipCreateSurfaceObject
struct {
    hipCtx_t * ctx
    hipCtx_t ctx__val
    unsigned int flags
    hipDevice_t device
} hipCtxCreate
struct {
    hipCtx_t ctx
} hipCtxDestroy
struct {
    hipCtx_t peerCtx
} hipCtxDisablePeerAccess
struct {
    hipCtx_t peerCtx
    unsigned int flags
} hipCtxEnablePeerAccess
struct {
    hipCtx_t ctx
    int * apiVersion
    int apiVersion__val
} hipCtxGetApiVersion
struct {
    hipFuncCache_t * cacheConfig
    hipFuncCache_t cacheConfig__val
} hipCtxGetCacheConfig
struct {
    hipCtx_t * ctx
    hipCtx_t ctx__val

```



```

} hipCtxGetCurrent
struct {
    hipDevice_t * device
    hipDevice_t device__val
} hipCtxGetDevice
struct {
    unsigned int * flags
    unsigned int flags__val
} hipCtxGetFlags
struct {
    hipSharedMemConfig * pConfig
    hipSharedMemConfig pConfig__val
} hipCtxGetSharedMemConfig
struct {
    hipCtx_t * ctx
    hipCtx_t ctx__val
} hipCtxPopCurrent
struct {
    hipCtx_t ctx
} hipCtxPushCurrent
struct {
    hipFuncCache\_t cacheConfig
} hipCtxSetCacheConfig
struct {
    hipCtx_t ctx
} hipCtxSetCurrent
struct {
    hipSharedMemConfig config
} hipCtxSetSharedMemConfig
struct {
    hipExternalMemory_t extMem
} hipDestroyExternalMemory
struct {
    hipExternalSemaphore_t extSem
} hipDestroyExternalSemaphore
struct {
    hipSurfaceObject_t surfaceObject
} hipDestroySurfaceObject
struct {
    int * canAccessPeer
    int canAccessPeer__val
    int deviceId
    int peerDeviceId
} hipDeviceCanAccessPeer
struct {
    int * major
    int major__val
    int * minor
    int minor__val
    hipDevice_t device
} hipDeviceComputeCapability
struct {
    int peerDeviceId
} hipDeviceDisablePeerAccess
struct {
    int peerDeviceId
    unsigned int flags
} hipDeviceEnablePeerAccess

```

```

struct {
    hipDevice_t * device
    hipDevice_t device__val
    int ordinal
} hipDeviceGet
struct {
    int * pi
    int pi__val
    hipDeviceAttribute_t attr
    int deviceld
} hipDeviceGetAttribute
struct {
    int * device
    int device__val
    const char * pciBusId
    char pciBusId__val
} hipDeviceGetByPCIBusId
struct {
    hipFuncCache_t * cacheConfig
    hipFuncCache_t cacheConfig__val
} hipDeviceGetCacheConfig
struct {
    size_t * pValue
    size_t pValue__val
    enum hipLimit_t limit
} hipDeviceGetLimit
struct {
    char * name
    char name__val
    int len
    hipDevice_t device
} hipDeviceGetName
struct {
    int * value
    int value__val
    hipDeviceP2PAttr attr
    int srcDevice
    int dstDevice
} hipDeviceGetP2PAttribute
struct {
    char * pciBusId
    char pciBusId__val
    int len
    int device
} hipDeviceGetPCIBusId
struct {
    hipSharedMemConfig * pConfig
    hipSharedMemConfig pConfig__val
} hipDeviceGetSharedMemConfig
struct {
    int * leastPriority
    int leastPriority__val
    int * greatestPriority
    int greatestPriority__val
} hipDeviceGetStreamPriorityRange
struct {
    hipDevice_t dev
    unsigned int * flags

```

```

    unsigned int flags__val
    int * active
    int active__val
} hipDevicePrimaryCtxGetState
struct {
    hipDevice_t dev
} hipDevicePrimaryCtxRelease
struct {
    hipDevice_t dev
} hipDevicePrimaryCtxReset
struct {
    hipCtx_t * pctx
    hipCtx_t pctx__val
    hipDevice_t dev
} hipDevicePrimaryCtxRetain
struct {
    hipDevice_t dev
    unsigned int flags
} hipDevicePrimaryCtxSetFlags
struct {
    hipFuncCache\_t cacheConfig
} hipDeviceSetCacheConfig
struct {
    hipSharedMemConfig config
} hipDeviceSetSharedMemConfig
struct {
    size_t * bytes
    size_t bytes__val
    hipDevice_t device
} hipDeviceTotalMem
struct {
    int * driverVersion
    int driverVersion__val
} hipDriverGetVersion
struct {
    const hip\_Memcpy2D * pCopy
    hip\_Memcpy2D pCopy__val
} hipDrvMemcpy2DUnaligned
struct {
    const HIP\_MEMCPY3D * pCopy
    HIP\_MEMCPY3D pCopy__val
} hipDrvMemcpy3D
struct {
    const HIP\_MEMCPY3D * pCopy
    HIP\_MEMCPY3D pCopy__val
    hipStream_t stream
} hipDrvMemcpy3DAsync
struct {
    hipEvent_t * event
    hipEvent_t event__val
} hipEventCreate
struct {
    hipEvent_t * event
    hipEvent_t event__val
    unsigned int flags
} hipEventCreateWithFlags
struct {
    hipEvent_t event

```

```

} hipEventDestroy
struct {
    float * ms
    float ms__val
    hipEvent_t start
    hipEvent_t stop
} hipEventElapsedTime
struct {
    hipEvent_t event
} hipEventQuery
struct {
    hipEvent_t event
    hipStream_t stream
} hipEventRecord
struct {
    hipEvent_t event
} hipEventSynchronize
struct {
    int device1
    int device2
    unsigned int * linktype
    unsigned int linktype__val
    unsigned int * hopcount
    unsigned int hopcount__val
} hipExtGetLinkTypeAndHopCount
struct {
    const void * function_address
    dim3 numBlocks
    dim3 dimBlocks
    void ** args
    void * args__val
    size_t sharedMemBytes
    hipStream_t stream
    hipEvent_t startEvent
    hipEvent_t stopEvent
    int flags
} hipExtLaunchKernel
struct {
    hipLaunchParams * launchParamsList
    hipLaunchParams launchParamsList__val
    int numDevices
    unsigned int flags
} hipExtLaunchMultiKernelMultiDevice
struct {
    void ** ptr
    void * ptr__val
    size_t sizeBytes
    unsigned int flags
} hipExtMallocWithFlags
struct {
    hipFunction_t f
    unsigned int globalWorkSizeX
    unsigned int globalWorkSizeY
    unsigned int globalWorkSizeZ
    unsigned int localWorkSizeX
    unsigned int localWorkSizeY
    unsigned int localWorkSizeZ
    size_t sharedMemBytes

```

```

    hipStream_t hStream
    void ** kernelParams
    void * kernelParams__val
    void ** extra
    void * extra__val
    hipEvent_t startEvent
    hipEvent_t stopEvent
    unsigned int flags
} hipExtModuleLaunchKernel
struct {
    hipStream_t * stream
    hipStream_t stream__val
    unsigned int cuMaskSize
    const unsigned int * cuMask
    unsigned int cuMask__val
} hipExtStreamCreateWithCUMask
struct {
    hipStream_t stream
    unsigned int cuMaskSize
    unsigned int * cuMask
    unsigned int cuMask__val
} hipExtStreamGetCUMask
struct {
    void ** devPtr
    void * devPtr__val
    hipExternalMemory_t extMem
    const hipExternalMemoryBufferDesc * bufferDesc
    hipExternalMemoryBufferDesc bufferDesc__val
} hipExternalMemoryGetMappedBuffer
struct {
    void * ptr
} hipFree
struct {
    hipArray * array
    hipArray array__val
} hipFreeArray
struct {
    void * ptr
} hipFreeHost
struct {
    hipMipmappedArray\_t mipmappedArray
} hipFreeMipmappedArray
struct {
    int * value
    int value__val
    hipFunction_attribute attrib
    hipFunction_t hfunc
} hipFuncGetAttribute
struct {
    hipFuncAttributes * attr
    hipFuncAttributes attr__val
    const void * func
} hipFuncGetAttributes
struct {
    const void * func
    hipFuncAttribute attr
    int value
} hipFuncSetAttribute

```

```

struct {
    const void * func
    hipFuncCache\_t config
} hipFuncSetCacheConfig
struct {
    const void * func
    hipSharedMemConfig config
} hipFuncSetSharedMemConfig
struct {
    unsigned int * pHipDeviceCount
    unsigned int pHipDeviceCount__val
    int * pHipDevices
    int pHipDevices__val
    unsigned int hipDeviceCount
    hipGLDeviceList deviceList
} hipGLGetDevices
struct {
    int * deviceld
    int deviceld__val
} hipGetDevice
struct {
    int * count
    int count__val
} hipGetDeviceCount
struct {
    unsigned int * flags
    unsigned int flags__val
} hipGetDeviceFlags
struct {
    hipDeviceProp\_t * props
    hipDeviceProp\_t props__val
    hipDevice\_t device
} hipGetDeviceProperties
struct {
    hipArray\_t * levelArray
    hipArray\_t levelArray__val
    hipMipmappedArray\_const\_t mipmappedArray
    unsigned int level
} hipGetMipmappedArrayLevel
struct {
    void ** devPtr
    void * devPtr__val
    const void * symbol
} hipGetSymbolAddress
struct {
    size_t * size
    size_t size__val
    const void * symbol
} hipGetSymbolSize
struct {
    hipGraph\_t graph
    const hipGraphNode\_t * from
    hipGraphNode\_t from__val
    const hipGraphNode\_t * to
    hipGraphNode\_t to__val
    size_t numDependencies
} hipGraphAddDependencies
struct {

```

```

    hipGraphNode_t * pGraphNode
    hipGraphNode_t pGraphNode__val
    hipGraph_t graph
    const hipGraphNode_t * pDependencies
    hipGraphNode_t pDependencies__val
    size_t numDependencies
} hipGraphAddEmptyNode
struct {
    hipGraphNode_t * pGraphNode
    hipGraphNode_t pGraphNode__val
    hipGraph_t graph
    const hipGraphNode_t * pDependencies
    hipGraphNode_t pDependencies__val
    size_t numDependencies
    const hipKernelNodeParams * pNodeParams
    hipKernelNodeParams pNodeParams__val
} hipGraphAddKernelNode
struct {
    hipGraphNode_t * pGraphNode
    hipGraphNode_t pGraphNode__val
    hipGraph_t graph
    const hipGraphNode_t * pDependencies
    hipGraphNode_t pDependencies__val
    size_t numDependencies
    const hipMemcpy3DParms * pCopyParams
    hipMemcpy3DParms pCopyParams__val
} hipGraphAddMemcpyNode
struct {
    hipGraphNode_t * pGraphNode
    hipGraphNode_t pGraphNode__val
    hipGraph_t graph
    const hipGraphNode_t * pDependencies
    hipGraphNode_t pDependencies__val
    size_t numDependencies
    const hipMemsetParams * pMemsetParams
    hipMemsetParams pMemsetParams__val
} hipGraphAddMemsetNode
struct {
    hipGraph_t * pGraph
    hipGraph_t pGraph__val
    unsigned int flags
} hipGraphCreate
struct {
    hipGraph_t graph
} hipGraphDestroy
struct {
    hipGraphExec_t pGraphExec
} hipGraphExecDestroy
struct {
    hipGraphExec_t hGraphExec
    hipGraphNode_t node
    const hipKernelNodeParams * pNodeParams
    hipKernelNodeParams pNodeParams__val
} hipGraphExecKernelNodeSetParams
struct {
    hipGraph_t graph
    hipGraphNode_t * nodes
    hipGraphNode_t nodes__val

```

```

    size_t * numNodes
    size_t numNodes__val
} hipGraphGetNodes
struct {
    hipGraph_t graph
    hipGraphNode_t * pRootNodes
    hipGraphNode_t pRootNodes__val
    size_t * pNumRootNodes
    size_t pNumRootNodes__val
} hipGraphGetRootNodes
struct {
    hipGraphExec_t * pGraphExec
    hipGraphExec_t pGraphExec__val
    hipGraph_t graph
    hipGraphNode_t * pErrorNode
    hipGraphNode_t pErrorNode__val
    char * pLogBuffer
    char pLogBuffer__val
    size_t bufferSize
} hipGraphInstantiate
struct {
    hipGraphNode_t node
    hipKernelNodeParams * pNodeParams
    hipKernelNodeParams pNodeParams__val
} hipGraphKernelNodeGetParams
struct {
    hipGraphNode_t node
    const hipKernelNodeParams * pNodeParams
    hipKernelNodeParams pNodeParams__val
} hipGraphKernelNodeSetParams
struct {
    hipGraphExec_t graphExec
    hipStream_t stream
} hipGraphLaunch
struct {
    hipGraphNode_t node
    hipMemcpy3DParms * pNodeParams
    hipMemcpy3DParms pNodeParams__val
} hipGraphMemcpyNodeGetParams
struct {
    hipGraphNode_t node
    const hipMemcpy3DParms * pNodeParams
    hipMemcpy3DParms pNodeParams__val
} hipGraphMemcpyNodeSetParams
struct {
    hipGraphNode_t node
    hipMemsetParams * pNodeParams
    hipMemsetParams pNodeParams__val
} hipGraphMemsetNodeGetParams
struct {
    hipGraphNode_t node
    const hipMemsetParams * pNodeParams
    hipMemsetParams pNodeParams__val
} hipGraphMemsetNodeSetParams
struct {
    hipGraphicsResource ** resource
    hipGraphicsResource * resource__val
    GLuint buffer

```



```

    unsigned int flags
} hipGraphicsGLRegisterBuffer
struct {
    int count
    hipGraphicsResource_t * resources
    hipGraphicsResource_t resources__val
    hipStream_t stream
} hipGraphicsMapResources
struct {
    void ** devPtr
    void * devPtr__val
    size_t * size
    size_t size__val
    hipGraphicsResource_t resource
} hipGraphicsResourceGetMappedPointer
struct {
    int count
    hipGraphicsResource_t * resources
    hipGraphicsResource_t resources__val
    hipStream_t stream
} hipGraphicsUnmapResources
struct {
    hipGraphicsResource_t resource
} hipGraphicsUnregisterResource
struct {
    hipFunction_t f
    unsigned int globalWorkSizeX
    unsigned int globalWorkSizeY
    unsigned int globalWorkSizeZ
    unsigned int blockDimX
    unsigned int blockDimY
    unsigned int blockDimZ
    size_t sharedMemBytes
    hipStream_t hStream
    void ** kernelParams
    void * kernelParams__val
    void ** extra
    void * extra__val
    hipEvent_t startEvent
    hipEvent_t stopEvent
} hipHccModuleLaunchKernel
struct {
    void ** ptr
    void * ptr__val
    size_t size
    unsigned int flags
} hipHostAlloc
struct {
    void * ptr
} hipHostFree
struct {
    void ** devPtr
    void * devPtr__val
    void * hstPtr
    unsigned int flags
} hipHostGetDevicePointer
struct {
    unsigned int * flagsPtr

```

```

    unsigned int flagsPtr__val
    void * hostPtr
} hipHostGetFlags
struct {
    void ** ptr
    void * ptr__val
    size_t size
    unsigned int flags
} hipHostMalloc
struct {
    void * hostPtr
    size_t sizeBytes
    unsigned int flags
} hipHostRegister
struct {
    void * hostPtr
} hipHostUnregister
struct {
    hipExternalMemory_t * extMem_out
    hipExternalMemory_t extMem_out__val
    const hipExternalMemoryHandleDesc * memHandleDesc
    hipExternalMemoryHandleDesc memHandleDesc__val
} hipImportExternalMemory
struct {
    hipExternalSemaphore_t * extSem_out
    hipExternalSemaphore_t extSem_out__val
    const hipExternalSemaphoreHandleDesc * semHandleDesc
    hipExternalSemaphoreHandleDesc semHandleDesc__val
} hipImportExternalSemaphore
struct {
    unsigned int flags
} hipInit
struct {
    void * devPtr
} hipIpcCloseMemHandle
struct {
    hipIpcEventHandle\_t * handle
    hipIpcEventHandle\_t handle__val
    hipEvent_t event
} hipIpcGetEventHandle
struct {
    hipIpcMemHandle\_t * handle
    hipIpcMemHandle\_t handle__val
    void * devPtr
} hipIpcGetMemHandle
struct {
    hipEvent_t * event
    hipEvent_t event__val
    hipIpcEventHandle\_t handle
} hipIpcOpenEventHandle
struct {
    void ** devPtr
    void * devPtr__val
    hipIpcMemHandle\_t handle
    unsigned int flags
} hipIpcOpenMemHandle
struct {
    const void * hostFunction

```

```

} hipLaunchByPtr
struct {
    const void * f
    dim3 gridDim
    dim3 blockDimX
    void ** kernelParams
    void * kernelParams__val
    unsigned int sharedMemBytes
    hipStream_t stream
} hipLaunchCooperativeKernel
struct {
    hipLaunchParams * launchParamsList
    hipLaunchParams launchParamsList__val
    int numDevices
    unsigned int flags
} hipLaunchCooperativeKernelMultiDevice
struct {
    const void * function_address
    dim3 numBlocks
    dim3 dimBlocks
    void ** args
    void * args__val
    size_t sharedMemBytes
    hipStream_t stream
} hipLaunchKernel
struct {
    void ** ptr
    void * ptr__val
    size_t size
} hipMalloc
struct {
    hipPitchedPtr * pitchedDevPtr
    hipPitchedPtr pitchedDevPtr__val
    hipExtent extent
} hipMalloc3D
struct {
    hipArray_t * array
    hipArray_t array__val
    const hipChannelFormatDesc * desc
    hipChannelFormatDesc desc__val
    hipExtent extent
    unsigned int flags
} hipMalloc3DArray
struct {
    hipArray ** array
    hipArray * array__val
    const hipChannelFormatDesc * desc
    hipChannelFormatDesc desc__val
    size_t width
    size_t height
    unsigned int flags
} hipMallocArray
struct {
    void ** ptr
    void * ptr__val
    size_t size
} hipMallocHost
struct {

```

```

    void ** dev_ptr
    void * dev_ptr__val
    size_t size
    unsigned int flags
} hipMallocManaged
struct {
    hipMipmappedArray_t * mipmappedArray
    hipMipmappedArray_t mipmappedArray__val
    const hipChannelFormatDesc * desc
    hipChannelFormatDesc desc__val
    hipExtent extent
    unsigned int numLevels
    unsigned int flags
} hipMallocMipmappedArray
struct {
    void ** ptr
    void * ptr__val
    size_t * pitch
    size_t pitch__val
    size_t width
    size_t height
} hipMallocPitch
struct {
    const void * dev_ptr
    size_t count
    hipMemoryAdvise advice
    int device
} hipMemAdvise
struct {
    void ** ptr
    void * ptr__val
    size_t size
} hipMemAllocHost
struct {
    hipDeviceptr_t * dptr
    hipDeviceptr_t dptr__val
    size_t * pitch
    size_t pitch__val
    size_t widthInBytes
    size_t height
    unsigned int elementSizeBytes
} hipMemAllocPitch
struct {
    hipDeviceptr_t * pbase
    hipDeviceptr_t pbase__val
    size_t * psize
    size_t psize__val
    hipDeviceptr_t dptr
} hipMemGetAddressRange
struct {
    size_t * free
    size_t free__val
    size_t * total
    size_t total__val
} hipMemGetInfo
struct {
    const void * dev_ptr
    size_t count

```

```

    int device
    hipStream_t stream
} hipMemPrefetchAsync
struct {
    void * ptr
    size_t * size
    size_t size__val
} hipMemPtrGetInfo
struct {
    void * data
    size_t data_size
hipMemRangeAttribute attribute
    const void * dev_ptr
    size_t count
} hipMemRangeGetAttribute
struct {
    void ** data
    void * data__val
    size_t * data_sizes
    size_t data_sizes__val
hipMemRangeAttribute * attributes
hipMemRangeAttribute attributes__val
    size_t num_attributes
    const void * dev_ptr
    size_t count
} hipMemRangeGetAttributes
struct {
    void * dst
    const void * src
    size_t sizeBytes
    hipMemcpyKind kind
} hipMemcpy
struct {
    void * dst
    size_t dpitch
    const void * src
    size_t spitch
    size_t width
    size_t height
    hipMemcpyKind kind
} hipMemcpy2D
struct {
    void * dst
    size_t dpitch
    const void * src
    size_t spitch
    size_t width
    size_t height
    hipMemcpyKind kind
    hipStream_t stream
} hipMemcpy2DAsync
struct {
    void * dst
    size_t dpitch
hipArray\_const\_t src
    size_t wOffset
    size_t hOffset
    size_t width

```

```

    size_t height
    hipMemcpyKind kind
} hipMemcpy2DFromArray
struct {
    void * dst
    size_t dpitch
    hipArray_const_t src
    size_t wOffset
    size_t hOffset
    size_t width
    size_t height
    hipMemcpyKind kind
    hipStream_t stream
} hipMemcpy2DFromArrayAsync
struct {
    hipArray * dst
    hipArray dst__val
    size_t wOffset
    size_t hOffset
    const void * src
    size_t spitch
    size_t width
    size_t height
    hipMemcpyKind kind
} hipMemcpy2DToArray
struct {
    hipArray * dst
    hipArray dst__val
    size_t wOffset
    size_t hOffset
    const void * src
    size_t spitch
    size_t width
    size_t height
    hipMemcpyKind kind
    hipStream_t stream
} hipMemcpy2DToArrayAsync
struct {
    const hipMemcpy3DParms * p
    hipMemcpy3DParms p__val
} hipMemcpy3D
struct {
    const hipMemcpy3DParms * p
    hipMemcpy3DParms p__val
    hipStream_t stream
} hipMemcpy3DAsync
struct {
    void * dst
    const void * src
    size_t sizeBytes
    hipMemcpyKind kind
    hipStream_t stream
} hipMemcpyAsync
struct {
    void * dst
    hipArray * srcArray
    hipArray srcArray__val
    size_t srcOffset

```

```

    size_t count
} hipMemcpyAtoH
struct {
    hipDeviceptr_t dst
    hipDeviceptr_t src
    size_t sizeBytes
} hipMemcpyDtoD
struct {
    hipDeviceptr_t dst
    hipDeviceptr_t src
    size_t sizeBytes
    hipStream_t stream
} hipMemcpyDtoDAsync
struct {
    void * dst
    hipDeviceptr_t src
    size_t sizeBytes
} hipMemcpyDtoH
struct {
    void * dst
    hipDeviceptr_t src
    size_t sizeBytes
    hipStream_t stream
} hipMemcpyDtoHAsync
struct {
    void * dst
    hipArray_const_t srcArray
    size_t wOffset
    size_t hOffset
    size_t count
    hipMemcpyKind kind
} hipMemcpyFromArray
struct {
    void * dst
    const void * symbol
    size_t sizeBytes
    size_t offset
    hipMemcpyKind kind
} hipMemcpyFromSymbol
struct {
    void * dst
    const void * symbol
    size_t sizeBytes
    size_t offset
    hipMemcpyKind kind
    hipStream_t stream
} hipMemcpyFromSymbolAsync
struct {
    hipArray * dstArray
    hipArray dstArray__val
    size_t dstOffset
    const void * srcHost
    size_t count
} hipMemcpyHtoA
struct {
    hipDeviceptr_t dst
    void * src
    size_t sizeBytes

```

```

} hipMemcpyHtoD
struct {
    hipDeviceptr_t dst
    void * src
    size_t sizeBytes
    hipStream_t stream
} hipMemcpyHtoDAsync
struct {
    const hip_Memcpy2D * pCopy
    hip_Memcpy2D pCopy__val
} hipMemcpyParam2D
struct {
    const hip_Memcpy2D * pCopy
    hip_Memcpy2D pCopy__val
    hipStream_t stream
} hipMemcpyParam2DAsync
struct {
    void * dst
    int dstDeviceId
    const void * src
    int srcDeviceId
    size_t sizeBytes
} hipMemcpyPeer
struct {
    void * dst
    int dstDeviceId
    const void * src
    int srcDevice
    size_t sizeBytes
    hipStream_t stream
} hipMemcpyPeerAsync
struct {
    hipArray * dst
    hipArray dst__val
    size_t wOffset
    size_t hOffset
    const void * src
    size_t count
    hipMemcpyKind kind
} hipMemcpyToArray
struct {
    const void * symbol
    const void * src
    size_t sizeBytes
    size_t offset
    hipMemcpyKind kind
} hipMemcpyToSymbol
struct {
    const void * symbol
    const void * src
    size_t sizeBytes
    size_t offset
    hipMemcpyKind kind
    hipStream_t stream
} hipMemcpyToSymbolAsync
struct {
    void * dst
    const void * src

```



```

    size_t sizeBytes
    hipMemcpyKind kind
    hipStream_t stream
} hipMemcpyWithStream
struct {
    void * dst
    int value
    size_t sizeBytes
} hipMemset
struct {
    void * dst
    size_t pitch
    int value
    size_t width
    size_t height
} hipMemset2D
struct {
    void * dst
    size_t pitch
    int value
    size_t width
    size_t height
    hipStream_t stream
} hipMemset2DAsync
struct {
    hipPitchedPtr pitchedDevPtr
    int value
    hipExtent extent
} hipMemset3D
struct {
    hipPitchedPtr pitchedDevPtr
    int value
    hipExtent extent
    hipStream_t stream
} hipMemset3DAsync
struct {
    void * dst
    int value
    size_t sizeBytes
    hipStream_t stream
} hipMemsetAsync
struct {
    hipDeviceptr_t dest
    unsigned short value
    size_t count
} hipMemsetD16
struct {
    hipDeviceptr_t dest
    unsigned short value
    size_t count
    hipStream_t stream
} hipMemsetD16Async
struct {
    hipDeviceptr_t dest
    int value
    size_t count
} hipMemsetD32
struct {

```

```

    hipDeviceptr_t dst
    int value
    size_t count
    hipStream_t stream
} hipMemsetD32Async
struct {
    hipDeviceptr_t dest
    unsigned char value
    size_t count
} hipMemsetD8
struct {
    hipDeviceptr_t dest
    unsigned char value
    size_t count
    hipStream_t stream
} hipMemsetD8Async
struct {
    hipMipmappedArray_t * pHandle
    hipMipmappedArray_t pHandle__val
    HIP_ARRAY3D_DESCRIPTOR * pMipmappedArrayDesc
    HIP_ARRAY3D_DESCRIPTOR pMipmappedArrayDesc__val
    unsigned int numMipmapLevels
} hipMipmappedArrayCreate
struct {
    hipMipmappedArray_t hMipmappedArray
} hipMipmappedArrayDestroy
struct {
    hipArray_t * pLevelArray
    hipArray_t pLevelArray__val
    hipMipmappedArray_t hMipMappedArray
    unsigned int level
} hipMipmappedArrayGetLevel
struct {
    hipFunction_t * function
    hipFunction_t function__val
    hipModule_t module
    const char * kname
    char kname__val
} hipModuleGetFunction
struct {
    hipDeviceptr_t * dptr
    hipDeviceptr_t dptr__val
    size_t * bytes
    size_t bytes__val
    hipModule_t hmod
    const char * name
    char name__val
} hipModuleGetGlobal
struct {
    textureReference ** texRef
    textureReference * texRef__val
    hipModule_t hmod
    const char * name
    char name__val
} hipModuleGetTexRef
struct {
    hipFunction_t f
    unsigned int gridDimX

```

```

    unsigned int gridDimY
    unsigned int gridDimZ
    unsigned int blockDimX
    unsigned int blockDimY
    unsigned int blockDimZ
    unsigned int sharedMemBytes
    hipStream_t stream
    void ** kernelParams
    void * kernelParams__val
    void ** extra
    void * extra__val
} hipModuleLaunchKernel
struct {
    hipModule_t * module
    hipModule_t module__val
    const char * fname
    char fname__val
} hipModuleLoad
struct {
    hipModule_t * module
    hipModule_t module__val
    const void * image
} hipModuleLoadData
struct {
    hipModule_t * module
    hipModule_t module__val
    const void * image
    unsigned int numOptions
    hipJitOption * options
    hipJitOption options__val
    void ** optionsValues
    void * optionsValues__val
} hipModuleLoadDataEx
struct {
    int * numBlocks
    int numBlocks__val
    hipFunction_t f
    int blockSize
    size_t dynSharedMemPerBlk
} hipModuleOccupancyMaxActiveBlocksPerMultiprocessor
struct {
    int * numBlocks
    int numBlocks__val
    hipFunction_t f
    int blockSize
    size_t dynSharedMemPerBlk
    unsigned int flags
} hipModuleOccupancyMaxActiveBlocksPerMultiprocessorWithFlags
struct {
    int * gridSize
    int gridSize__val
    int * blockSize
    int blockSize__val
    hipFunction_t f
    size_t dynSharedMemPerBlk
    int blockSizeLimit
} hipModuleOccupancyMaxPotentialBlockSize
struct {

```

```

    int * gridSize
    int gridSize__val
    int * blockSize
    int blockSize__val
    hipFunction_t f
    size_t dynSharedMemPerBlk
    int blockSizeLimit
    unsigned int flags
} hipModuleOccupancyMaxPotentialBlockSizeWithFlags
struct {
    hipModule_t module
} hipModuleUnload
struct {
    int * numBlocks
    int numBlocks__val
    const void * f
    int blockSize
    size_t dynamicSMemSize
} hipOccupancyMaxActiveBlocksPerMultiprocessor
struct {
    int * numBlocks
    int numBlocks__val
    const void * f
    int blockSize
    size_t dynamicSMemSize
    unsigned int flags
} hipOccupancyMaxActiveBlocksPerMultiprocessorWithFlags
struct {
    int * gridSize
    int gridSize__val
    int * blockSize
    int blockSize__val
    const void * f
    size_t dynSharedMemPerBlk
    int blockSizeLimit
} hipOccupancyMaxPotentialBlockSize
struct {
    hipPointerAttribute\_t * attributes
    hipPointerAttribute\_t attributes__val
    const void * ptr
} hipPointerGetAttributes
struct {
    int * runtimeVersion
    int runtimeVersion__val
} hipRuntimeGetVersion
struct {
    int deviceId
} hipSetDevice
struct {
    unsigned int flags
} hipSetDeviceFlags
struct {
    const void * arg
    size_t size
    size_t offset
} hipSetupArgument
struct {
    const hipExternalSemaphore\_t * extSemArray

```

```

    hipExternalSemaphore_t extSemArray__val
    const hipExternalSemaphoreSignalParams * paramsArray
    hipExternalSemaphoreSignalParams paramsArray__val
    unsigned int numExtSems
    hipStream_t stream
} hipSignalExternalSemaphoresAsync
struct {
    hipStream_t stream
    hipStreamCallback\_t callback
    void * userData
    unsigned int flags
} hipStreamAddCallback
struct {
    hipStream_t stream
    void * dev_ptr
    size_t length
    unsigned int flags
} hipStreamAttachMemAsync
struct {
    hipStream_t stream
    hipStreamCaptureMode mode
} hipStreamBeginCapture
struct {
    hipStream_t * stream
    hipStream_t stream__val
} hipStreamCreate
struct {
    hipStream_t * stream
    hipStream_t stream__val
    unsigned int flags
} hipStreamCreateWithFlags
struct {
    hipStream_t * stream
    hipStream_t stream__val
    unsigned int flags
    int priority
} hipStreamCreateWithPriority
struct {
    hipStream_t stream
} hipStreamDestroy
struct {
    hipStream_t stream
    hipGraph\_t * pGraph
    hipGraph\_t pGraph__val
} hipStreamEndCapture
struct {
    hipStream_t stream
    unsigned int * flags
    unsigned int flags__val
} hipStreamGetFlags
struct {
    hipStream_t stream
    int * priority
    int priority__val
} hipStreamGetPriority
struct {
    hipStream_t stream
} hipStreamQuery

```

```

struct {
    hipStream_t stream
} hipStreamSynchronize
struct {
    hipStream_t stream
    hipEvent_t event
    unsigned int flags
} hipStreamWaitEvent
struct {
    hipStream_t stream
    void * ptr
    uint32_t value
    unsigned int flags
    unsigned int mask
} hipStreamWaitValue32
struct {
    hipStream_t stream
    void * ptr
    uint64_t value
    unsigned int flags
    uint64_t mask
} hipStreamWaitValue64
struct {
    hipStream_t stream
    void * ptr
    uint32_t value
    unsigned int flags
} hipStreamWriteValue32
struct {
    hipStream_t stream
    void * ptr
    uint64_t value
    unsigned int flags
} hipStreamWriteValue64
struct {
    hipDeviceptr_t * dev_ptr
    hipDeviceptr_t dev_ptr__val
    const textureReference * texRef
    textureReference texRef__val
} hipTexRefGetAddress
struct {
    unsigned int * pFlags
    unsigned int pFlags__val
    const textureReference * texRef
    textureReference texRef__val
} hipTexRefGetFlags
struct {
    hipArray_Format * pFormat
    hipArray_Format pFormat__val
    int * pNumChannels
    int pNumChannels__val
    const textureReference * texRef
    textureReference texRef__val
} hipTexRefGetFormat
struct {
    int * pmaxAnsio
    int pmaxAnsio__val
    const textureReference * texRef

```

```

    textureReference texRef__val
} hipTexRefGetMaxAnisotropy
struct {
    hipMipmappedArray_t * pArray
    hipMipmappedArray_t pArray__val
    const textureReference * texRef
    textureReference texRef__val
} hipTexRefGetMipMappedArray
struct {
    float * pbias
    float pbias__val
    const textureReference * texRef
    textureReference texRef__val
} hipTexRefGetMipmapLevelBias
struct {
    float * pminMipmapLevelClamp
    float pminMipmapLevelClamp__val
    float * pmaxMipmapLevelClamp
    float pmaxMipmapLevelClamp__val
    const textureReference * texRef
    textureReference texRef__val
} hipTexRefGetMipmapLevelClamp
struct {
    size_t * ByteOffset
    size_t ByteOffset__val
    textureReference * texRef
    textureReference texRef__val
    hipDeviceptr_t dptr
    size_t bytes
} hipTexRefSetAddress
struct {
    textureReference * texRef
    textureReference texRef__val
    const HIP_ARRAY_DESCRIPTOR * desc
    HIP_ARRAY_DESCRIPTOR desc__val
    hipDeviceptr_t dptr
    size_t Pitch
} hipTexRefSetAddress2D
struct {
    textureReference * texRef
    textureReference texRef__val
    float * pBorderColor
    float pBorderColor__val
} hipTexRefSetBorderColor
struct {
    textureReference * texRef
    textureReference texRef__val
    hipArray_Format fmt
    int NumPackedComponents
} hipTexRefSetFormat
struct {
    textureReference * texRef
    textureReference texRef__val
    unsigned int maxAniso
} hipTexRefSetMaxAnisotropy
struct {
    textureReference * texRef
    textureReference texRef__val

```

```

    float minMipMapLevelClamp
    float maxMipMapLevelClamp
} hipTexRefSetMipmapLevelClamp
struct {
    textureReference * texRef
    textureReference texRef__val
    hipMipmappedArray * mipmappedArray
    hipMipmappedArray mipmappedArray__val
    unsigned int Flags
} hipTexRefSetMipmappedArray
struct {
    const hipExternalSemaphore_t * extSemArray
    hipExternalSemaphore_t extSemArray__val
    const hipExternalSemaphoreWaitParams * paramsArray
    hipExternalSemaphoreWaitParams paramsArray__val
    unsigned int numExtSems
    hipStream_t stream
} hipWaitExternalSemaphoresAsync
} args

```

## 5.28 HIP\_ARRAY3D\_DESCRIPTOR Struct Reference

### Public Attributes

- size\_t **Width**
- size\_t **Height**
- size\_t **Depth**
- enum hipArray\_Format **Format**
- unsigned int **NumChannels**
- unsigned int **Flags**

## 5.29 HIP\_ARRAY\_DESCRIPTOR Struct Reference

### Public Attributes

- size\_t **Width**
- size\_t **Height**
- enum hipArray\_Format **Format**
- unsigned int **NumChannels**

## 5.30 hip\_bfloat16 Struct Reference

Struct to represent a 16 bit brain floating point number.

### Public Attributes

- uint16\_t **data**

### 5.30.1 Detailed Description

Struct to represent a 16 bit brain floating point number.



## 5.31 hip\_Memcpy2D Struct Reference

### Public Attributes

- `size_t srcXInBytes`
- `size_t srcY`
- `hipMemoryType srcMemoryType`
- `const void * srcHost`
- `hipDeviceptr_t srcDevice`
- `hipArray * srcArray`
- `size_t srcPitch`
- `size_t dstXInBytes`
- `size_t dstY`
- `hipMemoryType dstMemoryType`
- `void * dstHost`
- `hipDeviceptr_t dstDevice`
- `hipArray * dstArray`
- `size_t dstPitch`
- `size_t WidthInBytes`
- `size_t Height`

## 5.32 HIP\_MEMCPY3D Struct Reference

### Public Attributes

- `unsigned int srcXInBytes`
- `unsigned int srcY`
- `unsigned int srcZ`
- `unsigned int srcLOD`
- `hipMemoryType srcMemoryType`
- `const void * srcHost`
- `hipDeviceptr_t srcDevice`
- `hipArray_t srcArray`
- `unsigned int srcPitch`
- `unsigned int srcHeight`
- `unsigned int dstXInBytes`
- `unsigned int dstY`
- `unsigned int dstZ`
- `unsigned int dstLOD`
- `hipMemoryType dstMemoryType`
- `void * dstHost`
- `hipDeviceptr_t dstDevice`
- `hipArray_t dstArray`
- `unsigned int dstPitch`
- `unsigned int dstHeight`
- `unsigned int WidthInBytes`
- `unsigned int Height`
- `unsigned int Depth`

## 5.33 HIP\_RESOURCE\_DESC\_st Struct Reference

### Public Attributes

- HIPResourcetype [resType](#)
- - union {
    - struct {
 [hipArray\\_t](#) [hArray](#)
    - } **array**
    - struct {
 [hipMipmappedArray\\_t](#) [hMipmappedArray](#)
    - } **mipmap**
    - struct {
 [hipDeviceptr\\_t](#) [devPtr](#)
    - [hipArray\\_Format](#) [format](#)
    - unsigned int [numChannels](#)
    - size\_t [sizeInBytes](#)
    - } **linear**
    - struct {
 [hipDeviceptr\\_t](#) [devPtr](#)
    - [hipArray\\_Format](#) [format](#)
    - unsigned int [numChannels](#)
    - size\_t [width](#)
    - size\_t [height](#)
    - size\_t [pitchInBytes](#)
    - } **pitch2D**
    - struct {
 int **reserved** [32]
    - } **reserved**
  - } **res**
- unsigned int [flags](#)

### 5.33.1 Member Data Documentation

#### 5.33.1.1 devPtr

[hipDeviceptr\\_t](#) HIP\_RESOURCE\_DESC\_st::devPtr  
Device pointer

#### 5.33.1.2 flags

unsigned int HIP\_RESOURCE\_DESC\_st::flags  
Flags (must be zero)

#### 5.33.1.3 format

[hipArray\\_Format](#) HIP\_RESOURCE\_DESC\_st::format  
Array format

#### 5.33.1.4 hArray

[hipArray\\_t](#) HIP\_RESOURCE\_DESC\_st::hArray  
HIP array

#### 5.33.1.5 height

size\_t HIP\_RESOURCE\_DESC\_st::height  
Height of the array in elements

#### 5.33.1.6 hMipmappedArray

hipMipmappedArray\_t HIP\_RESOURCE\_DESC\_st::hMipmappedArray  
HIP mipmapped array

#### 5.33.1.7 numChannels

unsigned int HIP\_RESOURCE\_DESC\_st::numChannels  
Channels per array element

#### 5.33.1.8 pitchInBytes

size\_t HIP\_RESOURCE\_DESC\_st::pitchInBytes  
Pitch between two rows in bytes

#### 5.33.1.9 resType

HIPresourcetype HIP\_RESOURCE\_DESC\_st::resType  
Resource type

#### 5.33.1.10 sizeInBytes

size\_t HIP\_RESOURCE\_DESC\_st::sizeInBytes  
Size in bytes

#### 5.33.1.11 width

size\_t HIP\_RESOURCE\_DESC\_st::width  
Width of the array in elements

## 5.34 HIP\_RESOURCE\_VIEW\_DESC\_st Struct Reference

### Public Attributes

- HIPresourceViewFormat [format](#)
- size\_t [width](#)
- size\_t [height](#)
- size\_t [depth](#)
- unsigned int [firstMipmapLevel](#)
- unsigned int [lastMipmapLevel](#)
- unsigned int [firstLayer](#)
- unsigned int [lastLayer](#)
- unsigned int **reserved** [16]

### 5.34.1 Detailed Description

Resource view descriptor

### 5.34.2 Member Data Documentation

#### 5.34.2.1 depth

size\_t HIP\_RESOURCE\_VIEW\_DESC\_st::depth  
Depth of the resource view

#### 5.34.2.2 firstLayer

unsigned int HIP\_RESOURCE\_VIEW\_DESC\_st::firstLayer  
First layer index

#### 5.34.2.3 firstMipmapLevel

unsigned int HIP\_RESOURCE\_VIEW\_DESC\_st::firstMipmapLevel  
First defined mipmap level

#### 5.34.2.4 format

HIPResourceViewFormat HIP\_RESOURCE\_VIEW\_DESC\_st::format  
Resource view format

#### 5.34.2.5 height

size\_t HIP\_RESOURCE\_VIEW\_DESC\_st::height  
Height of the resource view

#### 5.34.2.6 lastLayer

unsigned int HIP\_RESOURCE\_VIEW\_DESC\_st::lastLayer  
Last layer index

#### 5.34.2.7 lastMipmapLevel

unsigned int HIP\_RESOURCE\_VIEW\_DESC\_st::lastMipmapLevel  
Last defined mipmap level

#### 5.34.2.8 width

size\_t HIP\_RESOURCE\_VIEW\_DESC\_st::width  
Width of the resource view

### 5.35 HIP\_TEXTURE\_DESC\_st Struct Reference

#### Public Attributes

- HIPAddress\_mode [addressMode](#) [3]
- HIPfilter\_mode [filterMode](#)
- unsigned int [flags](#)
- unsigned int [maxAnisotropy](#)
- HIPfilter\_mode [mipmapFilterMode](#)
- float [mipmapLevelBias](#)
- float [minMipmapLevelClamp](#)
- float [maxMipmapLevelClamp](#)
- float [borderColor](#) [4]
- int [reserved](#) [12]

#### 5.35.1 Detailed Description

Texture descriptor

## 5.35.2 Member Data Documentation

### 5.35.2.1 addressMode

HIPaddress\_mode HIP\_TEXTURE\_DESC\_st::addressMode[3]  
Address modes

### 5.35.2.2 borderColor

float HIP\_TEXTURE\_DESC\_st::borderColor[4]  
Border Color

### 5.35.2.3 filterMode

HIPfilter\_mode HIP\_TEXTURE\_DESC\_st::filterMode  
Filter mode

### 5.35.2.4 flags

unsigned int HIP\_TEXTURE\_DESC\_st::flags  
Flags

### 5.35.2.5 maxAnisotropy

unsigned int HIP\_TEXTURE\_DESC\_st::maxAnisotropy  
Maximum anisotropy ratio

### 5.35.2.6 maxMipmapLevelClamp

float HIP\_TEXTURE\_DESC\_st::maxMipmapLevelClamp  
Mipmap maximum level clamp

### 5.35.2.7 minMipmapLevelClamp

float HIP\_TEXTURE\_DESC\_st::minMipmapLevelClamp  
Mipmap minimum level clamp

### 5.35.2.8 mipmapFilterMode

HIPfilter\_mode HIP\_TEXTURE\_DESC\_st::mipmapFilterMode  
Mipmap filter mode

### 5.35.2.9 mipmapLevelBias

float HIP\_TEXTURE\_DESC\_st::mipmapLevelBias  
Mipmap level bias

## 5.36 hipArray Struct Reference

### Public Attributes

- void \* **data**
- struct [hipChannelFormatDesc](#) **desc**
- unsigned int **type**
- unsigned int **width**
- unsigned int **height**
- unsigned int **depth**

- enum hipArray\_Format **Format**
- unsigned int **NumChannels**
- bool **isDrv**
- unsigned int **textureType**

## 5.37 hipChannelFormatDesc Struct Reference

### Public Attributes

- int **x**
- int **y**
- int **z**
- int **w**
- enum hipChannelFormatKind **f**

## 5.38 hipDeviceArch\_t Struct Reference

### Public Attributes

- unsigned [hasGlobalInt32Atomics](#): 1  
*32-bit integer atomics for global memory.*
- unsigned [hasGlobalFloatAtomicExch](#): 1  
*32-bit float atomic exch for global memory.*
- unsigned [hasSharedInt32Atomics](#): 1  
*32-bit integer atomics for shared memory.*
- unsigned [hasSharedFloatAtomicExch](#): 1  
*32-bit float atomic exch for shared memory.*
- unsigned [hasFloatAtomicAdd](#): 1  
*32-bit float atomic add in global and shared memory.*
- unsigned [hasGlobalInt64Atomics](#): 1  
*64-bit integer atomics for global memory.*
- unsigned [hasSharedInt64Atomics](#): 1  
*64-bit integer atomics for shared memory.*
- unsigned [hasDoubles](#): 1  
*Double-precision floating point.*
- unsigned [hasWarpVote](#): 1  
*Warp vote instructions (`__any`, `__all`).*
- unsigned [hasWarpBallot](#): 1  
*Warp ballot instructions (`__ballot`).*
- unsigned [hasWarpShuffle](#): 1  
*Warp shuffle operations. (`__shfl_*`).*
- unsigned [hasFunnelShift](#): 1  
*Funnel two words into one with shift&mask caps.*
- unsigned [hasThreadFenceSystem](#): 1  
*`__threadfence_system`.*
- unsigned [hasSyncThreadsExt](#): 1  
*`__syncthreads_count`, `syncthreads_and`, `syncthreads_or`.*
- unsigned [hasSurfaceFuncs](#): 1  
*Surface functions.*
- unsigned [has3dGrid](#): 1  
*Grid and group dims are 3D (rather than 2D).*
- unsigned [hasDynamicParallelism](#): 1  
*Dynamic parallelism.*

## 5.39 hipDeviceProp\_t Struct Reference

### Public Attributes

- char [name](#) [256]  
*Device name.*
- size\_t [totalGlobalMem](#)  
*Size of global memory region (in bytes).*
- size\_t [sharedMemPerBlock](#)  
*Size of shared memory region (in bytes).*
- int [regsPerBlock](#)  
*Registers per block.*
- int [warpSize](#)  
*Warp size.*
- int [maxThreadsPerBlock](#)  
*Max work items per work group or workgroup max size.*
- int [maxThreadsDim](#) [3]  
*Max number of threads in each dimension (XYZ) of a block.*
- int [maxGridSize](#) [3]  
*Max grid dimensions (XYZ).*
- int [clockRate](#)  
*Max clock frequency of the multiProcessors in khz.*
- int [memoryClockRate](#)  
*Max global memory clock frequency in khz.*
- int [memoryBusWidth](#)  
*Global memory bus width in bits.*
- size\_t [totalConstMem](#)  
*Size of shared memory region (in bytes).*
- int [major](#)
- int [minor](#)
- int [multiProcessorCount](#)  
*Number of multi-processors (compute units).*
- int [l2CacheSize](#)  
*L2 cache size.*
- int [maxThreadsPerMultiProcessor](#)  
*Maximum resident threads per multi-processor.*
- int [computeMode](#)  
*Compute mode.*
- int [clockInstructionRate](#)
- [hipDeviceArch\\_t](#) [arch](#)  
*Architectural feature flags. New for HIP.*
- int [concurrentKernels](#)  
*Device can possibly execute multiple kernels concurrently.*
- int [pciDomainID](#)  
*PCI Domain ID.*
- int [pciBusID](#)  
*PCI Bus ID.*
- int [pciDeviceID](#)  
*PCI Device ID.*
- size\_t [maxSharedMemoryPerMultiProcessor](#)  
*Maximum Shared Memory Per Multiprocessor.*
- int [isMultiGpuBoard](#)

- 1 if device is on a multi-GPU board, 0 if not.
- int [canMapHostMemory](#)
  - Check whether HIP can map host memory.
- int [gcnArch](#)
  - DEPRECATED: use [gcnArchName](#) instead.
- char [gcnArchName](#) [256]
  - AMD GCN Arch Name.
- int [integrated](#)
  - APU vs dGPU.
- int [cooperativeLaunch](#)
  - HIP device supports cooperative launch.
- int [cooperativeMultiDeviceLaunch](#)
  - HIP device supports cooperative launch on multiple devices.
- int [maxTexture1DLinear](#)
  - Maximum size for 1D textures bound to linear memory.
- int [maxTexture1D](#)
  - Maximum number of elements in 1D images.
- int [maxTexture2D](#) [2]
  - Maximum dimensions (width, height) of 2D images, in image elements.
- int [maxTexture3D](#) [3]
  - Maximum dimensions (width, height, depth) of 3D images, in image elements.
- unsigned int \* [hdpMemFlushCntl](#)
  - Address of HDP\_MEM\_COHERENCY\_FLUSH\_CNTL register.
- unsigned int \* [hdpRegFlushCntl](#)
  - Address of HDP\_REG\_COHERENCY\_FLUSH\_CNTL register.
- size\_t [memPitch](#)
  - Maximum pitch in bytes allowed by memory copies.
- size\_t [textureAlignment](#)
  - Alignment requirement for textures.
- size\_t [texturePitchAlignment](#)
  - Pitch alignment requirement for texture references bound to pitched memory.
- int [kernelExecTimeoutEnabled](#)
  - Run time limit for kernels executed on the device.
- int [ECCEnabled](#)
  - Device has ECC support enabled.
- int [tccDriver](#)
  - 1: If device is Tesla device using TCC driver, else 0
- int [cooperativeMultiDeviceUnmatchedFunc](#)
- int [cooperativeMultiDeviceUnmatchedGridDim](#)
- int [cooperativeMultiDeviceUnmatchedBlockDim](#)
- int [cooperativeMultiDeviceUnmatchedSharedMem](#)
- int [isLargeBar](#)
  - 1: if it is a large PCI bar device, else 0
- int [asicRevision](#)
  - Revision of the GPU in this device.
- int [managedMemory](#)
  - Device supports allocating managed memory on this system.
- int [directManagedMemAccessFromHost](#)
  - Host can directly access managed memory on the device without migration.
- int [concurrentManagedAccess](#)
  - Device can coherently access managed memory concurrently with the CPU.
- int [pageableMemoryAccess](#)
- int [pageableMemoryAccessUsesHostPageTables](#)
  - Device accesses pageable memory via the host's page tables.



### 5.39.1 Detailed Description

hipDeviceProp

### 5.39.2 Member Data Documentation

#### 5.39.2.1 clockInstructionRate

```
int hipDeviceProp_t::clockInstructionRate
```

Frequency in khz of the timer used by the device-side "clock\*" instructions. New for HIP.

#### 5.39.2.2 cooperativeMultiDeviceUnmatchedBlockDim

```
int hipDeviceProp_t::cooperativeMultiDeviceUnmatchedBlockDim
```

HIP device supports cooperative launch on multiple devices with unmatched block dimensions

#### 5.39.2.3 cooperativeMultiDeviceUnmatchedFunc

```
int hipDeviceProp_t::cooperativeMultiDeviceUnmatchedFunc
```

HIP device supports cooperative launch on multiple devices with unmatched functions

#### 5.39.2.4 cooperativeMultiDeviceUnmatchedGridDim

```
int hipDeviceProp_t::cooperativeMultiDeviceUnmatchedGridDim
```

HIP device supports cooperative launch on multiple devices with unmatched grid dimensions

#### 5.39.2.5 cooperativeMultiDeviceUnmatchedSharedMem

```
int hipDeviceProp_t::cooperativeMultiDeviceUnmatchedSharedMem
```

HIP device supports cooperative launch on multiple devices with unmatched shared memories

#### 5.39.2.6 major

```
int hipDeviceProp_t::major
```

Major compute capability. On HCC, this is an approximation and features may differ from CUDA CC. See the arch feature flags for portable ways to query feature caps.

#### 5.39.2.7 minor

```
int hipDeviceProp_t::minor
```

Minor compute capability. On HCC, this is an approximation and features may differ from CUDA CC. See the arch feature flags for portable ways to query feature caps.

#### 5.39.2.8 pageableMemoryAccess

```
int hipDeviceProp_t::pageableMemoryAccess
```

Device supports coherently accessing pageable memory without calling hipHostRegister on it

## 5.40 hipExtent Struct Reference

### Public Attributes

- `size_t width`
- `size_t height`
- `size_t depth`

## 5.41 hipExternalMemoryBufferDesc\_st Struct Reference

### Public Attributes

- unsigned long long **offset**
- unsigned long long **size**
- unsigned int **flags**

## 5.42 hipExternalMemoryHandleDesc\_st Struct Reference

### Public Attributes

- hipExternalMemoryHandleType **type**
- 

```
union {
    int fd
    struct {
        void * handle
        const void * name
    } win32
} handle
```

- unsigned long long **size**
- unsigned int **flags**

## 5.43 hipExternalSemaphoreHandleDesc\_st Struct Reference

### Public Attributes

- hipExternalSemaphoreHandleType **type**
- 

```
union {
    int fd
    struct {
        void * handle
        const void * name
    } win32
} handle
```

- unsigned int **flags**

## 5.44 hipExternalSemaphoreSignalParams\_st Struct Reference

### Public Attributes

```
•
struct {
    struct {
        unsigned long long value
    } fence
    struct {
        unsigned long long key
    } keyedMutex
    unsigned int reserved [12]
} params
```

- unsigned int **flags**

## 5.45 hipExternalSemaphoreWaitParams\_st Struct Reference

### Public Attributes

- ```

struct {
    struct {
        unsigned long long value
    } fence
    struct {
        unsigned long long key
        unsigned int timeoutMs
    } keyedMutex
    unsigned int reserved [10]
} params

```
- unsigned int **flags**

### 5.45.1 Detailed Description

External semaphore wait parameters, compatible with driver type

## 5.46 hipFuncAttributes Struct Reference

### Public Attributes

- int **binaryVersion**
- int **cacheModeCA**
- size\_t **constSizeBytes**
- size\_t **localSizeBytes**
- int **maxDynamicSharedSizeBytes**
- int **maxThreadsPerBlock**
- int **numRegs**
- int **preferredShmemCarveout**
- int **ptxVersion**
- size\_t **sharedSizeBytes**

## 5.47 hipHostNodeParams Struct Reference

### Public Attributes

- hipHostFn\_t **fn**
- void \* **userData**

## 5.48 hipIpcEventHandle\_st Struct Reference

### Public Attributes

- char **reserved** [HIP\_IPC\_HANDLE\_SIZE]

## 5.49 hipIpcMemHandle\_st Struct Reference

### Public Attributes

- char **reserved** [HIP\_IPC\_HANDLE\_SIZE]

## 5.50 hipKernelNodeParams Struct Reference

### Public Attributes

- [dim3](#) **blockDim**
- void \*\* **extra**
- void \* **func**
- [dim3](#) **gridDim**
- void \*\* **kernelParams**
- unsigned int **sharedMemBytes**

## 5.51 hipLaunchParams\_t Struct Reference

### Public Attributes

- void \* [func](#)  
*Device function symbol.*
- [dim3](#) [gridDim](#)  
*Grid dimentions.*
- [dim3](#) [blockDim](#)  
*Block dimentions.*
- void \*\* [args](#)  
*Arguments.*
- size\_t [sharedMem](#)  
*Shared memory.*
- hipStream\_t [stream](#)  
*Stream identifier.*

## 5.52 hipMemcpy3DParms Struct Reference

### Public Attributes

- [hipArray\\_t](#) **srcArray**
- struct [hipPos](#) **srcPos**
- struct [hipPitchedPtr](#) **srcPtr**
- [hipArray\\_t](#) **dstArray**
- struct [hipPos](#) **dstPos**
- struct [hipPitchedPtr](#) **dstPtr**
- struct [hipExtent](#) **extent**
- enum [hipMemcpyKind](#) **kind**

## 5.53 hipMemsetParams Struct Reference

### Public Attributes

- void \* **dst**
- unsigned int **elementSize**
- size\_t **height**

- size\_t **pitch**
- unsigned int **value**
- size\_t **width**

## 5.54 hipMipmappedArray Struct Reference

### Public Attributes

- void \* **data**
- struct [hipChannelFormatDesc](#) **desc**
- unsigned int **type**
- unsigned int **width**
- unsigned int **height**
- unsigned int **depth**
- unsigned int **min\_mipmap\_level**
- unsigned int **max\_mipmap\_level**
- unsigned int **flags**
- enum hipArray\_Format **format**

## 5.55 hipPitchedPtr Struct Reference

### Public Attributes

- void \* **ptr**
- size\_t **pitch**
- size\_t **xsize**
- size\_t **ysize**

## 5.56 hipPointerAttribute\_t Struct Reference

### Public Attributes

- enum hipMemoryType **memoryType**
- int **device**
- void \* **devicePointer**
- void \* **hostPointer**
- int **isManaged**
- unsigned **allocationFlags**

### 5.56.1 Detailed Description

Pointer attributes

## 5.57 hipPos Struct Reference

### Public Attributes

- size\_t **x**
- size\_t **y**
- size\_t **z**

## 5.58 hipResourceDesc Struct Reference

### Public Attributes

- enum hipResourceType **resType**
- 
- union {
 struct {
 [hipArray\\_t](#) **array**
 } **array**
 struct {
 [hipMipmappedArray\\_t](#) **mipmap**
 } **mipmap**
 struct {
 void \* **devPtr**
 struct [hipChannelFormatDesc](#) **desc**
 size\_t **sizeInBytes**
 } **linear**
 struct {
 void \* **devPtr**
 struct [hipChannelFormatDesc](#) **desc**
 size\_t **width**
 size\_t **height**
 size\_t **pitchInBytes**
 } **pitch2D**
 } **res**

### 5.58.1 Detailed Description

HIP resource descriptor

## 5.59 hipResourceViewDesc Struct Reference

### Public Attributes

- enum hipResourceViewFormat **format**
- size\_t **width**
- size\_t **height**
- size\_t **depth**
- unsigned int **firstMipmapLevel**
- unsigned int **lastMipmapLevel**
- unsigned int **firstLayer**
- unsigned int **lastLayer**

### 5.59.1 Detailed Description

hip resource view descriptor

## 5.60 hipTextureDesc Struct Reference

### Public Attributes

- enum hipTextureAddressMode **addressMode** [3]
- enum hipTextureFilterMode **filterMode**
- enum hipTextureReadMode **readMode**
- int **sRGB**

- float **borderColor** [4]
- int **normalizedCoords**
- unsigned int **maxAnisotropy**
- enum hipTextureFilterMode **mipmapFilterMode**
- float **mipmapLevelBias**
- float **minMipmapLevelClamp**
- float **maxMipmapLevelClamp**

### 5.60.1 Detailed Description

hip texture descriptor

## 5.61 int1 Union Reference

### Public Attributes

- int **data**

## 5.62 int16 Union Reference

### Public Attributes

- int **data** [16]

## 5.63 int2 Union Reference

### Public Attributes

- int **data** [2]

## 5.64 int3 Union Reference

### Public Attributes

- [int4](#) **data**

## 5.65 int4 Union Reference

### Public Attributes

- int **data** [4]

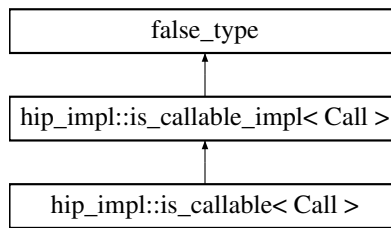
## 5.66 int8 Union Reference

### Public Attributes

- int **data** [8]

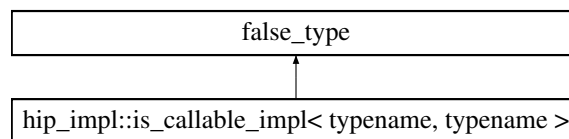
## 5.67 hip\_impl::is\_callable< Call > Struct Template Reference

Inheritance diagram for hip\_impl::is\_callable< Call >:



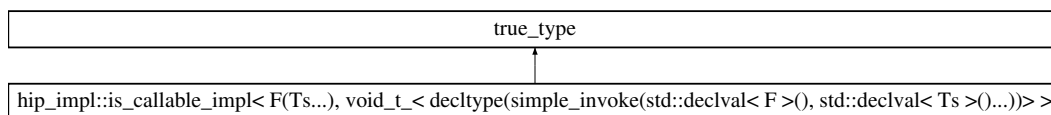
## 5.68 hip\_impl::is\_callable\_impl< typename, typename > Struct Template Reference

Inheritance diagram for hip\_impl::is\_callable\_impl< typename, typename >:



## 5.69 hip\_impl::is\_callable\_impl< F(Ts...), void\_t\_< decltype(simple\_invoke(std::declval< F >()), std::declval< Ts >()...))> > Struct Template Reference

Inheritance diagram for hip\_impl::is\_callable\_impl< F(Ts...), void\_t\_< decltype(simple\_invoke(std::declval< F >()), std::declval< Ts >()...))> >:



## 5.70 hip\_impl::kernarg Class Reference

### Public Member Functions

- **kernarg** ([kernarg](#) &&)
- `std::uint8_t * data ()`
- `std::size_t size ()`
- `void reserve (std::size_t)`
- `void resize (std::size_t)`
- **kernarg** ([kernarg](#) &&)
- `std::uint8_t * data ()`
- `std::size_t size ()`
- `void reserve (std::size_t)`
- `void resize (std::size_t)`

## 5.71 hip\_impl::kernargs\_size\_align Class Reference

### Public Member Functions

- `std::size_t size (std::size_t n) const`



- `std::size_t alignment` (`std::size_t n`) `const`
- `const void * getHandle` () `const`
- `std::size_t size` (`std::size_t n`) `const`
- `std::size_t alignment` (`std::size_t n`) `const`
- `const void * getHandle` () `const`

### Friends

- [kernargs\\_size\\_align](#) `program_state::get_kernargs_size_align` (`std::uintptr_t`)
- [kernargs\\_size\\_align](#) `program_state::get_kernargs_size_align` (`std::uintptr_t`)

## 5.72 long1 Union Reference

### Public Attributes

- `long data`

## 5.73 long16 Union Reference

### Public Attributes

- `long data` [16]

## 5.74 long2 Union Reference

### Public Attributes

- `long data` [2]

## 5.75 long3 Union Reference

### Public Attributes

- [long4](#) `data`

## 5.76 long4 Union Reference

### Public Attributes

- `long data` [4]

## 5.77 long8 Union Reference

### Public Attributes

- `long data` [8]

## 5.78 longlong1 Union Reference

### Public Attributes

- `long long data`

## 5.79 `longlong16` Union Reference

### Public Attributes

- `long long data` [16]

## 5.80 `longlong2` Union Reference

### Public Attributes

- `long long data` [2]

## 5.81 `longlong3` Union Reference

### Public Attributes

- `longlong4 data`

## 5.82 `longlong4` Union Reference

### Public Attributes

- `long long data` [4]

## 5.83 `longlong8` Union Reference

### Public Attributes

- `long long data` [8]

## 5.84 `hip_impl::program_state` Class Reference

### Public Member Functions

- `program_state` (const `program_state` &)=delete
- `hipFunction_t kernel_descriptor` (std::uintptr\_t, hsa\_agent\_t)
- `kernargs_size_align get_kernargs_size_align` (std::uintptr\_t)
- `hsa_executable_t load_executable` (const char \*, const size\_t, hsa\_executable\_t, hsa\_agent\_t)
- `hsa_executable_t load_executable_no_copy` (const char \*, const size\_t, hsa\_executable\_t, hsa\_agent\_t)
- void \* `global_addr_by_name` (const char \*name)
- `program_state` (const `program_state` &)=delete
- `hipFunction_t kernel_descriptor` (std::uintptr\_t, hsa\_agent\_t)
- `kernargs_size_align get_kernargs_size_align` (std::uintptr\_t)
- `hsa_executable_t load_executable` (const char \*, const size\_t, hsa\_executable\_t, hsa\_agent\_t)
- `hsa_executable_t load_executable_no_copy` (const char \*, const size\_t, hsa\_executable\_t, hsa\_agent\_t)
- void \* `global_addr_by_name` (const char \*name)

### Friends

- class `agent_globals_impl`

## 5.85 `short1` Union Reference

### Public Attributes

- `short data`

## 5.86 short16 Union Reference

### Public Attributes

- short **data** [16]

## 5.87 short2 Union Reference

### Public Attributes

- short **data** [2]

## 5.88 short3 Union Reference

### Public Attributes

- [short4](#) **data**

## 5.89 short4 Union Reference

### Public Attributes

- short **data** [4]

## 5.90 short8 Union Reference

### Public Attributes

- short **data** [8]

## 5.91 surfaceReference Struct Reference

### Public Attributes

- hipSurfaceObject\_t **surfaceObject**

### 5.91.1 Detailed Description

hip surface reference

## 5.92 TData Union Reference

### Public Attributes

- \_\_hip\_float4\_vector\_value\_type **f**
- \_\_hip\_int4\_vector\_value\_type **i**
- \_\_hip\_uint4\_vector\_value\_type **u**

## 5.93 textureReference Struct Reference

### Public Attributes

- int **normalized**
- enum hipTextureReadMode **readMode**
- enum hipTextureFilterMode **filterMode**

- enum hipTextureAddressMode **addressMode** [3]
- struct [hipChannelFormatDesc](#) **channelDesc**
- int **sRGB**
- unsigned int **maxAnisotropy**
- enum hipTextureFilterMode **mipmapFilterMode**
- float **mipmapLevelBias**
- float **minMipmapLevelClamp**
- float **maxMipmapLevelClamp**
- hipTextureObject\_t **textureObject**
- int **numChannels**
- enum hipArray\_Format **format**

### 5.93.1 Detailed Description

hip texture reference

## 5.94 uchar1 Union Reference

### Public Attributes

- unsigned char **data**

## 5.95 uchar16 Union Reference

### Public Attributes

- unsigned char **data** [16]

## 5.96 uchar2 Union Reference

### Public Attributes

- unsigned char **data** [2]

## 5.97 uchar2Holder Struct Reference

### Public Attributes

- ```
union {
    unsigned int ui [2]
    unsigned char c [8]
};
```

## 5.98 uchar3 Union Reference

### Public Attributes

- [uchar4](#) **data**

## 5.99 uchar4 Union Reference

### Public Attributes

- unsigned char **data** [4]

## 5.100 uchar8 Union Reference

### Public Attributes

- unsigned char **data** [8]

## 5.101 ucharHolder Struct Reference

### Public Attributes

- union {  
    unsigned char **c** [4]  
    unsigned int **ui**  
} **\_\_attribute\_\_**

## 5.102 uint1 Union Reference

### Public Attributes

- unsigned int **data**

## 5.103 uint16 Union Reference

### Public Attributes

- unsigned int **data** [16]

## 5.104 uint2 Union Reference

### Public Attributes

- unsigned int **data** [2]

## 5.105 uint3 Union Reference

### Public Attributes

- [uint4](#) **data**

## 5.106 uint4 Union Reference

### Public Attributes

- unsigned int **data** [4]

## 5.107 uint8 Union Reference

### Public Attributes

- unsigned int **data** [8]

## 5.108 ulong1 Union Reference

### Public Attributes

- unsigned long **data**

## 5.109 ulong16 Union Reference

### Public Attributes

- unsigned long **data** [16]

## 5.110 ulong2 Union Reference

### Public Attributes

- unsigned long **data** [2]

## 5.111 ulong3 Union Reference

### Public Attributes

- [ulong4](#) **data**

## 5.112 ulong4 Union Reference

### Public Attributes

- unsigned long **data** [4]

## 5.113 ulong8 Union Reference

### Public Attributes

- unsigned long **data** [8]

## 5.114 ulonglong1 Union Reference

### Public Attributes

- unsigned long long **data**

## 5.115 ulonglong16 Union Reference

### Public Attributes

- unsigned long long **data** [16]

## 5.116 `ulonglong2` Union Reference

### Public Attributes

- unsigned long long `data` [2]

## 5.117 `ulonglong3` Union Reference

### Public Attributes

- `ulonglong4` `data`

## 5.118 `ulonglong4` Union Reference

### Public Attributes

- unsigned long long `data` [4]

## 5.119 `ulonglong8` Union Reference

### Public Attributes

- unsigned long long `data` [8]

## 5.120 `ushort1` Union Reference

### Public Attributes

- unsigned short `data`

## 5.121 `ushort16` Union Reference

### Public Attributes

- unsigned short `data` [16]

## 5.122 `ushort2` Union Reference

### Public Attributes

- unsigned short `data` [2]

## 5.123 `ushort3` Union Reference

### Public Attributes

- `ushort4` `data`

## 5.124 `ushort4` Union Reference

### Public Attributes

- unsigned short `data` [4]

## 5.125 ushort8 Union Reference

### Public Attributes

- unsigned short **data** [8]