



AMD ROC_m[™]

HIP-Supported CUDA API Reference Guide

Publication #	1.0	Revision:	0511
Issue Date:	May 2021		

Specification Agreement

This Specification Agreement (this “Agreement”) is a legal agreement between Advanced Micro Devices, Inc. (“AMD”) and “You” as the recipient of the attached AMD Specification (the “Specification”). If you are accessing the Specification as part of your performance of work for another party, you acknowledge that you have authority to bind such party to the terms and conditions of this Agreement. If you accessed the Specification by any means or otherwise use or provide Feedback (defined below) on the Specification, You agree to the terms and conditions set forth in this Agreement. If You do not agree to the terms and conditions set forth in this Agreement, you are not licensed to use the Specification; do not use, access or provide Feedback about the Specification. In consideration of Your use or access of the Specification (in whole or in part), the receipt and sufficiency of which are acknowledged, You agree as follows:

1. You may review the Specification only (a) as a reference to assist You in planning and designing Your product, service or technology (“Product”) to interface with an AMD product in compliance with the requirements as set forth in the Specification and (b) to provide Feedback about the information disclosed in the Specification to AMD.
2. Except as expressly set forth in Paragraph 1, all rights in and to the Specification are retained by AMD. This Agreement does not give You any rights under any AMD patents, copyrights, trademarks or other intellectual property rights. You may not (i) duplicate any part of the Specification; (ii) remove this Agreement or any notices from the Specification, or (iii) give any part of the Specification, or assign or otherwise provide Your rights under this Agreement, to anyone else.
3. The Specification may contain preliminary information, errors, or inaccuracies, or may not include certain necessary information. Additionally, AMD reserves the right to discontinue or make changes to the Specification and its products at any time without notice. The Specification is provided entirely “AS IS.” AMD MAKES NO WARRANTY OF ANY KIND AND DISCLAIMS ALL EXPRESS, IMPLIED AND STATUTORY WARRANTIES, INCLUDING BUT NOT LIMITED TO IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, NONINFRINGEMENT, TITLE OR THOSE WARRANTIES ARISING AS A COURSE OF DEALING OR CUSTOM OF TRADE. AMD SHALL NOT BE LIABLE FOR DIRECT, INDIRECT, CONSEQUENTIAL, SPECIAL, INCIDENTAL, PUNITIVE OR EXEMPLARY DAMAGES OF ANY KIND (INCLUDING LOSS OF BUSINESS, LOSS OF INFORMATION OR DATA, LOST PROFITS, LOSS OF CAPITAL, LOSS OF GOODWILL) REGARDLESS OF THE FORM OF ACTION WHETHER IN CONTRACT, TORT (INCLUDING NEGLIGENCE) AND STRICT PRODUCT LIABILITY OR OTHERWISE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.
4. Furthermore, AMD’s products are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or in any other application in which the failure of AMD’s product could create a situation where personal injury, death, or severe property or environmental damage may occur.
5. You have no obligation to give AMD any suggestions, comments or feedback (“Feedback”) relating to the Specification. However, any Feedback You voluntarily provide may be used by AMD without restriction, fee or obligation of confidentiality. Accordingly, if You do give AMD Feedback on any version of the Specification, You agree AMD may freely use, reproduce, license, distribute, and otherwise commercialize Your Feedback in any product, as well as has the right to sublicense third parties to do the same. Further, You will not give AMD any Feedback that You may have reason to believe is (i) subject to any patent, copyright or other intellectual property claim or right of any third party; or (ii) subject to license terms which seek to require any product or intellectual property incorporating or derived from Feedback or any Product or other AMD intellectual property to be licensed to or otherwise provided to any third party.
6. You shall adhere to all applicable U.S. import/export laws and regulations, as well as the import/export control laws and regulations of other countries as applicable. You further agree to not export, re-export, or transfer, directly or indirectly, any product, technical data, software or source code received from AMD under this license, or the direct product of such technical data or software to any country for which the United States or any other applicable government requires an export license or other governmental approval without first obtaining such licenses or approvals; or in violation of any applicable laws or regulations of the United States or the country where the technical data or software was obtained. You acknowledge the technical data and software received will not, in the absence of authorization from U.S. or local law and regulations as applicable, be used by or exported, re-exported or transferred to: (i) any sanctioned or embargoed country, or to nationals or residents of such countries; (ii) any restricted end-user as identified on any applicable government end-user list; or (iii) any party where the end-use involves nuclear, chemical/biological weapons, rocket systems, or unmanned air vehicles. For the most current Country Group listings,

or for additional information about the EAR or Your obligations under those regulations, please refer to the U.S. Bureau of Industry and Security's website at <http://www.bis.doc.gov/>.

7. The Software and related documentation are "commercial items", as that term is defined at 48 C.F.R. §2.101, consisting of "commercial computer software" and "commercial computer software documentation", as such terms are used in 48 C.F.R. §12.212 and 48 C.F.R. §227.7202, respectively. Consistent with 48 C.F.R. §12.212 or 48 C.F.R. §227.7202-1 through 227.7202-4, as applicable, the commercial computer software and commercial computer software documentation are being licensed to U.S. Government end users (a) only as commercial items and (b) with only those rights as are granted to all other end users pursuant to the terms and conditions set forth in this Agreement. Unpublished rights are reserved under the copyright laws of the United States.

8. This Agreement is governed by the laws of the State of California without regard to its choice of law principles. Any dispute involving it must be brought in a court having jurisdiction of such dispute in Santa Clara County, California, and You waive any defenses and rights allowing the dispute to be litigated elsewhere. If any part of this agreement is unenforceable, it will be considered modified to the extent necessary to make it enforceable, and the remainder shall continue in effect. The failure of AMD to enforce any rights granted hereunder or to take action against You in the event of any breach hereunder shall not be deemed a waiver by AMD as to subsequent enforcement of rights or subsequent actions in the event of future breaches. This Agreement is the entire agreement between You and AMD concerning the Specification; it may be changed only by a written document signed by both You and an authorized representative of AMD.

DISCLAIMER

The information contained herein is for informational purposes only, and is subject to change without notice. In addition, any stated support is planned and is also subject to change. While every precaution has been taken in the preparation of this document, it may contain technical inaccuracies, omissions and typographical errors, and AMD is under no obligation to update or otherwise correct this information. Advanced Micro Devices, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this document, and assumes no liability of any kind, including the implied warranties of noninfringement, merchantability or fitness for particular purposes, with respect to the operation or use of AMD hardware, software or other products described herein. No license, including implied or arising by estoppel, to any intellectual property rights is granted by this document. Terms and limitations applicable to the purchase or use of AMD's products are as set forth in a signed agreement between the parties or in AMD's Standard Terms and Conditions of Sale.

* AMD®, the AMD Arrow logo, [AMD Instinct™](#), [Radeon™](#), ROCm® and combinations

* thereof are trademarks of Advanced Micro Devices, Inc. Linux® is the registered trademark of Linus Torvalds in the U.S. and other countries.

* PCIe® is a registered trademark of PCI-SIG Corporation. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies.

[This page left blank intentionally]

Table of Contents

Table of Contents	5
Chapter 1 HIP-Supported CUDA API.....	8
1.1 CUDA Runtime API Supported by HIP	8
1.1.1 Device Management	8
1.1.2 Thread Management [Deprecated]	9
1.1.3 Error Handling	9
1.1.4 Stream Management	9
1.1.5 Event Management	9
1.1.6 Execution Control	10
1.1.7 Occupancy.....	10
1.1.8 Memory Management.....	10
1.1.9 Memory Management [Deprecated]	11
1.1.10 Unified Addressing	11
1.1.11 Peer Device Memory Access	11
1.1.12 Texture Reference Management [Deprecated]	11
1.1.13 Texture Object Management.....	12
1.1.14 Surface Object Management.....	12
1.1.15 Version Management	12
1.1.16 Profiler Control	12
1.1.17 Data types used by CUDA Runtime	12
1.1.18 Execution Control [Removed]	18
1.1.19 Unsupported APIs.....	19
1.2 CUDA Driver API Supported by HIP	19
1.2.1 CUDA Driver Data Types	19
1.2.2 Initialization	29
1.2.3 Version Management	29
1.2.4 Device Management	29
1.2.5 Device Management [Deprecated]	29
1.2.6 Primary Context Management	29
1.2.7 Context Management	29

1.2.8	Module Management.....	30
1.2.9	Memory Management	30
1.2.10	Unified Addressing	32
1.2.11	Stream Management.....	32
1.2.12	Event Management.....	32
1.2.13	Stream Memory Operations	32
1.2.14	Execution Control	32
1.2.15	Occupancy	33
1.2.16	Texture Reference Management [Deprecated].....	33
1.2.17	Texture Object Management.....	34
1.2.18	Peer Context Memory Access	34
1.2.19	Profiler Control	34
1.2.20	Unsupported APIs	34
1.3	CUCOMPLEX API Supported by HIP.....	35
1.3.1	cuComplex Data Types	35
1.3.2	cuComplex API Functions	35
1.4	CUDA DEVICE API Supported by HIP	35
1.4.1	Device Functions.....	35
1.5	CUBLAS API Supported by HIP.....	45
1.5.1	CUBLAS Data Types.....	45
1.5.2	CUDA Data Types Reference	46
1.5.3	CUBLAS Helper Function Reference	47
1.5.4	CUBLAS Level-1 Function Reference	47
1.5.5	CUBLAS Level-2 Function Reference	49
1.5.6	CUBLAS Level-3 Function Reference	52
1.5.7	BLAS-like Extension	54
1.6	CURAND API Supported by HIP.....	55
1.6.1	CURAND Data Types.....	55
1.6.2	Host API Functions	56
1.6.3	Device API Functions	56
1.7	CUFFT API Supported by HIP	57
1.7.1	CUFFT Data Types	57
1.7.2	CUFFT API Functions	58

1.8	CUSPARSE API Supported by HIP	59
1.8.1	CUSPARSE Types References	59
1.8.2	CUSPARSE Management Function Reference	60
1.8.3	CUSPARSE Helper Function Reference	60
1.8.4	CUSPARSE Level 1 Function Reference	61
1.8.5	CUSPARSE Level 2 Function Reference	62
1.8.6	CUSPARSE Level 3 Function Reference	63
1.8.7	CUSPARSE Extra Function Reference	63
1.8.8	CUSPARSE Preconditioners Reference	64
1.8.9	CUSPARSE Format Conversion Reference	66
1.8.10	Unsupported APIs	68
1.9	CUDNN API Supported by HIP	68
1.9.1	CUDNN Data Types	68
1.9.2	CUDNN Functions	72
1.10	Stream Memory Operations	75

Chapter 1 HIP-Supported CUDA API

This reference guide consists of CUDA APIs supported in HIP and covers the Driver API, Runtime API, cuComplex API, Device API, and APIs for the following supported libraries:

- cuBLAS
- cuRAND
- cuFFT
- cuSPARSE
- cuDNN

Note, deprecated APIs are identified and marked in the Deprecated column.

For details about HIP APIs, see the HIP API Guide at

https://github.com/RadeonOpenCompute/ROCm/blob/master/AMD_HIP_API_Guide_4.2.pdf

1.1 CUDA Runtime API Supported by HIP

1.1.1 Device Management

CUDA	Deprecated	HIP	Deprecated
cudaChooseDevice		hipChooseDevice	
cudaDeviceGetAttribute		hipDeviceGetAttribute	
cudaDeviceGetByPCIBusId		hipDeviceGetByPCIBusId	
cudaDeviceGetCacheConfig		hipDeviceGetCacheConfig	
cudaDeviceGetLimit		hipDeviceGetLimit	
cudaDeviceGetP2PAttribute		hipDeviceGetP2PAttribute	
cudaDeviceGetPCIBusId		hipDeviceGetPCIBusId	
cudaDeviceGetSharedMemConfig		hipDeviceGetSharedMemConfig	
cudaDeviceGetStreamPriorityRange		hipDeviceGetStreamPriorityRange	
cudaDeviceReset		hipDeviceReset	
cudaDeviceSetCacheConfig		hipDeviceSetCacheConfig	
cudaDeviceSetSharedMemConfig		hipDeviceSetSharedMemConfig	
cudaDeviceSynchronize		hipDeviceSynchronize	
cudaGetDevice		hipGetDevice	
cudaGetDeviceCount		hipGetDeviceCount	
cudaGetDeviceFlags		hipGetDeviceFlags	
cudaGetDeviceProperties		hipGetDeviceProperties	
cudaIpcCloseMemHandle		hipIpcCloseMemHandle	
cudaIpcGetEventHandle		hipIpcGetEventHandle	
cudaIpcGetMemHandle		hipIpcGetMemHandle	
cudaIpcOpenEventHandle		hipIpcOpenEventHandle	
cudaIpcOpenMemHandle		hipIpcOpenMemHandle	
cudaSetDevice		hipSetDevice	
cudaSetDeviceFlags		hipSetDeviceFlags	

1.1.2 Thread Management [Deprecated]

CUDA	Deprecated	HIP	Deprecated
cudaThreadExit	✓	hipDeviceReset	
cudaThreadGetCacheConfig	✓	hipDeviceGetCacheConfig	
cudaThreadSetCacheConfig	✓	hipDeviceSetCacheConfig	
cudaThreadSynchronize	✓	hipDeviceSynchronize	

1.1.3 Error Handling

CUDA	Deprecated	HIP	Deprecated
cudaGetErrorName		hipGetErrorName	
cudaGetErrorString		hipGetErrorString	
cudaGetLastError		hipGetLastError	
cudaPeekAtLastError		hipPeekAtLastError	

1.1.4 Stream Management

CUDA	Deprecated	HIP	Deprecated
cudaStreamAddCallback		hipStreamAddCallback	
cudaStreamAttachMemAsync		hipStreamAttachMemAsync	
cudaStreamCreate		hipStreamCreate	
cudaStreamCreateWithFlags		hipStreamCreateWithFlags	
cudaStreamCreateWithPriority		hipStreamCreateWithPriority	
cudaStreamDestroy		hipStreamDestroy	
cudaStreamGetFlags		hipStreamGetFlags	
cudaStreamGetPriority		hipStreamGetPriority	
cudaStreamQuery		hipStreamQuery	
cudaStreamSynchronize		hipStreamSynchronize	
cudaStreamWaitEvent		hipStreamWaitEvent	
cudaStreamAddCallback		hipStreamAddCallback	

1.1.5 Event Management

CUDA	Deprecated	HIP	Deprecated
cudaEventCreate		hipEventCreate	
cudaEventCreateWithFlags		hipEventCreateWithFlags	
cudaEventDestroy		hipEventDestroy	
cudaEventElapsedTime		hipEventElapsedTime	
cudaEventQuery		hipEventQuery	
cudaEventRecord		hipEventRecord	
cudaEventSynchronize		hipEventSynchronize	

1.1.6 Execution Control

CUDA	Deprecated	HIP	Deprecated
cudaFuncGetAttributes		hipFuncGetAttributes	
cudaFuncSetAttribute		hipFuncSetAttribute	
cudaFuncSetCacheConfig		hipFuncSetCacheConfig	
cudaFuncSetSharedMemConfig		hipFuncSetSharedMemConfig	
cudaLaunchCooperativeKernel		hipLaunchCooperativeKernel	
cudaLaunchCooperativeKernelMultiDevice		hipLaunchCooperativeKernelMultiDevice	
cudaLaunchKernel		hipLaunchKernel	

1.1.7 Occupancy

CUDA	Deprecated	HIP	Deprecated
cudaOccupancyMaxActiveBlocksPerMultiprocessor		hipOccupancyMaxActiveBlocksPerMultiprocessor	
cudaOccupancyMaxActiveBlocksPerMultiprocessorWithFlags		hipOccupancyMaxActiveBlocksPerMultiprocessorWithFlags	
cudaOccupancyMaxPotentialBlockSize		hipOccupancyMaxPotentialBlockSize	
cudaOccupancyMaxPotentialBlockSizeWithFlags		hipOccupancyMaxPotentialBlockSizeWithFlags	

1.1.8 Memory Management

CUDA	Deprecated	HIP	Deprecated
cudaFree		hipFree	
cudaFreeArray		hipFreeArray	
cudaFreeHost		hipHostFree	
cudaFreeMipmappedArray		hipFreeMipmappedArray	
cudaGetMipmappedArrayLevel		hipGetMipmappedArrayLevel	
cudaGetSymbolAddress		hipGetSymbolAddress	
cudaGetSymbolSize		hipGetSymbolSize	
cudaHostAlloc		hipHostMalloc	
cudaHostGetDevicePointer		hipHostGetDevicePointer	
cudaHostGetFlags		hipHostGetFlags	
cudaHostRegister		hipHostRegister	
cudaHostUnregister		hipHostUnregister	
cudaMalloc		hipMalloc	
cudaMalloc3D		hipMalloc3D	
cudaMalloc3DArray		hipMalloc3DArray	
cudaMallocArray		hipMallocArray	
cudaMallocHost		hipHostMalloc	
cudaMallocManaged		hipMallocManaged	
cudaMallocMipmappedArray		hipMallocMipmappedArray	
cudaMallocPitch		hipMallocPitch	
cudaMemAdvise		hipMemAdvise	
cudaMemGetInfo		hipMemGetInfo	
cudaMemPrefetchAsync		hipMemPrefetchAsync	
cudaMemRangeGetAttribute		hipMemRangeGetAttribute	
cudaMemRangeGetAttributes		hipMemRangeGetAttributes	
cudaMemcpy		hipMemcpy	
cudaMemcpy2D		hipMemcpy2D	

cudaMemcpy2DAsync		hipMemcpy2DAsync	
cudaMemcpy2DFromArray		hipMemcpy2DFromArray	
cudaMemcpy2DFromArrayAsync		hipMemcpy2DFromArrayAsync	
cudaMemcpy2DToArray		hipMemcpy2DToArray	
cudaMemcpy3D		hipMemcpy3D	
cudaMemcpy3DAsync		hipMemcpy3DAsync	
cudaMemcpyAsync		hipMemcpyAsync	
cudaMemcpyFromSymbol		hipMemcpyFromSymbol	
cudaMemcpyFromSymbolAsync		hipMemcpyFromSymbolAsync	
cudaMemcpyPeer		hipMemcpyPeer	
cudaMemcpyPeerAsync		hipMemcpyPeerAsync	
cudaMemcpyToSymbol		hipMemcpyToSymbol	
cudaMemcpyToSymbolAsync		hipMemcpyToSymbolAsync	
cudaMemset		hipMemset	
cudaMemset2D		hipMemset2D	
cudaMemset2DAsync		hipMemset2DAsync	
cudaMemset3D		hipMemset3D	
cudaMemset3DAsync		hipMemset3DAsync	
cudaMemsetAsync		hipMemsetAsync	
make cudaExtent		make hipExtent	
make cudaPitchedPtr		make hipPitchedPtr	
make cudaPos		make hipPos	

1.1.9 Memory Management [Deprecated]

CUDA	Deprecated	HIP	Deprecated
cudaMemcpyFromArray	✓	hipMemcpyFromArray	✓
cudaMemcpyToArray	✓	hipMemcpyToArray	✓

1.1.10 Unified Addressing

CUDA	Deprecated	HIP	Deprecated
cudaPointerGetAttributes		hipPointerGetAttributes	

1.1.11 Peer Device Memory Access

CUDA	Deprecated	HIP	Deprecated
cudaDeviceCanAccessPeer		hipDeviceCanAccessPeer	
cudaDeviceDisablePeerAccess		hipDeviceDisablePeerAccess	
cudaDeviceEnablePeerAccess		hipDeviceEnablePeerAccess	

1.1.12 Texture Reference Management [Deprecated]

CUDA	Deprecated	HIP	Deprecated
cudaBindTexture	✓	hipBindTexture	✓
cudaBindTexture2D	✓	hipBindTexture2D	✓
cudaBindTextureToArray	✓	hipBindTextureToArray	✓
cudaBindTextureToMipmappedArray	✓	hipBindTextureToMipmappedArray	
cudaCreateChannelDesc		hipCreateChannelDesc	
cudaGetChannelDesc		hipGetChannelDesc	
cudaGetTextureAlignmentOffset	✓	hipGetTextureAlignmentOffset	✓

CUDA	Deprecated	HIP	Deprecated
cudaGetTextureReference	✓	hipGetTextureReference	
cudaUnbindTexture	✓	hipUnbindTexture	✓

1.1.13 Texture Object Management

CUDA	Deprecated	HIP	Deprecated
cuTexObjectGetTextureDesc		hipGetTextureObjectTextureDesc	
cudaCreateTextureObject		hipCreateTextureObject	
cudaDestroyTextureObject		hipDestroyTextureObject	
cudaGetTextureObjectResourceDesc		hipGetTextureObjectResourceDesc	
cudaGetTextureObjectResourceViewDesc		hipGetTextureObjectResourceViewDesc	

1.1.14 Surface Object Management

CUDA	Deprecated	HIP	Deprecated
cudaCreateSurfaceObject		hipCreateSurfaceObject	
cudaDestroySurfaceObject		hipDestroySurfaceObject	

1.1.15 Version Management

CUDA	Deprecated	HIP	Deprecated
cudaDriverGetVersion		hipDriverGetVersion	
cudaRuntimeGetVersion		hipRuntimeGetVersion	

1.1.16 Profiler Control

CUDA	Deprecated	HIP	Deprecated
cudaProfilerStart		hipProfilerStart	✓
cudaProfilerStop		hipProfilerStop	✓

1.1.17 Data types used by CUDA Runtime

CUDA	Deprecated	HIP	Deprecated
CUDA_IPC_HANDLE_SIZE		HIP_IPC_HANDLE_SIZE	
CUevent_st		ihipEvent_t	
CUstream_st		ihipStream_t	
cudaAddressModeBorder		hipAddressModeBorder	
cudaAddressModeClamp		hipAddressModeClamp	
cudaAddressModeMirror		hipAddressModeMirror	
cudaAddressModeWrap		hipAddressModeWrap	
cudaArray		hipArray	
cudaArrayCubemap		hipArrayCubemap	
cudaArrayDefault		hipArrayDefault	
cudaArrayLayered		hipArrayLayered	
cudaArraySurfaceLoadStore		hipArraySurfaceLoadStore	
cudaArrayTextureGather		hipArrayTextureGather	
cudaArray_const_t		hipArray_const_t	
cudaArray_t		hipArray_t	
cudaBoundaryModeClamp		hipBoundaryModeClamp	

CUDA	Deprecated	HIP	Deprecated
cudaBoundaryModeTrap		hipBoundaryModeTrap	
cudaBoundaryModeZero		hipBoundaryModeZero	
cudaChannelFormatDesc		hipChannelFormatDesc	
cudaChannelFormatKind		hipChannelFormatKind	
cudaChannelFormatKindFloat		hipChannelFormatKindFloat	
cudaChannelFormatKindNone		hipChannelFormatKindNone	
cudaChannelFormatKindSigned		hipChannelFormatKindSigned	
cudaChannelFormatKindUnsigned		hipChannelFormatKindUnsigned	
cudaComputeMode		hipComputeMode	
cudaComputeModeDefault		hipComputeModeDefault	
cudaComputeModeExclusive		hipComputeModeExclusive	
cudaComputeModeExclusiveProcess		hipComputeModeExclusiveProcess	
cudaComputeModeProhibited		hipComputeModeProhibited	
cudaCooperativeLaunchMultiDeviceNoPostSync		hipCooperativeLaunchMultiDeviceNoPostSync	
cudaCooperativeLaunchMultiDeviceNoPreSync		hipCooperativeLaunchMultiDeviceNoPreSync	
cudaCpuDeviceId		hipCpuDeviceId	
cudaDevAttrCanMapHostMemory		hipDeviceAttributeCanMapHostMemory	
cudaDevAttrClockRate		hipDeviceAttributeClockRate	
cudaDevAttrComputeCapabilityMajor		hipDeviceAttributeComputeCapabilityMajor	
cudaDevAttrComputeCapabilityMinor		hipDeviceAttributeComputeCapabilityMinor	
cudaDevAttrComputeMode		hipDeviceAttributeComputeMode	
cudaDevAttrConcurrentKernels		hipDeviceAttributeConcurrentKernels	
cudaDevAttrConcurrentManagedAccess		hipDeviceAttributeConcurrentManagedAccess	
cudaDevAttrCooperativeLaunch		hipDeviceAttributeCooperativeLaunch	
cudaDevAttrCooperativeMultiDeviceLaunch		hipDeviceAttributeCooperativeMultiDeviceLaunch	
cudaDevAttrDirectManagedMemAccessFromHost		hipDeviceAttributeDirectManagedMemAccessFromHost	
cudaDevAttrEccEnabled		hipDeviceAttributeEccEnabled	
cudaDevAttrGlobalMemoryBusWidth		hipDeviceAttributeMemoryBusWidth	
cudaDevAttrIntegrated		hipDeviceAttributeIntegrated	
cudaDevAttrIsMultiGpuBoard		hipDeviceAttributeIsMultiGpuBoard	
cudaDevAttrKernelExecTimeout		hipDeviceAttributeKernelExecTimeout	
cudaDevAttrL2CacheSize		hipDeviceAttributeL2CacheSize	
cudaDevAttrManagedMemory		hipDeviceAttributeManagedMemory	
cudaDevAttrMaxBlockDimX		hipDeviceAttributeMaxBlockDimX	
cudaDevAttrMaxBlockDimY		hipDeviceAttributeMaxBlockDimY	
cudaDevAttrMaxBlockDimZ		hipDeviceAttributeMaxBlockDimZ	
cudaDevAttrMaxGridDimX		hipDeviceAttributeMaxGridDimX	
cudaDevAttrMaxGridDimY		hipDeviceAttributeMaxGridDimY	
cudaDevAttrMaxGridDimZ		hipDeviceAttributeMaxGridDimZ	
cudaDevAttrMaxPitch		hipDeviceAttributeMaxPitch	
cudaDevAttrMaxRegistersPerBlock		hipDeviceAttributeMaxRegistersPerBlock	
cudaDevAttrMaxSharedMemoryPerBlock		hipDeviceAttributeMaxSharedMemoryPerBlock	

CUDA	Deprecated	HIP	Deprecated
cudaDevAttrMaxSharedMemoryPerMultiprocessor		hipDeviceAttributeMaxSharedMemoryPerMultiprocessor	
cudaDevAttrMaxTexture1DWidth		hipDeviceAttributeMaxTexture1DWidth	
cudaDevAttrMaxTexture2DHeight		hipDeviceAttributeMaxTexture2DHeight	
cudaDevAttrMaxTexture2DWidth		hipDeviceAttributeMaxTexture2DWidth	
cudaDevAttrMaxTexture3DDepth		hipDeviceAttributeMaxTexture3DDepth	
cudaDevAttrMaxTexture3DHeight		hipDeviceAttributeMaxTexture3DHeight	
cudaDevAttrMaxTexture3DWidth		hipDeviceAttributeMaxTexture3DWidth	
cudaDevAttrMaxThreadsPerBlock		hipDeviceAttributeMaxThreadsPerBlock	
cudaDevAttrMaxThreadsPerMultiProcessor		hipDeviceAttributeMaxThreadsPerMultiProcessor	
cudaDevAttrMemoryClockRate		hipDeviceAttributeMemoryClockRate	
cudaDevAttrMultiProcessorCount		hipDeviceAttributeMultiprocessorCount	
cudaDevAttrPageableMemoryAccess		hipDeviceAttributePageableMemoryAccess	
cudaDevAttrPageableMemoryAccessUsesHostPageTables		hipDeviceAttributePageableMemoryAccessUsesHostPageTables	
cudaDevAttrPciBusId		hipDeviceAttributePciBusId	
cudaDevAttrPciDeviceId		hipDeviceAttributePciDeviceId	
cudaDevAttrTextureAlignment		hipDeviceAttributeTextureAlignment	
cudaDevAttrTotalConstantMemory		hipDeviceAttributeTotalConstantMemory	
cudaDevAttrWarpSize		hipDeviceAttributeWarpSize	
cudaDevP2PAttrAccessSupported		hipDevP2PAttrAccessSupported	
cudaDevP2PAttrCudaArrayAccessSupported		hipDevP2PAttrHipArrayAccessSupported	
cudaDevP2PAttrNativeAtomicSupported		hipDevP2PAttrNativeAtomicSupported	
cudaDevP2PAttrPerformanceRank		hipDevP2PAttrPerformanceRank	
cudaDeviceAttr		hipDeviceAttribute_t	
cudaDeviceBlockingSync		hipDeviceScheduleBlockingSync	
cudaDeviceLmemResizeToMax		hipDeviceLmemResizeToMax	
cudaDeviceMapHost		hipDeviceMapHost	
cudaDeviceP2PAttr		hipDeviceP2PAttr	
cudaDeviceProp		hipDeviceProp_t	
cudaDeviceScheduleAuto		hipDeviceScheduleAuto	
cudaDeviceScheduleBlockingSync		hipDeviceScheduleBlockingSync	
cudaDeviceScheduleMask		hipDeviceScheduleMask	
cudaDeviceScheduleSpin		hipDeviceScheduleSpin	
cudaDeviceScheduleYield		hipDeviceScheduleYield	
cudaError		hipError_t	
cudaErrorAlreadyAcquired		hipErrorAlreadyAcquired	
cudaErrorAlreadyMapped		hipErrorAlreadyMapped	
cudaErrorArrayIsMapped		hipErrorArrayIsMapped	
cudaErrorAssert		hipErrorAssert	
cudaErrorCooperativeLaunchTooLarge		hipErrorCooperativeLaunchTooLarge	
cudaErrorCudartUnloading		hipErrorDeinitialized	
cudaErrorDeviceAlreadyInUse		hipErrorContextAlreadyInUse	
cudaErrorDeviceUninitialized		hipErrorInvalidContext	
cudaErrorDeviceUninitialized		hipErrorInvalidContext	
cudaErrorECCUncorrectable		hipErrorECCNotCorrectable	
cudaErrorFileNotFound		hipErrorFileNotFound	
cudaErrorHostMemoryAlreadyRegistered		hipErrorHostMemoryAlreadyRegistered	

CUDA	Deprecated	HIP	Deprecated
cudaErrorHostMemoryNotRegistered		hipErrorHostMemoryNotRegistered	
cudaErrorIllegalAddress		hipErrorIllegalAddress	
cudaErrorInitializationError		hipErrorNotInitialized	
cudaErrorInsufficientDriver		hipErrorInsufficientDriver	
cudaErrorInvalidConfiguration		hipErrorInvalidConfiguration	
cudaErrorInvalidDevice		hipErrorInvalidDevice	
cudaErrorInvalidDeviceFunction		hipErrorInvalidDeviceFunction	
cudaErrorInvalidDevicePointer	✓	hipErrorInvalidDevicePointer	
cudaErrorInvalidGraphicsContext		hipErrorInvalidGraphicsContext	
cudaErrorInvalidKernelImage		hipErrorInvalidImage	
cudaErrorInvalidMemcpyDirection		hipErrorInvalidMemcpyDirection	
cudaErrorInvalidPtx		hipErrorInvalidKernelFile	
cudaErrorInvalidResourceHandle		hipErrorInvalidHandle	
cudaErrorInvalidSource		hipErrorInvalidSource	
cudaErrorInvalidSymbol		hipErrorInvalidSymbol	
cudaErrorInvalidValue		hipErrorInvalidValue	
cudaErrorLaunchFailure		hipErrorLaunchFailure	
cudaErrorLaunchOutOfResources		hipErrorLaunchOutOfResources	
cudaErrorLaunchTimeout		hipErrorLaunchTimeOut	
cudaErrorMapBufferObjectFailed		hipErrorMapFailed	
cudaErrorMemoryAllocation		hipErrorOutOfMemory	
cudaErrorMissingConfiguration		hipErrorMissingConfiguration	
cudaErrorNoDevice		hipErrorNoDevice	
cudaErrorNoKernelImageForDevice		hipErrorNoBinaryForGpu	
cudaErrorNotMapped		hipErrorNotMapped	
cudaErrorNotMappedAsArray		hipErrorNotMappedAsArray	
cudaErrorNotMappedAsPointer		hipErrorNotMappedAsPointer	
cudaErrorNotReady		hipErrorNotReady	
cudaErrorNotSupported		hipErrorNotSupported	
cudaErrorOperatingSystem		hipErrorOperatingSystem	
cudaErrorPeerAccessAlreadyEnabled		hipErrorPeerAccessAlreadyEnabled	
cudaErrorPeerAccessNotEnabled		hipErrorPeerAccessNotEnabled	
cudaErrorPeerAccessUnsupported		hipErrorPeerAccessUnsupported	
cudaErrorPriorLaunchFailure	✓	hipErrorPriorLaunchFailure	
cudaErrorProfilerAlreadyStarted	✓	hipErrorProfilerAlreadyStarted	
cudaErrorProfilerAlreadyStopped	✓	hipErrorProfilerAlreadyStopped	
cudaErrorProfilerDisabled		hipErrorProfilerDisabled	
cudaErrorProfilerNotInitialized	✓	hipErrorProfilerNotInitialized	
cudaErrorSetOnActiveProcess		hipErrorSetOnActiveProcess	
cudaErrorSharedObjectInitFailed		hipErrorSharedObjectInitFailed	
cudaErrorSharedObjectSymbolNotFound		hipErrorSharedObjectSymbolNotFound	
cudaErrorSymbolNotFound		hipErrorNotFound	
cudaErrorUnknown		hipErrorUnknown	
cudaErrorUnmapBufferObjectFailed		hipErrorUnmapFailed	
cudaErrorUnsupportedLimit		hipErrorUnsupportedLimit	
cudaError_t		hipError_t	
cudaEventBlockingSync		hipEventBlockingSync	
cudaEventDefault		hipEventDefault	
cudaEventDisableTiming		hipEventDisableTiming	
cudaEventInterprocess		hipEventInterprocess	
cudaEvent_t		hipEvent_t	

CUDA	Deprecated	HIP	Deprecated
cudaExtent		hipExtent	
cudaFilterModeLinear		hipFilterModeLinear	
cudaFilterModePoint		hipFilterModePoint	
cudaFuncAttribute		hipFuncAttribute	
cudaFuncAttributeMax		hipFuncAttributeMax	
cudaFuncAttributeMaxDynamicSharedMemorySize		hipFuncAttributeMaxDynamicSharedMemorySize	
cudaFuncAttributePreferredSharedMemoryCarveout		hipFuncAttributePreferredSharedMemoryCarveout	
cudaFuncAttributes		hipFuncAttributes	
cudaFuncCache		hipFuncCache_t	
cudaFuncCachePreferEqual		hipFuncCachePreferEqual	
cudaFuncCachePreferL1		hipFuncCachePreferL1	
cudaFuncCachePreferNone		hipFuncCachePreferNone	
cudaFuncCachePreferShared		hipFuncCachePreferShared	
cudaHostAllocDefault		hipHostMallocDefault	
cudaHostAllocMapped		hipHostMallocMapped	
cudaHostAllocPortable		hipHostMallocPortable	
cudaHostAllocWriteCombined		hipHostMallocWriteCombined	
cudaHostRegisterDefault		hipHostRegisterDefault	
cudaHostRegisterIoMemory		hipHostRegisterIoMemory	
cudaHostRegisterMapped		hipHostRegisterMapped	
cudaHostRegisterPortable		hipHostRegisterPortable	
cudaInvalidDeviceId		hipInvalidDeviceId	
cudaIpcEventHandle_st		hipIpcEventHandle_st	
cudaIpcEventHandle_t		hipIpcEventHandle_t	
cudaIpcMemHandle_st		hipIpcMemHandle_st	
cudaIpcMemHandle_t		hipIpcMemHandle_t	
cudaIpcMemLazyEnablePeerAccess		hipIpcMemLazyEnablePeerAccess	
cudaLaunchParams		hipLaunchParams	
cudaLimit		hipLimit_t	
cudaLimitMallocHeapSize		hipLimitMallocHeapSize	
cudaMemAdviseSetAccessedBy		hipMemAdviseSetAccessedBy	
cudaMemAdviseSetPreferredLocation		hipMemAdviseSetPreferredLocation	
cudaMemAdviseSetReadMostly		hipMemAdviseSetReadMostly	
cudaMemAdviseUnsetAccessedBy		hipMemAdviseUnsetAccessedBy	
cudaMemAdviseUnsetPreferredLocation		hipMemAdviseUnsetPreferredLocation	
cudaMemAdviseUnsetReadMostly		hipMemAdviseUnsetReadMostly	
cudaMemAttachGlobal		hipMemAttachGlobal	
cudaMemAttachHost		hipMemAttachHost	
cudaMemAttachSingle		hipMemAttachSingle	
cudaMemRangeAttribute		hipMemRangeAttribute	
cudaMemRangeAttributeAccessedBy		hipMemRangeAttributeAccessedBy	
cudaMemRangeAttributeLastPrefetchLocation		hipMemRangeAttributeLastPrefetchLocation	
cudaMemRangeAttributePreferredLocation		hipMemRangeAttributePreferredLocation	
cudaMemRangeAttributeReadMostly		hipMemRangeAttributeReadMostly	
cudaMemcpy3DParms		hipMemcpy3DParms	
cudaMemcpyDefault		hipMemcpyDefault	
cudaMemcpyDeviceToDevice		hipMemcpyDeviceToDevice	
cudaMemcpyDeviceToHost		hipMemcpyDeviceToHost	
cudaMemcpyHostToDevice		hipMemcpyHostToDevice	

CUDA	Deprecated	HIP	Deprecated
cudaMemcpyHostToHost		hipMemcpyHostToHost	
cudaMemcpyKind		hipMemcpyKind	
cudaMemoryAdvise		hipMemoryAdvise	
cudaMipmappedArray		hipMipmappedArray	
cudaMipmappedArray const t		hipMipmappedArray const t	
cudaMipmappedArray t		hipMipmappedArray t	
cudaOccupancyDefault		hipOccupancyDefault	
cudaPitchedPtr		hipPitchedPtr	
cudaPointerAttributes		hipPointerAttribute t	
cudaPos		hipPos	
cudaReadModeElementType		hipReadModeElementType	
cudaReadModeNormalizedFloat		hipReadModeNormalizedFloat	
cudaResViewFormatFloat1		hipResViewFormatFloat1	
cudaResViewFormatFloat2		hipResViewFormatFloat2	
cudaResViewFormatFloat4		hipResViewFormatFloat4	
cudaResViewFormatHalf1		hipResViewFormatHalf1	
cudaResViewFormatHalf2		hipResViewFormatHalf2	
cudaResViewFormatHalf4		hipResViewFormatHalf4	
cudaResViewFormatNone		hipResViewFormatNone	
cudaResViewFormatSignedBlockCompressed4		hipResViewFormatSignedBlockCompressed4	
cudaResViewFormatSignedBlockCompressed5		hipResViewFormatSignedBlockCompressed5	
cudaResViewFormatSignedBlockCompressed6H		hipResViewFormatSignedBlockCompressed6H	
cudaResViewFormatSignedChar1		hipResViewFormatSignedChar1	
cudaResViewFormatSignedChar2		hipResViewFormatSignedChar2	
cudaResViewFormatSignedChar4		hipResViewFormatSignedChar4	
cudaResViewFormatSignedInt1		hipResViewFormatSignedInt1	
cudaResViewFormatSignedInt2		hipResViewFormatSignedInt2	
cudaResViewFormatSignedInt4		hipResViewFormatSignedInt4	
cudaResViewFormatSignedShort1		hipResViewFormatSignedShort1	
cudaResViewFormatSignedShort2		hipResViewFormatSignedShort2	
cudaResViewFormatSignedShort4		hipResViewFormatSignedShort4	
cudaResViewFormatUnsignedBlockCompressed1		hipResViewFormatUnsignedBlockCompressed1	
cudaResViewFormatUnsignedBlockCompressed2		hipResViewFormatUnsignedBlockCompressed2	
cudaResViewFormatUnsignedBlockCompressed3		hipResViewFormatUnsignedBlockCompressed3	
cudaResViewFormatUnsignedBlockCompressed4		hipResViewFormatUnsignedBlockCompressed4	
cudaResViewFormatUnsignedBlockCompressed5		hipResViewFormatUnsignedBlockCompressed5	
cudaResViewFormatUnsignedBlockCompressed6H		hipResViewFormatUnsignedBlockCompressed6H	
cudaResViewFormatUnsignedBlockCompressed7		hipResViewFormatUnsignedBlockCompressed7	
cudaResViewFormatUnsignedChar1		hipResViewFormatUnsignedChar1	
cudaResViewFormatUnsignedChar2		hipResViewFormatUnsignedChar2	
cudaResViewFormatUnsignedChar4		hipResViewFormatUnsignedChar4	
cudaResViewFormatUnsignedInt1		hipResViewFormatUnsignedInt1	

CUDA	Deprecated	HIP	Deprecated
cudaResViewFormatUnsignedInt2		hipResViewFormatUnsignedInt2	
cudaResViewFormatUnsignedInt4		hipResViewFormatUnsignedInt4	
cudaResViewFormatUnsignedShort1		hipResViewFormatUnsignedShort1	
cudaResViewFormatUnsignedShort2		hipResViewFormatUnsignedShort2	
cudaResViewFormatUnsignedShort4		hipResViewFormatUnsignedShort4	
cudaResourceDesc		hipResourceDesc	
cudaResourceType		hipResourceType	
cudaResourceTypeArray		hipResourceTypeArray	
cudaResourceTypeLinear		hipResourceTypeLinear	
cudaResourceTypeMipmappedArray		hipResourceTypeMipmappedArray	
cudaResourceTypePitch2D		hipResourceTypePitch2D	
cudaResourceViewDesc		hipResourceViewDesc	
cudaResourceViewFormat		hipResourceViewFormat	
cudaSharedMemBankSizeDefault		hipSharedMemBankSizeDefault	
cudaSharedMemBankSizeEightByte		hipSharedMemBankSizeEightByte	
cudaSharedMemBankSizeFourByte		hipSharedMemBankSizeFourByte	
cudaSharedMemConfig		hipSharedMemConfig	
cudaStreamCallback_t		hipStreamCallback_t	
cudaStreamDefault		hipStreamDefault	
cudaStreamNonBlocking		hipStreamNonBlocking	
cudaStream_t		hipStream_t	
cudaSuccess		hipSuccess	
cudaSurfaceBoundaryMode		hipSurfaceBoundaryMode	
cudaSurfaceObject_t		hipSurfaceObject_t	
cudaTextureAddressMode		hipTextureAddressMode	
cudaTextureDesc		hipTextureDesc	
cudaTextureFilterMode		hipTextureFilterMode	
cudaTextureObject_t		hipTextureObject_t	
cudaTextureReadMode		hipTextureReadMode	
cudaTextureType1D		hipTextureType1D	
cudaTextureType1DLayered		hipTextureType1DLayered	
cudaTextureType2D		hipTextureType2D	
cudaTextureType2DLayered		hipTextureType2DLayered	
cudaTextureType3D		hipTextureType3D	
cudaTextureTypeCubemap		hipTextureTypeCubemap	
cudaTextureTypeCubemapLayered		hipTextureTypeCubemapLayered	
surfaceReference		surfaceReference	

1.1.18 Execution Control [Removed]

CUDA	Deprecated	HIP	Deprecated
cudaConfigureCall		hipConfigureCall	
cudaLaunch		hipLaunchByPtr	
cudaSetupArgument		hipSetupArgument	

1.1.19 Unsupported APIs

API	Unsupported	Deprecated
Graph Management	✓	
C++ API Routines	✓	
Interactions with the CUDA Driver API	✓	
Profiler Control	✓	✓
Surface Reference Management	✓	✓
OpenGL Interoperability	✓	
External Resource Interoperability	✓	
OpenGL Interoperability	✓	✓
Direct3D 9 Interoperability	✓	
Direct3D 9 Interoperability	✓	✓
Direct3D 10 Interoperability	✓	
Direct3D 10 Interoperability	✓	✓
Direct3D 11 Interoperability	✓	
Direct3D 11 Interoperability	✓	✓
VDPAU Interoperability	✓	
EGL Interoperability	✓	
Graphics Interoperability	✓	
Stream Ordered Memory Allocator	✓	

1.2 CUDA Driver API Supported by HIP

1.2.1 CUDA Driver Data Types

CUDA	Deprecated	HIP	Deprecated
CUDA_ARRAY3D_CUBEMAP		hipArrayCubemap	
CUDA_ARRAY3D_DESCRIPTOR		HIP_ARRAY3D_DESCRIPTOR	
CUDA_ARRAY3D_DESCRIPTOR_st		HIP_ARRAY3D_DESCRIPTOR	
CUDA_ARRAY3D_LAYERED		hipArrayLayered	
CUDA_ARRAY3D_SURFACE_LDST		hipArraySurfaceLoadStore	
CUDA_ARRAY3D_TEXTURE_GATHER		hipArrayTextureGather	
CUDA_ARRAY_DESCRIPTOR		HIP_ARRAY_DESCRIPTOR	
CUDA_ARRAY_DESCRIPTOR_st		HIP_ARRAY_DESCRIPTOR	
CUDA_ARRAY_DESCRIPTOR_v1		HIP_ARRAY_DESCRIPTOR	
CUDA_ARRAY_DESCRIPTOR_v1_st		HIP_ARRAY_DESCRIPTOR	
CUDA_COOPERATIVE_LAUNCH_MULTI_DEVICE_NO_POST_LAUNCH_SYNC		hipCooperativeLaunchMultiDeviceNoPostSync	
CUDA_COOPERATIVE_LAUNCH_MULTI_DEVICE_NO_PRE_LAUNCH_SYNC		hipCooperativeLaunchMultiDeviceNoPreSync	
CUDA_ERROR_ALREADY_ACQUIRED		hipErrorAlreadyAcquired	
CUDA_ERROR_ALREADY_MAPPED		hipErrorAlreadyMapped	
CUDA_ERROR_ARRAY_IS_MAPPED		hipErrorArrayIsMapped	
CUDA_ERROR_ASSERT		hipErrorAssert	

CUDA	Deprecated	HIP	Deprecated
CUDA_ERROR_CONTEXT_ALREADY_CURRENT	✓	hipErrorContextAlreadyCurrent	
CUDA_ERROR_CONTEXT_ALREADY_IN_USE		hipErrorContextAlreadyInUse	
CUDA_ERROR_COOPERATIVE_LAUNCH_TOO_LARGE		hipErrorCooperativeLaunchTooLarge	
CUDA_ERROR_DEINITIALIZED		hipErrorDeinitialized	
CUDA_ERROR_ECC_UNCORRECTABLE		hipErrorECCNotCorrectable	
CUDA_ERROR_FILE_NOT_FOUND		hipErrorFileNotFound	
CUDA_ERROR_HOST_MEMORY_ALREADY_REGISTERED		hipErrorHostMemoryAlreadyRegistered	
CUDA_ERROR_HOST_MEMORY_NOT_REGISTERED		hipErrorHostMemoryNotRegistered	
CUDA_ERROR_ILLEGAL_ADDRESS		hipErrorIllegalAddress	
CUDA_ERROR_INVALID_CONTEXT		hipErrorInvalidContext	
CUDA_ERROR_INVALID_DEVICE		hipErrorInvalidDevice	
CUDA_ERROR_INVALID_GRAPHICS_CONTEXT		hipErrorInvalidGraphicsContext	
CUDA_ERROR_INVALID_HANDLE		hipErrorInvalidHandle	
CUDA_ERROR_INVALID_IMAGE		hipErrorInvalidImage	
CUDA_ERROR_INVALID_PTX		hipErrorInvalidKernelFile	
CUDA_ERROR_INVALID_SOURCE		hipErrorInvalidSource	
CUDA_ERROR_INVALID_VALUE		hipErrorInvalidValue	
CUDA_ERROR_LAUNCH_FAILED		hipErrorLaunchFailure	
CUDA_ERROR_LAUNCH_OUT_OF_RESOURCES		hipErrorLaunchOutOfResources	
CUDA_ERROR_LAUNCH_TIMEOUT		hipErrorLaunchTimeOut	
CUDA_ERROR_MAP_FAILED		hipErrorMapFailed	
CUDA_ERROR_NOT_FOUND		hipErrorNotFound	
CUDA_ERROR_NOT_INITIALIZED		hipErrorNotInitialized	
CUDA_ERROR_NOT_MAPPED		hipErrorNotMapped	
CUDA_ERROR_NOT_MAPPED_AS_ARRAY		hipErrorNotMappedAsArray	
CUDA_ERROR_NOT_MAPPED_AS_POINTER		hipErrorNotMappedAsPointer	
CUDA_ERROR_NOT_READY		hipErrorNotReady	
CUDA_ERROR_NOT_SUPPORTED		hipErrorNotSupported	
CUDA_ERROR_NO_BINARY_FOR_GPU		hipErrorNoBinaryForGpu	
CUDA_ERROR_NO_DEVICE		hipErrorNoDevice	
CUDA_ERROR_OPERATING_SYSTEM		hipErrorOperatingSystem	
CUDA_ERROR_OUT_OF_MEMORY		hipErrorOutOfMemory	
CUDA_ERROR_PEER_ACCESS_ALREADY_ENABLED		hipErrorPeerAccessAlreadyEnabled	
CUDA_ERROR_PEER_ACCESS_NOT_ENABLED		hipErrorPeerAccessNotEnabled	
CUDA_ERROR_PEER_ACCESS_UNSUPPORTED		hipErrorPeerAccessUnsupported	
CUDA_ERROR_PRIMARY_CONTEXT_ACTIVE		hipErrorSetOnActiveProcess	

CUDA	Deprecated	HIP	Deprecated
CUDA_ERROR_PROFILER_ALREADY_STARTED	✓	hipErrorProfilerAlreadyStarted	
CUDA_ERROR_PROFILER_ALREADY_STOPPED	✓	hipErrorProfilerAlreadyStopped	
CUDA_ERROR_PROFILER_DISABLED		hipErrorProfilerDisabled	
CUDA_ERROR_PROFILER_NOT_INITIALIZED	✓	hipErrorProfilerNotInitialized	
CUDA_ERROR_SHARED_OBJECT_INIT_FAILED		hipErrorSharedObjectInitFailed	
CUDA_ERROR_SHARED_OBJECT_SYMBOL_NOT_FOUND		hipErrorSharedObjectSymbolNotFound	
CUDA_ERROR_UNKNOWN		hipErrorUnknown	
CUDA_ERROR_UNMAP_FAILED		hipErrorUnmapFailed	
CUDA_ERROR_UNSUPPORTED_LIMIT		hipErrorUnsupportedLimit	
CUDA_LAUNCH_PARAMS		hipLaunchParams	
CUDA_LAUNCH_PARAMS_st		hipLaunchParams	
CUDA_MEMCPY2D		hip_Memcpy2D	
CUDA_MEMCPY2D_st		hip_Memcpy2D	
CUDA_MEMCPY2D_v1		hip_Memcpy2D	
CUDA_MEMCPY2D_v1_st		hip_Memcpy2D	
CUDA_MEMCPY3D		HIP_MEMCPY3D	
CUDA_MEMCPY3D_st		HIP_MEMCPY3D	
CUDA_MEMCPY3D_v1		HIP_MEMCPY3D	
CUDA_MEMCPY3D_v1_st		HIP_MEMCPY3D	
CUDA_RESOURCE_DESC		HIP_RESOURCE_DESC	
CUDA_RESOURCE_DESC_st		HIP_RESOURCE_DESC_st	
CUDA_RESOURCE_VIEW_DESC		HIP_RESOURCE_VIEW_DESC	
CUDA_RESOURCE_VIEW_DESC_st		HIP_RESOURCE_VIEW_DESC_st	
CUDA_SUCCESS		hipSuccess	
CUDA_TEXTURE_DESC		HIP_TEXTURE_DESC	
CUDA_TEXTURE_DESC_st		HIP_TEXTURE_DESC_st	
CU_AD_FORMAT_FLOAT		HIP_AD_FORMAT_FLOAT	
CU_AD_FORMAT_HALF		HIP_AD_FORMAT_HALF	
CU_AD_FORMAT_SIGNED_INT16		HIP_AD_FORMAT_SIGNED_INT16	
CU_AD_FORMAT_SIGNED_INT32		HIP_AD_FORMAT_SIGNED_INT32	
CU_AD_FORMAT_SIGNED_INT8		HIP_AD_FORMAT_SIGNED_INT8	
CU_AD_FORMAT_UNSIGNED_INT16		HIP_AD_FORMAT_UNSIGNED_INT16	
CU_AD_FORMAT_UNSIGNED_INT32		HIP_AD_FORMAT_UNSIGNED_INT32	
CU_AD_FORMAT_UNSIGNED_INT8		HIP_AD_FORMAT_UNSIGNED_INT8	
CU_COMPUTEMODE_DEFAULT		hipComputeModeDefault	
CU_COMPUTEMODE_EXCLUSIVE		hipComputeModeExclusive	
CU_COMPUTEMODE_EXCLUSIVE_PROCESS		hipComputeModeExclusiveProcess	
CU_COMPUTEMODE_PROHIBITED		hipComputeModeProhibited	
CU_CTX_BLOCKING_SYNC	✓	hipDeviceScheduleBlockingSync	
CU_CTX_LMEM_RESIZE_TO_MAX		hipDeviceLmemResizeToMax	
CU_CTX_MAP_HOST		hipDeviceMapHost	
CU_CTX_SCHED_AUTO		hipDeviceScheduleAuto	

CUDA	Deprecated	HIP	Deprecated
CU_CTX_SCHED_BLOCKING_SYNC		hipDeviceScheduleBlockingSync	
CU_CTX_SCHED_MASK		hipDeviceScheduleMask	
CU_CTX_SCHED_SPIN		hipDeviceScheduleSpin	
CU_CTX_SCHED_YIELD		hipDeviceScheduleYield	
CU_DEVICE_ATTRIBUTE_CAN_MAP_HOST_MEMORY		hipDeviceAttributeCanMapHostMemory	
CU_DEVICE_ATTRIBUTE_CLOCK_RATE		hipDeviceAttributeClockRate	
CU_DEVICE_ATTRIBUTE_COMPUTE_CAPABILITY_MAJOR		hipDeviceAttributeComputeCapabilityMajor	
CU_DEVICE_ATTRIBUTE_COMPUTE_CAPABILITY_MINOR		hipDeviceAttributeComputeCapabilityMinor	
CU_DEVICE_ATTRIBUTE_COMPUTE_MODE		hipDeviceAttributeComputeMode	
CU_DEVICE_ATTRIBUTE_CONCURRENT_KERNELS		hipDeviceAttributeConcurrentKernels	
CU_DEVICE_ATTRIBUTE_CONCURRENT_MANAGED_ACCESS		hipDeviceAttributeConcurrentManagedAccess	
CU_DEVICE_ATTRIBUTE_COOPERATIVE_LAUNCH		hipDeviceAttributeCooperativeLaunch	
CU_DEVICE_ATTRIBUTE_COOPERATIVE_MULTI_DEVICE_LAUNCH		hipDeviceAttributeCooperativeMultiDeviceLaunch	
CU_DEVICE_ATTRIBUTE_DIRECT_MANAGED_MEM_ACCESS_FROM_HOST		hipDeviceAttributeDirectManagedMemAccessFromHost	
CU_DEVICE_ATTRIBUTE_ECC_ENABLED		hipDeviceAttributeEccEnabled	
CU_DEVICE_ATTRIBUTE_GLOBAL_MEMORY_BUS_WIDTH		hipDeviceAttributeMemoryBusWidth	
CU_DEVICE_ATTRIBUTE_INTEGRATED		hipDeviceAttributeIntegrated	
CU_DEVICE_ATTRIBUTE_KERNEL_EXEC_TIMEOUT		hipDeviceAttributeKernelExecTimeout	
CU_DEVICE_ATTRIBUTE_L2_CACHE_SIZE		hipDeviceAttributeL2CacheSize	
CU_DEVICE_ATTRIBUTE_MANAGED_MEMORY		hipDeviceAttributeManagedMemory	
CU_DEVICE_ATTRIBUTE_MAXIMUM_TEXTURE1D_WIDTH		hipDeviceAttributeMaxTexture1DWidth	
CU_DEVICE_ATTRIBUTE_MAXIMUM_TEXTURE2D_HEIGHT		hipDeviceAttributeMaxTexture2DHeight	
CU_DEVICE_ATTRIBUTE_MAXIMUM_TEXTURE2D_WIDTH		hipDeviceAttributeMaxTexture2DWidth	
CU_DEVICE_ATTRIBUTE_MAXIMUM_TEXTURE3D_DEPTH		hipDeviceAttributeMaxTexture3DDepth	
CU_DEVICE_ATTRIBUTE_MAXIMUM_TEXTURE3D_HEIGHT		hipDeviceAttributeMaxTexture3DHeight	
CU_DEVICE_ATTRIBUTE_MAXIMUM_TEXTURE3D_WIDTH		hipDeviceAttributeMaxTexture3DWidth	
CU_DEVICE_ATTRIBUTE_MAX_BLOCK_DIM_X		hipDeviceAttributeMaxBlockDimX	
CU_DEVICE_ATTRIBUTE_MAX_BLOCK_DIM_Y		hipDeviceAttributeMaxBlockDimY	

CUDA	Deprecated	HIP	Deprecated
CU_DEVICE_ATTRIBUTE_MAX_BLOCK_DIM_Z		hipDeviceAttributeMaxBlockDimZ	
CU_DEVICE_ATTRIBUTE_MAX_GRID_DIM_X		hipDeviceAttributeMaxGridDimX	
CU_DEVICE_ATTRIBUTE_MAX_GRID_DIM_Y		hipDeviceAttributeMaxGridDimY	
CU_DEVICE_ATTRIBUTE_MAX_GRID_DIM_Z		hipDeviceAttributeMaxGridDimZ	
CU_DEVICE_ATTRIBUTE_MAX_PITCH		hipDeviceAttributeMaxPitch	
CU_DEVICE_ATTRIBUTE_MAX_REGISTERS_PER_BLOCK		hipDeviceAttributeMaxRegistersPerBlock	
CU_DEVICE_ATTRIBUTE_MAX_SHARED_MEMORY_PER_BLOCK		hipDeviceAttributeMaxSharedMemoryPerBlock	
CU_DEVICE_ATTRIBUTE_MAX_SHARED_MEMORY_PER_MULTIPROCESSOR		hipDeviceAttributeMaxSharedMemoryPerMultiprocessor	
CU_DEVICE_ATTRIBUTE_MAX_THREADS_PER_BLOCK		hipDeviceAttributeMaxThreadsPerBlock	
CU_DEVICE_ATTRIBUTE_MAX_THREADS_PER_MULTIPROCESSOR		hipDeviceAttributeMaxThreadsPerMultiprocessor	
CU_DEVICE_ATTRIBUTE_MEMORY_CLOCK_RATE		hipDeviceAttributeMemoryClockRate	
CU_DEVICE_ATTRIBUTE_MULTIPROCESSOR_COUNT		hipDeviceAttributeMultiprocessorCount	
CU_DEVICE_ATTRIBUTE_MULTIGPU_BOARD		hipDeviceAttributeIsMultiGpuBoard	
CU_DEVICE_ATTRIBUTE_PAGEABLE_MEMORY_ACCESS		hipDeviceAttributePageableMemoryAccess	
CU_DEVICE_ATTRIBUTE_PAGEABLE_MEMORY_ACCESS_USES_HOST_PAGE_TABLES		hipDeviceAttributePageableMemoryAccessUsesHostPageTables	
CU_DEVICE_ATTRIBUTE_PCI_BUS_ID		hipDeviceAttributePciBusId	
CU_DEVICE_ATTRIBUTE_PCI_DEVICE_ID		hipDeviceAttributePciDeviceId	
CU_DEVICE_ATTRIBUTE_REGISTERS_PER_BLOCK	✓	hipDeviceAttributeMaxRegistersPerBlock	
CU_DEVICE_ATTRIBUTE_SHARED_MEMORY_PER_BLOCK	✓	hipDeviceAttributeMaxSharedMemoryPerBlock	
CU_DEVICE_ATTRIBUTE_TEXTURE_ALIGNMENT		hipDeviceAttributeTextureAlignment	
CU_DEVICE_ATTRIBUTE_TOTAL_CONSTANT_MEMORY		hipDeviceAttributeTotalConstantMemory	
CU_DEVICE_ATTRIBUTE_WARP_SIZE		hipDeviceAttributeWarpSize	
CU_DEVICE_CPU		hipCpuDeviceId	
CU_DEVICE_INVALID		hipInvalidDeviceId	
CU_DEVICE_P2P_ATTRIBUTE_ACCESS_ACCESS_SUPPORTED	✓	hipDevP2PAttrHipArrayAccessSupported	
CU_DEVICE_P2P_ATTRIBUTE_ACCESS_SUPPORTED		hipDevP2PAttrAccessSupported	

CUDA	Deprecated	HIP	Deprecated
CU_DEVICE_P2P_ATTRIBUTE_ARRAY_ACCESS_SUPPORTED	✓	hipDevP2PAttrHipArrayAccessSupported	
CU_DEVICE_P2P_ATTRIBUTE_CUDA_ARRAY_ACCESS_SUPPORTED		hipDevP2PAttrHipArrayAccessSupported	
CU_DEVICE_P2P_ATTRIBUTE_NATIVE_ATOMIC_SUPPORTED		hipDevP2PAttrNativeAtomicSupported	
CU_DEVICE_P2P_ATTRIBUTE_PERFORMANCE_RANK		hipDevP2PAttrPerformanceRank	
CU_EVENT_BLOCKING_SYNC		hipEventBlockingSync	
CU_EVENT_DEFAULT		hipEventDefault	
CU_EVENT_DISABLE_TIMING		hipEventDisableTiming	
CU_EVENT_INTERPROCESS		hipEventInterprocess	
CU_FUNC_ATTRIBUTE_BINARY_VERSION		HIP_FUNC_ATTRIBUTE_BINARY_VERSION	
CU_FUNC_ATTRIBUTE_CACHE_MODE_CA		HIP_FUNC_ATTRIBUTE_CACHE_MODE_CA	
CU_FUNC_ATTRIBUTE_CONST_SIZE_BYTES		HIP_FUNC_ATTRIBUTE_CONST_SIZE_BYTES	
CU_FUNC_ATTRIBUTE_LOCAL_SIZE_BYTES		HIP_FUNC_ATTRIBUTE_LOCAL_SIZE_BYTES	
CU_FUNC_ATTRIBUTE_MAX		HIP_FUNC_ATTRIBUTE_MAX	
CU_FUNC_ATTRIBUTE_MAX_DYNAMIC_SHARED_SIZE_BYTES		HIP_FUNC_ATTRIBUTE_MAX_DYNAMIC_SHARED_SIZE_BYTES	
CU_FUNC_ATTRIBUTE_MAX_THREADS_PER_BLOCK		HIP_FUNC_ATTRIBUTE_MAX_THREADS_PER_BLOCK	
CU_FUNC_ATTRIBUTE_NUM_REGS		HIP_FUNC_ATTRIBUTE_NUM_REGS	
CU_FUNC_ATTRIBUTE_PREFERRED_SHARED_MEMORY_CARVEOUT		HIP_FUNC_ATTRIBUTE_PREFERRED_SHARED_MEMORY_CARVEOUT	
CU_FUNC_ATTRIBUTE_PTX_VERSION		HIP_FUNC_ATTRIBUTE_PTX_VERSION	
CU_FUNC_ATTRIBUTE_SHARED_SIZE_BYTES		HIP_FUNC_ATTRIBUTE_SHARED_SIZE_BYTES	
CU_FUNC_CACHE_PREFER_EQUAL		hipFuncCachePreferEqual	
CU_FUNC_CACHE_PREFER_L1		hipFuncCachePreferL1	
CU_FUNC_CACHE_PREFER_NONE		hipFuncCachePreferNone	
CU_FUNC_CACHE_PREFER_SHARED		hipFuncCachePreferShared	
CU_IPC_HANDLE_SIZE		HIP_IPC_HANDLE_SIZE	
CU_IPC_MEM_LAZY_ENABLE_PEER_ACCESS		hipIpcMemLazyEnablePeerAccess	
CU_JIT_CACHE_MODE		hipJitOptionCacheMode	
CU_JIT_ERROR_LOG_BUFFER		hipJitOptionErrorLogBuffer	
CU_JIT_ERROR_LOG_BUFFER_SIZE_BYTES		hipJitOptionErrorLogBufferSizeBytes	
CU_JIT_FALLBACK_STRATEGY		hipJitOptionFallbackStrategy	
CU_JIT_FAST_COMPILE		hipJitOptionFastCompile	
CU_JIT_GENERATE_DEBUG_INFO		hipJitOptionGenerateDebugInfo	
CU_JIT_GENERATE_LINE_INFO		hipJitOptionGenerateLineInfo	
CU_JIT_INFO_LOG_BUFFER		hipJitOptionInfoLogBuffer	
CU_JIT_INFO_LOG_BUFFER_SIZE_BYTES		hipJitOptionInfoLogBufferSizeBytes	
CU_JIT_LOG_VERBOSE		hipJitOptionLogVerbose	
CU_JIT_MAX_REGISTERS		hipJitOptionMaxRegisters	

CUDA	Deprecated	HIP	Deprecated
CU_JIT_NEW_SM3X_OPT		hipJitOptionSm3xOpt	
CU_JIT_NUM_OPTIONS		hipJitOptionNumOptions	
CU_JIT_OPTIMIZATION_LEVEL		hipJitOptionOptimizationLevel	
CU_JIT_TARGET		hipJitOptionTarget	
CU_JIT_TARGET_FROM_CUCONTEXT		hipJitOptionTargetFromContext	
CU_JIT_THREADS_PER_BLOCK		hipJitOptionThreadsPerBlock	
CU_JIT_WALL_TIME		hipJitOptionWallTime	
CU_LAUNCH_PARAM_BUFFER_POINTER		HIP_LAUNCH_PARAM_BUFFER_POINTER	
CU_LAUNCH_PARAM_BUFFER_SIZE		HIP_LAUNCH_PARAM_BUFFER_SIZE	
CU_LAUNCH_PARAM_END		HIP_LAUNCH_PARAM_END	
CU_LIMIT_MALLOC_HEAP_SIZE		hipLimitMallocHeapSize	
CU_MEMHOSTALLOC_DEVICEMAP		hipHostMallocMapped	
CU_MEMHOSTALLOC_PORTABLE		hipHostMallocPortable	
CU_MEMHOSTALLOC_WRITECOMBINED		hipHostMallocWriteCombined	
CU_MEMHOSTREGISTER_DEVICEMAP		hipHostRegisterMapped	
CU_MEMHOSTREGISTER_IOMEMORY		hipHostRegisterIoMemory	
CU_MEMHOSTREGISTER_PORTABLE		hipHostRegisterPortable	
CU_MEMORYTYPE_ARRAY		hipMemoryTypeArray	
CU_MEMORYTYPE_DEVICE		hipMemoryTypeDevice	
CU_MEMORYTYPE_HOST		hipMemoryTypeHost	
CU_MEMORYTYPE_UNIFIED		hipMemoryTypeUnified	
CU_MEM_ADVICE_SET_ACCESSED_BY		hipMemAdviseSetAccessedBy	
CU_MEM_ADVICE_SET_PREFERRED_LOCATION		hipMemAdviseSetPreferredLocation	
CU_MEM_ADVICE_SET_READ_MOSTLY		hipMemAdviseSetReadMostly	
CU_MEM_ADVICE_UNSET_ACCESSED_BY		hipMemAdviseUnsetAccessedBy	
CU_MEM_ADVICE_UNSET_PREFERRED_LOCATION		hipMemAdviseUnsetPreferredLocation	
CU_MEM_ADVICE_UNSET_READ_MOSTLY		hipMemAdviseUnsetReadMostly	
CU_MEM_ATTACH_GLOBAL		hipMemAttachGlobal	
CU_MEM_ATTACH_HOST		hipMemAttachHost	
CU_MEM_ATTACH_SINGLE		hipMemAttachSingle	
CU_MEM_RANGE_ATTRIBUTE_ACCESSED_BY		hipMemRangeAttributeAccessedBy	
CU_MEM_RANGE_ATTRIBUTE_LAST_PREFETCH_LOCATION		hipMemRangeAttributeLastPrefetchLocation	
CU_MEM_RANGE_ATTRIBUTE_PREFERRED_LOCATION		hipMemRangeAttributePreferredLocation	
CU_MEM_RANGE_ATTRIBUTE_READ_MOSTLY		hipMemRangeAttributeReadMostly	
CU_OCCUPANCY_DEFAULT		hipOccupancyDefault	

CUDA	Deprecated	HIP	Deprecated
CU_RESOURCE_TYPE_ARRAY		HIP_RESOURCE_TYPE_ARRAY	
CU_RESOURCE_TYPE_LINEAR		HIP_RESOURCE_TYPE_LINEAR	
CU_RESOURCE_TYPE_MIPMAPPED_ARRAY		HIP_RESOURCE_TYPE_MIPMAPPED_ARRAY	
CU_RESOURCE_TYPE_PITCH2D		HIP_RESOURCE_TYPE_PITCH2D	
CU_RES_VIEW_FORMAT_FLOAT_1X16		HIP_RES_VIEW_FORMAT_FLOAT_1X16	
CU_RES_VIEW_FORMAT_FLOAT_1X32		HIP_RES_VIEW_FORMAT_FLOAT_1X32	
CU_RES_VIEW_FORMAT_FLOAT_2X16		HIP_RES_VIEW_FORMAT_FLOAT_2X16	
CU_RES_VIEW_FORMAT_FLOAT_2X32		HIP_RES_VIEW_FORMAT_FLOAT_2X32	
CU_RES_VIEW_FORMAT_FLOAT_4X16		HIP_RES_VIEW_FORMAT_FLOAT_4X16	
CU_RES_VIEW_FORMAT_FLOAT_4X32		HIP_RES_VIEW_FORMAT_FLOAT_4X32	
CU_RES_VIEW_FORMAT_NONE		HIP_RES_VIEW_FORMAT_NONE	
CU_RES_VIEW_FORMAT_SIGNED_BC4		HIP_RES_VIEW_FORMAT_SIGNED_BC4	
CU_RES_VIEW_FORMAT_SIGNED_BC5		HIP_RES_VIEW_FORMAT_SIGNED_BC5	
CU_RES_VIEW_FORMAT_SIGNED_BC6H		HIP_RES_VIEW_FORMAT_SIGNED_BC6H	
CU_RES_VIEW_FORMAT_SINT_1X16		HIP_RES_VIEW_FORMAT_SINT_1X16	
CU_RES_VIEW_FORMAT_SINT_1X32		HIP_RES_VIEW_FORMAT_SINT_1X32	
CU_RES_VIEW_FORMAT_SINT_1X8		HIP_RES_VIEW_FORMAT_SINT_1X8	
CU_RES_VIEW_FORMAT_SINT_2X16		HIP_RES_VIEW_FORMAT_SINT_2X16	
CU_RES_VIEW_FORMAT_SINT_2X32		HIP_RES_VIEW_FORMAT_SINT_2X32	
CU_RES_VIEW_FORMAT_SINT_2X8		HIP_RES_VIEW_FORMAT_SINT_2X8	
CU_RES_VIEW_FORMAT_SINT_4X16		HIP_RES_VIEW_FORMAT_SINT_4X16	
CU_RES_VIEW_FORMAT_SINT_4X32		HIP_RES_VIEW_FORMAT_SINT_4X32	
CU_RES_VIEW_FORMAT_SINT_4X8		HIP_RES_VIEW_FORMAT_SINT_4X8	
CU_RES_VIEW_FORMAT_UINT_1X16		HIP_RES_VIEW_FORMAT_UINT_1X16	
CU_RES_VIEW_FORMAT_UINT_1X32		HIP_RES_VIEW_FORMAT_UINT_1X32	
CU_RES_VIEW_FORMAT_UINT_1X8		HIP_RES_VIEW_FORMAT_UINT_1X8	
CU_RES_VIEW_FORMAT_UINT_2X16		HIP_RES_VIEW_FORMAT_UINT_2X16	
CU_RES_VIEW_FORMAT_UINT_2X32		HIP_RES_VIEW_FORMAT_UINT_2X32	
CU_RES_VIEW_FORMAT_UINT_2X8		HIP_RES_VIEW_FORMAT_UINT_2X8	
CU_RES_VIEW_FORMAT_UINT_4X16		HIP_RES_VIEW_FORMAT_UINT_4X16	

CUDA	Deprecated	HIP	Deprecated
CU_RES_VIEW_FORMAT_UINT_4X32		HIP_RES_VIEW_FORMAT_UINT_4X32	
CU_RES_VIEW_FORMAT_UINT_4X8		HIP_RES_VIEW_FORMAT_UINT_4X8	
CU_RES_VIEW_FORMAT_UNSIGNED_BC1		HIP_RES_VIEW_FORMAT_UNSIGNED_BC1	
CU_RES_VIEW_FORMAT_UNSIGNED_BC2		HIP_RES_VIEW_FORMAT_UNSIGNED_BC2	
CU_RES_VIEW_FORMAT_UNSIGNED_BC3		HIP_RES_VIEW_FORMAT_UNSIGNED_BC3	
CU_RES_VIEW_FORMAT_UNSIGNED_BC4		HIP_RES_VIEW_FORMAT_UNSIGNED_BC4	
CU_RES_VIEW_FORMAT_UNSIGNED_BC5		HIP_RES_VIEW_FORMAT_UNSIGNED_BC5	
CU_RES_VIEW_FORMAT_UNSIGNED_BC6H		HIP_RES_VIEW_FORMAT_UNSIGNED_BC6H	
CU_RES_VIEW_FORMAT_UNSIGNED_BC7		HIP_RES_VIEW_FORMAT_UNSIGNED_BC7	
CU_SHARED_MEM_CONFIG_DEFAULT_BANK_SIZE		hipSharedMemBankSizeDefault	
CU_SHARED_MEM_CONFIG_EIGHT_BYTE_BANK_SIZE		hipSharedMemBankSizeEightByte	
CU_SHARED_MEM_CONFIG_FOUR_BYTE_BANK_SIZE		hipSharedMemBankSizeFourByte	
CU_STREAM_DEFAULT		hipStreamDefault	
CU_STREAM_NON_BLOCKING		hipStreamNonBlocking	
CU_STREAM_WAIT_VALUE_AND		hipStreamWaitValueAnd	
CU_STREAM_WAIT_VALUE_EQ		hipStreamWaitValueEq	
CU_STREAM_WAIT_VALUE_GEQ		hipStreamWaitValueGte	
CU_STREAM_WAIT_VALUE_NOR		hipStreamWaitValueNor	
CU_TRSA_OVERRIDE_FORMAT		HIP_TRSA_OVERRIDE_FORMAT	
CU_TRSF_NORMALIZED_COORDINATES		HIP_TRSF_NORMALIZED_COORDINATES	
CU_TRSF_READ_AS_INTEGER		HIP_TRSF_READ_AS_INTEGER	
CU_TRSF_SRGB		HIP_TRSF_SRGB	
CU_TR_ADDRESS_MODE_BORDER		HIP_TR_ADDRESS_MODE_BORDER	
CU_TR_ADDRESS_MODE_CLAMP		HIP_TR_ADDRESS_MODE_CLAMP	
CU_TR_ADDRESS_MODE_MIRROR		HIP_TR_ADDRESS_MODE_MIRROR	
CU_TR_ADDRESS_MODE_WRAP		HIP_TR_ADDRESS_MODE_WRAP	
CU_TR_FILTER_MODE_LINEAR		HIP_TR_FILTER_MODE_LINEAR	
CU_TR_FILTER_MODE_POINT		HIP_TR_FILTER_MODE_POINT	
CUaddress_mode		HIPaddress_mode	
CUaddress_mode_enum		HIPaddress_mode_enum	
CUarray		hipArray*	
CUarray_format		hipArray_Format	
CUarray_format_enum		hipArray_Format	
CUarray_st		hipArray	
CUcomputemode		hipComputeMode	

CUDA	Deprecated	HIP	Deprecated
CUcomputemode_enum		hipComputeMode	
CUcontext		hipCtx_t	
CUctx_st		ihipCtx_t	
CUdevice		hipDevice_t	
CUdevice_P2PAttribute		hipDeviceP2PAttr	
CUdevice_P2PAttribute_enum		hipDeviceP2PAttr	
CUdevice_attribute		hipDeviceAttribute_t	
CUdevice_attribute_enum		hipDeviceAttribute_t	
CUdeviceptr		hipDeviceptr_t	
CUdeviceptr_v1		hipDeviceptr_t	
CUevent		hipEvent_t	
CUevent_st		ihipEvent_t	
CUfilter_mode		HIPfilter_mode	
CUfilter_mode_enum		HIPfilter_mode_enum	
CUfunc_cache		hipFuncCache_t	
CUfunc_cache_enum		hipFuncCache_t	
CUfunc_st		ihipModuleSymbol_t	
CUfunction		hipFunction_t	
CUfunction_attribute		hipFunction_attribute	
CUfunction_attribute_enum		hipFunction_attribute	
CUipcEventHandle		hipIpcEventHandle_t	
CUipcEventHandle_st		hipIpcEventHandle_st	
CUipcMemHandle		hipIpcMemHandle_t	
CUipcMemHandle_st		hipIpcMemHandle_st	
CUjit_option		hipJitOption	
CUjit_option_enum		hipJitOption	
CULimit		hipLimit_t	
CULimit_enum		hipLimit_t	
CUmем_advise		hipMemoryAdvise	
CUmем_advise_enum		hipMemoryAdvise	
CUmем_range_attribute		hipMemRangeAttribute	
CUmем_range_attribute_enum		hipMemRangeAttribute	
CUmemorytype		hipMemoryType	
CUmemorytype_enum		hipMemoryType	
CUmipmappedArray		hipMipmappedArray_t	
CUmipmappedArray_st		hipMipmappedArray	
CUmod_st		ihipModule_t	
CUmodule		hipModule_t	
CUresourceViewFormat		HIPresourceViewFormat	
CUresourceViewFormat_enum		HIPresourceViewFormat_enum	
CUresourcetype		HIPresourcetype	
CUresourcetype_enum		HIPresourcetype_enum	
CUresult		hipError_t	
CUsharedconfig		hipSharedMemConfig	
CUsharedconfig_enum		hipSharedMemConfig	
CUstream		hipStream_t	
CUstreamCallback		hipStreamCallback_t	
CUstream_st		ihipStream_t	
CUtexObject		hipTextureObject_t	
CUtexref_st		textureReference	
CUDACC		HIPCC	
cudaError_enum		hipError_t	

1.2.2 Initialization

CUDA	Deprecated	HIP	Deprecated
cuInit		hipInit	

1.2.3 Version Management

CUDA	Deprecated	HIP	Deprecated
cuDriverGetVersion		hipDriverGetVersion	

1.2.4 Device Management

CUDA	Deprecated	HIP	Deprecated
cuDeviceGet		hipDeviceGet	
cuDeviceGetAttribute		hipDeviceGetAttribute	
cuDeviceGetCount		hipGetDeviceCount	
cuDeviceGetName		hipDeviceGetName	
cuDeviceTotalMem		hipDeviceTotalMem	
cuDeviceTotalMem_v2		hipDeviceTotalMem	

1.2.5 Device Management [Deprecated]

CUDA	Deprecated	HIP	Deprecated
cuDeviceComputeCapability	✓	hipDeviceComputeCapability	

1.2.6 Primary Context Management

CUDA	Deprecated	HIP	Deprecated
cuDevicePrimaryCtxGetState		hipDevicePrimaryCtxGetState	
cuDevicePrimaryCtxRelease		hipDevicePrimaryCtxRelease	
cuDevicePrimaryCtxRelease_v2		hipDevicePrimaryCtxRelease	
cuDevicePrimaryCtxReset		hipDevicePrimaryCtxReset	
cuDevicePrimaryCtxReset_v2		hipDevicePrimaryCtxReset	
cuDevicePrimaryCtxRetain		hipDevicePrimaryCtxRetain	
cuDevicePrimaryCtxSetFlags		hipDevicePrimaryCtxSetFlags	
cuDevicePrimaryCtxSetFlags_v2		hipDevicePrimaryCtxSetFlags	

1.2.7 Context Management

CUDA	Deprecated	HIP	Deprecated
cuCtxCreate		hipCtxCreate	✓
cuCtxCreate_v2		hipCtxCreate	✓
cuCtxDestroy		hipCtxDestroy	✓
cuCtxDestroy_v2		hipCtxDestroy	✓
cuCtxGetApiVersion		hipCtxGetApiVersion	✓
cuCtxGetCacheConfig		hipCtxGetCacheConfig	✓
cuCtxGetCurrent		hipCtxGetCurrent	✓
cuCtxGetDevice		hipCtxGetDevice	✓
cuCtxGetFlags		hipCtxGetFlags	✓

CUDA	Deprecated	HIP	Deprecated
cuCtxGetLimit		hipDeviceGetLimit	
cuCtxGetSharedMemConfig		hipCtxGetSharedMemConfig	✓
cuCtxGetStreamPriorityRange		hipDeviceGetStreamPriorityRange	
cuCtxPopCurrent		hipCtxPopCurrent	✓
cuCtxPopCurrent_v2		hipCtxPopCurrent	✓
cuCtxPushCurrent		hipCtxPushCurrent	✓
cuCtxPushCurrent_v2		hipCtxPushCurrent	✓
cuCtxSetCacheConfig		hipCtxSetCacheConfig	✓
cuCtxSetCurrent		hipCtxSetCurrent	✓
cuCtxSetSharedMemConfig		hipCtxSetSharedMemConfig	✓
cuCtxSynchronize		hipCtxSynchronize	✓

1.2.8 Module Management

CUDA	Deprecated	HIP	Deprecated
cuModuleGetFunction		hipModuleGetFunction	
cuModuleGetGlobal		hipModuleGetGlobal	
cuModuleGetGlobal_v2		hipModuleGetGlobal	
cuModuleGetTexRef		hipModuleGetTexRef	
cuModuleLoad		hipModuleLoad	
cuModuleLoadData		hipModuleLoadData	
cuModuleLoadDataEx		hipModuleLoadDataEx	
cuModuleUnload		hipModuleUnload	

1.2.9 Memory Management

CUDA	Deprecated	HIP	Deprecated
cuArray3DCreate		hipArray3DCreate	
cuArray3DCreate_v2		hipArray3DCreate	
cuArrayCreate		hipArrayCreate	
cuArrayCreate_v2		hipArrayCreate	
cuDeviceGetByPCIBusId		hipDeviceGetByPCIBusId	
cuDeviceGetPCIBusId		hipDeviceGetPCIBusId	
cuIpcCloseMemHandle		hipIpcCloseMemHandle	
cuIpcGetEventHandle		hipIpcGetEventHandle	
cuIpcGetMemHandle		hipIpcGetMemHandle	
cuIpcOpenEventHandle		hipIpcOpenEventHandle	
cuIpcOpenMemHandle		hipIpcOpenMemHandle	
cuMemAlloc		hipMalloc	
cuMemAllocHost		hipHostMalloc	
cuMemAllocHost_v2		hipHostMalloc	
cuMemAllocManaged		hipMallocManaged	
cuMemAllocPitch		hipMemAllocPitch	
cuMemAllocPitch_v2		hipMemAllocPitch	
cuMemAlloc_v2		hipMalloc	
cuMemFree		hipFree	
cuMemFreeHost		hipHostFree	
cuMemFree_v2		hipFree	
cuMemGetAddressRange		hipMemGetAddressRange	

CUDA	Deprecated	HIP	Deprecated
cuMemGetAddressRange v2		hipMemGetAddressRange	
cuMemGetInfo		hipMemGetInfo	
cuMemGetInfo v2		hipMemGetInfo	
cuMemHostAlloc		hipHostMalloc	
cuMemHostGetDevicePointer		hipHostGetDevicePointer	
cuMemHostGetDevicePointer v2		hipHostGetDevicePointer	
cuMemHostGetFlags		hipHostGetFlags	
cuMemHostRegister		hipHostRegister	
cuMemHostRegister v2		hipHostRegister	
cuMemHostUnregister		hipHostUnregister	
cuMemcpy2D		hipMemcpyParam2D	
cuMemcpy2DAsync		hipMemcpyParam2DAsync	
cuMemcpy2DAsync v2		hipMemcpyParam2DAsync	
cuMemcpy2D v2		hipMemcpyParam2D	
cuMemcpy3D		hipDrvMemcpy3D	
cuMemcpy3DAsync		hipDrvMemcpy3DAsync	
cuMemcpy3DAsync v2		hipDrvMemcpy3DAsync	
cuMemcpy3D v2		hipDrvMemcpy3D	
cuMemcpyAtoH		hipMemcpyAtoH	
cuMemcpyAtoH v2		hipMemcpyAtoH	
cuMemcpyDtoD		hipMemcpyDtoD	
cuMemcpyDtoDAsync		hipMemcpyDtoDAsync	
cuMemcpyDtoDAsync v2		hipMemcpyDtoDAsync	
cuMemcpyDtoD v2		hipMemcpyDtoD	
cuMemcpyDtoH		hipMemcpyDtoH	
cuMemcpyDtoHAsync		hipMemcpyDtoHAsync	
cuMemcpyDtoHAsync v2		hipMemcpyDtoHAsync	
cuMemcpyDtoH v2		hipMemcpyDtoH	
cuMemcpyHtoA		hipMemcpyHtoA	
cuMemcpyHtoA v2		hipMemcpyHtoA	
cuMemcpyHtoD		hipMemcpyHtoD	
cuMemcpyHtoDAsync		hipMemcpyHtoDAsync	
cuMemcpyHtoDAsync v2		hipMemcpyHtoDAsync	
cuMemcpyHtoD v2		hipMemcpyHtoD	
cuMemsetD16		hipMemsetD16	
cuMemsetD16Async		hipMemsetD16Async	
cuMemsetD16_v2		hipMemsetD16	
cuMemsetD32		hipMemsetD32	
cuMemsetD32Async		hipMemsetD32Async	
cuMemsetD32_v2		hipMemsetD32	
cuMemsetD8		hipMemsetD8	
cuMemsetD8Async		hipMemsetD8Async	
cuMemsetD8_v2		hipMemsetD8	
cuMipmappedArrayCreate		hipMipmappedArrayCreate	
cuMipmappedArrayDestroy		hipMipmappedArrayDestroy	
cuMipmappedArrayGetLevel		hipMipmappedArrayGetLevel	

1.2.10 Unified Addressing

CUDA	Deprecated	HIP	Deprecated
cuMemAdvise		hipMemAdvise	
cuMemRangeGetAttribute		hipMemRangeGetAttribute	
cuMemRangeGetAttributes		hipMemRangeGetAttributes	

1.2.11 Stream Management

CUDA	Deprecated	HIP	Deprecated
cuStreamAddCallback		hipStreamAddCallback	
cuStreamAttachMemAsync		hipStreamAttachMemAsync	
cuStreamCreate		hipStreamCreateWithFlags	
cuStreamCreateWithPriority		hipStreamCreateWithPriority	
cuStreamDestroy		hipStreamDestroy	
cuStreamDestroy_v2		hipStreamDestroy	
cuStreamGetFlags		hipStreamGetFlags	
cuStreamGetPriority		hipStreamGetPriority	
cuStreamQuery		hipStreamQuery	
cuStreamSynchronize		hipStreamSynchronize	
cuStreamWaitEvent		hipStreamWaitEvent	

1.2.12 Event Management

CUDA	Deprecated	HIP	Deprecated
cuEventCreate		hipEventCreateWithFlags	
cuEventDestroy		hipEventDestroy	
cuEventDestroy_v2		hipEventDestroy	
cuEventElapsedTime		hipEventElapsedTime	
cuEventQuery		hipEventQuery	
cuEventRecord		hipEventRecord	
cuEventSynchronize		hipEventSynchronize	

1.2.13 Stream Memory Operations

CUDA	Deprecated	HIP	Deprecated
cuStreamWaitValue32		hipStreamWaitValue32*	
cuStreamWaitValue64		hipStreamWaitValue64*	
cuStreamWriteValue32		hipStreamWriteValue32*	
cuStreamWriteValue64		hipStreamWriteValue64*	

* Possible data loss in 3 argument

1.2.14 Execution Control

CUDA	Deprecated	HIP	Deprecated
cuFuncGetAttribute		hipFuncGetAttribute	
cuLaunchKernel		hipModuleLaunchKernel	

1.2.15 Occupancy

CUDA	Deprecated	HIP	Deprecated
cuOccupancyMaxActiveBlocksPerMulti processor		hipModuleOccupancyMaxActiveBlocksP erMultiprocessor	
cuOccupancyMaxActiveBlocksPerMulti processorWithFlags		hipModuleOccupancyMaxActiveBlocksP erMultiprocessorWithFlags	
cuOccupancyMaxPotentialBlockSize		hipModuleOccupancyMaxPotentialBlock Size	
cuOccupancyMaxPotentialBlockSizeWit hFlags		hipModuleOccupancyMaxPotentialBlock SizeWithFlags	

1.2.16 Texture Reference Management [Deprecated]

CUDA	Deprecated	HIP	Deprecated
cuTexRefGetAddress	✓	hipTexRefGetAddress	
cuTexRefGetAddressMode	✓	hipTexRefGetAddressMode	
cuTexRefGetAddress_v2	✓	hipTexRefGetAddress	
cuTexRefGetArray	✓	hipTexRefGetArray	
cuTexRefGetFilterMode	✓	hipTexRefGetFilterMode	
cuTexRefGetFlags	✓	hipTexRefGetFlags	
cuTexRefGetFormat	✓	hipTexRefGetFormat	
cuTexRefGetMaxAnisotropy	✓	hipTexRefGetMaxAnisotropy	
cuTexRefGetMipmapFilterMode	✓	hipTexRefGetMipmapFilterMode	
cuTexRefGetMipmapLevelBias	✓	hipTexRefGetMipmapLevelBias	
cuTexRefGetMipmapLevelClamp	✓	hipTexRefGetMipmapLevelClamp	
cuTexRefGetMipmappedArray	✓	hipTexRefGetMipMappedArray	
cuTexRefSetAddress	✓	hipTexRefSetAddress	
cuTexRefSetAddress2D	✓	hipTexRefSetAddress2D	
cuTexRefSetAddress2D_v2		hipTexRefSetAddress2D	
cuTexRefSetAddress2D_v3		hipTexRefSetAddress2D	
cuTexRefSetAddressMode	✓	hipTexRefSetAddressMode	
cuTexRefSetAddress_v2	✓	hipTexRefSetAddress	
cuTexRefSetArray	✓	hipTexRefSetArray	
cuTexRefSetBorderColor	✓	hipTexRefSetBorderColor	
cuTexRefSetFilterMode	✓	hipTexRefSetFilterMode	
cuTexRefSetFlags	✓	hipTexRefSetFlags	
cuTexRefSetFormat	✓	hipTexRefSetFormat	
cuTexRefSetMaxAnisotropy	✓	hipTexRefSetMaxAnisotropy	
cuTexRefSetMipmapFilterMode	✓	hipTexRefSetMipmapFilterMode	
cuTexRefSetMipmapLevelBias	✓	hipTexRefSetMipmapLevelBias	
cuTexRefSetMipmapLevelClamp	✓	hipTexRefSetMipmapLevelClamp	
cuTexRefSetMipmappedArray	✓	hipTexRefSetMipmappedArray	

1.2.17 Texture Object Management

CUDA	Deprecated	HIP	Deprecated
cuTexObjectCreate		hipTexObjectCreate	
cuTexObjectDestroy		hipTexObjectDestroy	
cuTexObjectGetResourceDesc		hipTexObjectGetResourceDesc	
cuTexObjectGetResourceViewDesc		hipTexObjectGetResourceViewDesc	
cuTexObjectGetTextureDesc		hipTexObjectGetTextureDesc	

1.2.18 Peer Context Memory Access

CUDA	Deprecated	HIP	Deprecated
cuCtxDisablePeerAccess		hipCtxDisablePeerAccess	✓
cuCtxEnablePeerAccess		hipCtxEnablePeerAccess	✓
cuDeviceCanAccessPeer		hipDeviceCanAccessPeer	
cuDeviceGetP2PAttribute		hipDeviceGetP2PAttribute	

1.2.19 Profiler Control

CUDA	Deprecated	HIP	Deprecated
cuProfilerStart		hipProfilerStart	✓
cuProfilerStop		hipProfilerStop	✓

1.2.20 Unsupported APIs

API	Unsupported	Deprecated
Error Handling	✓	
Context Management	✓	✓
Virtual Memory Management	✓	
Stream Ordered Memory Allocator	✓	
External Resource Interoperability	✓	
Stream Memory Operations	✓	
Execution Control	✓	✓
Graph Management	✓	
Surface Reference Management	✓	✓
Surface Object Management	✓	
Graphics Interoperability	✓	
Profiler Control	✓	✓
OpenGL Interoperability	✓	
VDPAU Interoperability	✓	
EGL Interoperability	✓	
Direct3D 9 Interoperability	✓	
Direct3D 10 Interoperability	✓	
Direct3D 11 Interoperability	✓	

1.3 CUCOMPLEX API Supported by HIP

1.3.1 cuComplex Data Types

CUDA	Deprecated	HIP	Deprecated
cuComplex		hipComplex	
cuDoubleComplex		hipDoubleComplex	
cuFloatComplex		hipFloatComplex	

1.3.2 cuComplex API Functions

CUDA	Deprecated	HIP	Deprecated
cuCabs		hipCabs	
cuCabsf		hipCabsf	
cuCadd		hipCadd	
cuCaddf		hipCaddf	
cuCdiv		hipCdiv	
cuCdivf		hipCdivf	
cuCfma		hipCfma	
cuCfmaf		hipCfmaf	
cuCimag		hipCimag	
cuCimagf		hipCimagf	
cuCmul		hipCmul	
cuCmulf		hipCmulf	
cuComplexDoubleToFloat		hipComplexDoubleToFloat	
cuComplexFloatToDouble		hipComplexFloatToDouble	
cuConj		hipConj	
cuConjf		hipConjf	
cuCreal		hipCreal	
cuCrealf		hipCrealf	
cuCsub		hipCsub	
cuCsubf		hipCsubf	
make cuComplex		make hipComplex	
make cuDoubleComplex		make hipDoubleComplex	
make cuFloatComplex		make hipFloatComplex	

1.4 CUDA DEVICE API Supported by HIP

1.4.1 Device Functions

CUDA	Deprecated	HIP	Deprecated
all		all	
any		any	
assert fail		assert fail	
assertfail		assertfail	
ballot		ballot	
brev		brev	
brevll		brevll	
byte perm		byte perm	
clz		clz	

CUDA	Deprecated	HIP	Deprecated
clzll		clzll	
cosf		cosf	
double2float_rd		double2float_rd	
double2float_rn		double2float_rn	
double2float_ru		double2float_ru	
double2float_rz		double2float_rz	
double2hiint		double2hiint	
double2int_rd		double2int_rd	
double2int_rn		double2int_rn	
double2int_ru		double2int_ru	
double2int_rz		double2int_rz	
double2ll_rd		double2ll_rd	
double2ll_rn		double2ll_rn	
double2ll_ru		double2ll_ru	
double2ll_rz		double2ll_rz	
double2loint		double2loint	
double2uint_rd		double2uint_rd	
double2uint_rn		double2uint_rn	
double2uint_ru		double2uint_ru	
double2uint_rz		double2uint_rz	
double2ull_rd		double2ull_rd	
double2ull_rn		double2ull_rn	
double2ull_ru		double2ull_ru	
double2ull_rz		double2ull_rz	
double_as_longlong		double_as_longlong	
exp10f		exp10f	
expf		expf	
fadd_rd		fadd_rd	
fadd_rn		fadd_rn	
fadd_ru		fadd_ru	
fadd_rz		fadd_rz	
fdiv_rd		fdiv_rd	
fdiv_rn		fdiv_rn	
fdiv_ru		fdiv_ru	
fdiv_rz		fdiv_rz	
fdividef		fdividef	
ffs		ffs	
ffsll		ffsll	
float22half2_rn		float22half2_rn	
float2half		float2half	
float2half2_rn		float2half2_rn	
float2half_rd		float2half_rd	
float2half_rn		float2half_rn	
float2half_ru		float2half_ru	
float2half_rz		float2half_rz	
float2int_rd		float2int_rd	
float2int_rn		float2int_rn	
float2int_ru		float2int_ru	
float2int_rz		float2int_rz	
float2ll_rd		float2ll_rd	
float2ll_rn		float2ll_rn	
float2ll_ru		float2ll_ru	

CUDA	Deprecated	HIP	Deprecated
float2ll rz		float2ll rz	
float2uint rd		float2uint rd	
float2uint rn		float2uint rn	
float2uint ru		float2uint ru	
float2uint rz		float2uint rz	
float2ull rd		float2ull rd	
float2ull rn		float2ull rn	
float2ull ru		float2ull ru	
float2ull rz		float2ull rz	
float as int		float as int	
float as uint		float as uint	
floats2half2 rn		floats2half2 rn	
fmaf rd		fmaf rd	
fmaf rn		fmaf rn	
fmaf ru		fmaf ru	
fmaf rz		fmaf rz	
fmul rd		fmul rd	
fmul rn		fmul rn	
fmul ru		fmul ru	
fmul rz		fmul rz	
frcp rd		frcp rd	
frcp rn		frcp rn	
frcp ru		frcp ru	
frcp rz		frcp rz	
frsqrtn rn		frsqrtn rn	
fsqrt rd		fsqrt rd	
fsqrt rn		fsqrt rn	
fsqrt ru		fsqrt ru	
fsqrt rz		fsqrt rz	
fsub rd		fsub rd	
fsub rn		fsub rn	
fsub ru		fsub ru	
fsub rz		fsub rz	
h2div		h2div	
habs		habs	
habs2		habs2	
hadd		hadd	
hadd2		hadd2	
hadd2_sat		hadd2_sat	
hadd_sat		hadd_sat	
half22float2		half22float2	
half2float		half2float	
half2half2		half2half2	
half2int rd		half2int rd	
half2int rn		half2int rn	
half2int ru		half2int ru	
half2int rz		half2int rz	
half2ll rd		half2ll rd	
half2ll rn		half2ll rn	
half2ll ru		half2ll ru	
half2ll rz		half2ll rz	
half2short_rd		half2short_rd	

CUDA	Deprecated	HIP	Deprecated
half2short_rn		half2short_rn	
half2short_ru		half2short_ru	
half2short_rz		half2short_rz	
half2uint_rd		half2uint_rd	
half2uint_rn		half2uint_rn	
half2uint_ru		half2uint_ru	
half2uint_rz		half2uint_rz	
half2ull_rd		half2ull_rd	
half2ull_rn		half2ull_rn	
half2ull_ru		half2ull_ru	
half2ull_rz		half2ull_rz	
half2ushort_rd		half2ushort_rd	
half2ushort_rn		half2ushort_rn	
half2ushort_ru		half2ushort_ru	
half2ushort_rz		half2ushort_rz	
half_as_short		half_as_short	
half_as_ushort		half_as_ushort	
halves2half2		halves2half2	
hbeq2		hbeq2	
hbequ2		hbequ2	
hbge2		hbge2	
hbgeu2		hbgeu2	
hbgt2		hbgt2	
hbgtu2		hbgtu2	
hble2		hble2	
hbleu2		hbleu2	
hblt2		hblt2	
hbltu2		hbltu2	
hbne2		hbne2	
hbneu2		hbneu2	
hdiv		hdiv	
heq		heq	
heq2		heq2	
hequ		hequ	
hequ2		hequ2	
hfma		hfma	
hfma2		hfma2	
hfma2_sat		hfma2_sat	
hfma_sat		hfma_sat	
hge		hge	
hge2		hge2	
hgeu		hgeu	
hgeu2		hgeu2	
hgt		hgt	
hgt2		hgt2	
hgtu		hgtu	
hgtu2		hgtu2	
high2float		high2float	
high2half		high2half	
high2half2		high2half2	
highs2half2		highs2half2	
hiloint2double		hiloint2double	

CUDA	Deprecated	HIP	Deprecated
hisinf		hisinf	
hisnan		hisnan	
hisnan2		hisnan2	
hle		hle	
hle2		hle2	
hleu		hleu	
hleu2		hleu2	
hlt		hlt	
hlt2		hlt2	
hltu		hltu	
hltu2		hltu2	
hmul		hmul	
hmul2		hmul2	
hmul2 sat		hmul2 sat	
hmul sat		hmul sat	
hne		hne	
hne2		hne2	
hneg		hneg	
hneg2		hneg2	
hneu		hneu	
hneu2		hneu2	
hsub		hsub	
hsub2		hsub2	
hsub2 sat		hsub2 sat	
hsub sat		hsub sat	
int2double rn		int2double rn	
int2float rd		int2float rd	
int2float rn		int2float rn	
int2float ru		int2float ru	
int2float rz		int2float rz	
int2half rd		int2half rd	
int2half rn		int2half rn	
int2half ru		int2half ru	
int2half rz		int2half rz	
int as float		int as float	
ldca		ldca	
ldcg		ldcg	
ldcs		ldcs	
ldg		ldg	
ll2double rd		ll2double rd	
ll2double rn		ll2double rn	
ll2double ru		ll2double ru	
ll2double rz		ll2double rz	
ll2float rd		ll2float rd	
ll2float rn		ll2float rn	
ll2float ru		ll2float ru	
ll2float rz		ll2float rz	
ll2half rd		ll2half rd	
ll2half rn		ll2half rn	
ll2half ru		ll2half ru	
ll2half rz		ll2half rz	
log10f		log10f	

CUDA	Deprecated	HIP	Deprecated
<code>__log2f</code>		<code>__log2f</code>	
<code>__logf</code>		<code>__logf</code>	
<code>__longlong_as_double</code>		<code>__longlong_as_double</code>	
<code>__low2float</code>		<code>__low2float</code>	
<code>__low2half</code>		<code>__low2half</code>	
<code>__low2half2</code>		<code>__low2half2</code>	
<code>__lowhigh2highlow</code>		<code>__lowhigh2highlow</code>	
<code>__lows2half2</code>		<code>__lows2half2</code>	
<code>__mul24</code>		<code>__mul24</code>	
<code>__mul64hi</code>		<code>__mul64hi</code>	
<code>__mulhi</code>		<code>__mulhi</code>	
<code>__popc</code>		<code>__popc</code>	
<code>__popcll</code>		<code>__popcll</code>	
<code>__powf</code>		<code>__powf</code>	
<code>__rhadd</code>		<code>__rhadd</code>	
<code>__sad</code>		<code>__sad</code>	
<code>__saturatef</code>		<code>__saturatef</code>	
<code>__shfl</code>	✓	<code>__shfl</code>	
<code>__shfl_down</code>	✓	<code>__shfl_down</code>	
<code>__shfl_up</code>	✓	<code>__shfl_up</code>	
<code>__shfl_xor</code>	✓	<code>__shfl_xor</code>	
<code>__short2half_rd</code>		<code>__short2half_rd</code>	
<code>__short2half_rn</code>		<code>__short2half_rn</code>	
<code>__short2half_ru</code>		<code>__short2half_ru</code>	
<code>__short2half_rz</code>		<code>__short2half_rz</code>	
<code>__short_as_half</code>		<code>__short_as_half</code>	
<code>__sincosf</code>		<code>__sincosf</code>	
<code>__sinf</code>		<code>__sinf</code>	
<code>__syncthreads</code>		<code>__syncthreads</code>	
<code>__syncthreads_and</code>		<code>__syncthreads_and</code>	
<code>__syncthreads_count</code>		<code>__syncthreads_count</code>	
<code>__syncthreads_or</code>		<code>__syncthreads_or</code>	
<code>__tanf</code>		<code>__tanf</code>	
<code>__threadfence</code>		<code>__threadfence</code>	
<code>__threadfence_block</code>		<code>__threadfence_block</code>	
<code>__threadfence_system</code>		<code>__threadfence_system</code>	
<code>__uhadd</code>		<code>__uhadd</code>	
<code>__uint2double_rn</code>		<code>__uint2double_rn</code>	
<code>__uint2float_rd</code>		<code>__uint2float_rd</code>	
<code>__uint2float_rn</code>		<code>__uint2float_rn</code>	
<code>__uint2float_ru</code>		<code>__uint2float_ru</code>	
<code>__uint2float_rz</code>		<code>__uint2float_rz</code>	
<code>__uint2half_rd</code>		<code>__uint2half_rd</code>	
<code>__uint2half_rn</code>		<code>__uint2half_rn</code>	
<code>__uint2half_ru</code>		<code>__uint2half_ru</code>	
<code>__uint2half_rz</code>		<code>__uint2half_rz</code>	
<code>__uint_as_float</code>		<code>__uint_as_float</code>	
<code>__ull2double_rd</code>		<code>__ull2double_rd</code>	
<code>__ull2double_rn</code>		<code>__ull2double_rn</code>	
<code>__ull2double_ru</code>		<code>__ull2double_ru</code>	
<code>__ull2double_rz</code>		<code>__ull2double_rz</code>	
<code>__ull2float_rd</code>		<code>__ull2float_rd</code>	

CUDA	Deprecated	HIP	Deprecated
ull2float rn		ull2float rn	
ull2float ru		ull2float ru	
ull2float rz		ull2float rz	
ull2half rd		ull2half rd	
ull2half rn		ull2half rn	
ull2half ru		ull2half ru	
ull2half rz		ull2half rz	
umul24		umul24	
umul64hi		umul64hi	
umulhi		umulhi	
urhadd		urhadd	
usad		usad	
ushort2half rd		ushort2half rd	
ushort2half rn		ushort2half rn	
ushort2half ru		ushort2half ru	
ushort2half rz		ushort2half rz	
ushort as half		ushort as half	
abs		abs	
acos		acos	
acosf		acosf	
acosh		acosh	
acoshf		acoshf	
asin		asin	
asinf		asinf	
asinh		asinh	
asinhf		asinhf	
atan		atan	
atan2		atan2	
atan2f		atan2f	
atanf		atanf	
atanh		atanh	
atanhf		atanhf	
atomicAdd		atomicAdd	
atomicAnd		atomicAnd	
atomicCAS		atomicCAS	
atomicDec		atomicDec	
atomicExch		atomicExch	
atomicInc		atomicInc	
atomicMax		atomicMax	
atomicMin		atomicMin	
atomicOr		atomicOr	
atomicSub		atomicSub	
atomicXor		atomicXor	
cbirt		cbirt	
cbirtf		cbirtf	
ceil		ceil	
ceilf		ceilf	
clock		clock	
clock64		clock64	
copysign		copysign	
copysignf		copysignf	
cos		cos	

CUDA	Deprecated	HIP	Deprecated
cosf		cosf	
cosh		cosh	
coshf		coshf	
cospi		cospi	
cospif		cospif	
cyl_bessel_i0		cyl_bessel_i0	
cyl_bessel_i0f		cyl_bessel_i0f	
cyl_bessel_i1		cyl_bessel_i1	
cyl_bessel_i1f		cyl_bessel_i1f	
erf		erf	
erfc		erfc	
erfcf		erfcf	
erfcinv		erfcinv	
erfcinvf		erfcinvf	
erfcx		erfcx	
erfcxf		erfcxf	
erff		erff	
erfinv		erfinv	
erfinvf		erfinvf	
exp		exp	
exp10		exp10	
exp10f		exp10f	
exp2		exp2	
exp2f		exp2f	
expf		expf	
expm1		expm1	
expm1f		expm1f	
fabs		fabs	
fabsf		fabsf	
fdim		fdim	
fdimf		fdimf	
fdividef		fdividef	
floor		floor	
floorf		floorf	
fma		fma	
fmaf		fmaf	
fmax		fmax	
fmaxf		fmaxf	
fmin		fmin	
fminf		fminf	
fmod		fmod	
fmodf		fmodf	
frexp		frexp	
frexpf		frexpf	
h2ceil		h2ceil	
h2cos		h2cos	
h2exp		h2exp	
h2exp10		h2exp10	
h2exp2		h2exp2	
h2floor		h2floor	
h2log		h2log	
h2log10		h2log10	

CUDA	Deprecated	HIP	Deprecated
h2log2		h2log2	
h2rcp		h2rcp	
h2rint		h2rint	
h2rsqrt		h2rsqrt	
h2sin		h2sin	
h2sqrt		h2sqrt	
h2trunc		h2trunc	
hceil		hceil	
hcos		hcos	
hexp		hexp	
hexp10		hexp10	
hexp2		hexp2	
hfloor		hfloor	
hlog		hlog	
hlog10		hlog10	
hlog2		hlog2	
hrcp		hrcp	
hrint		hrint	
hrsqrt		hrsqrt	
hsin		hsin	
hsqrt		hsqrt	
htrunc		htrunc	
hypot		hypot	
hypotf		hypotf	
ilogb		ilogb	
ilogbf		ilogbf	
isfinite		isfinite	
isinf		isinf	
isnan		isnan	
j0		j0	
j0f		j0f	
j1		j1	
j1f		j1f	
jn		jn	
jnf		jnf	
labs		labs	
ldexp		ldexp	
ldexpf		ldexpf	
lgamma		lgamma	
lgammaf		lgammaf	
llabs		llabs	
llrint		llrint	
llrintf		llrintf	
llround		llround	
llroundf		llroundf	
log		log	
log10		log10	
log10f		log10f	
log1p		log1p	
log1pf		log1pf	
log2		log2	
log2f		log2f	

CUDA	Deprecated	HIP	Deprecated
logb		logb	
logbf		logbf	
logf		logf	
lrint		lrint	
lrintf		lrintf	
lround		lround	
lroundf		lroundf	
max		max	
min		min	
modf		modf	
modff		modff	
nan		nan	
nanf		nanf	
nearbyint		nearbyint	
nearbyintf		nearbyintf	
nextafter		nextafter	
nextafterf		nextafterf	
norm		norm	
norm3d		norm3d	
norm3df		norm3df	
norm4d		norm4d	
norm4df		norm4df	
normcdf		normcdf	
normcdf		normcdf	
normcdfinv		normcdfinv	
normcdfinvf		normcdfinvf	
normf		normf	
pow		pow	
powf		powf	
rcbrt		rcbrt	
rcbrtf		rcbrtf	
remainder		remainder	
remainderf		remainderf	
remquo		remquo	
remquof		remquof	
rhypot		rhypot	
rhypotf		rhypotf	
rint		rint	
rintf		rintf	
rnorm		rnorm	
rnorm3d		rnorm3d	
rnorm3df		rnorm3df	
rnorm4d		rnorm4d	
rnorm4df		rnorm4df	
rnormf		rnormf	
round		round	
roundf		roundf	
rsqrt		rsqrt	
rsqrtf		rsqrtf	
scalbln		scalbln	
scalblnf		scalblnf	
scalbn		scalbn	

CUDA	Deprecated	HIP	Deprecated
scalbnf		scalbnf	
signbit		signbit	
sin		sin	
sincos		sincos	
sincosf		sincosf	
sincospi		sincospi	
sincospif		sincospif	
sinf		sinf	
sinh		sinh	
sinhf		sinhf	
sinpi		sinpi	
sinpif		sinpif	
sqrt		sqrt	
sqrtf		sqrtf	
tan		tan	
tanf		tanf	
tanh		tanh	
tanhf		tanhf	
tgamma		tgamma	
tgammaf		tgammaf	
trunc		trunc	
truncf		truncf	
y0		y0	
y0f		y0f	
y1		y1	
y1f		y1f	
yn		yn	
ynf		ynf	

1.5 CUBLAS API Supported by HIP

1.5.1 CUBLAS Data Types

CUDA	Deprecated	HIP	Deprecated
CUBLAS_ATOMICS_ALLOWED		HIPBLAS_ATOMICS_ALLOWED	
CUBLAS_ATOMICS_NOT_ALLOWED		HIPBLAS_ATOMICS_NOT_ALLOWED	
CUBLAS_DIAG_NON_UNIT		HIPBLAS_DIAG_NON_UNIT	
CUBLAS_DIAG_UNIT		HIPBLAS_DIAG_UNIT	
CUBLAS_FILL_MODE_FULL		HIPBLAS_FILL_MODE_FULL	
CUBLAS_FILL_MODE_LOWER		HIPBLAS_FILL_MODE_LOWER	
CUBLAS_FILL_MODE_UPPER		HIPBLAS_FILL_MODE_UPPER	
CUBLAS_GEMM_DEFAULT		HIPBLAS_GEMM_DEFAULT	
CUBLAS_GEMM_DFALT		HIPBLAS_GEMM_DEFAULT	
CUBLAS_OP_C		HIPBLAS_OP_C	
CUBLAS_OP_HERMITAN		HIPBLAS_OP_C	
CUBLAS_OP_N		HIPBLAS_OP_N	
CUBLAS_OP_T		HIPBLAS_OP_T	
CUBLAS_POINTER_MODE_DEVICE		HIPBLAS_POINTER_MODE_DEVICE	

CUDA	Deprecated	HIP	Deprecated
CUBLAS_POINTER_MODE_HOST		HIPBLAS_POINTER_MODE_HOST	
CUBLAS_SIDE_LEFT		HIPBLAS_SIDE_LEFT	
CUBLAS_SIDE_RIGHT		HIPBLAS_SIDE_RIGHT	
CUBLAS_STATUS_ALLOC_FAILED		HIPBLAS_STATUS_ALLOC_FAILED	
CUBLAS_STATUS_ARCH_MISMATCH		HIPBLAS_STATUS_ARCH_MISMATCH	
CUBLAS_STATUS_EXECUTION_FAILED		HIPBLAS_STATUS_EXECUTION_FAILED	
CUBLAS_STATUS_INTERNAL_ERROR		HIPBLAS_STATUS_INTERNAL_ERROR	
CUBLAS_STATUS_INVALID_VALUE		HIPBLAS_STATUS_INVALID_VALUE	
CUBLAS_STATUS_MAPPING_ERROR		HIPBLAS_STATUS_MAPPING_ERROR	
CUBLAS_STATUS_NOT_INITIALIZED		HIPBLAS_STATUS_NOT_INITIALIZED	
CUBLAS_STATUS_NOT_SUPPORTED		HIPBLAS_STATUS_NOT_SUPPORTED	
CUBLAS_STATUS_SUCCESS		HIPBLAS_STATUS_SUCCESS	
cublasAtomicsMode_t		hipblasAtomicsMode_t	
cublasDataType_t		hipblasDatatype_t	
cublasDiagType_t		hipblasDiagType_t	
cublasFillMode_t		hipblasFillMode_t	
cublasGemmAlgo_t		hipblasGemmAlgo_t	
cublasHandle_t		hipblasHandle_t	
cublasOperation_t		hipblasOperation_t	
cublasPointerMode_t		hipblasPointerMode_t	
cublasSideMode_t		hipblasSideMode_t	
cublasStatus		hipblasStatus_t	
cublasStatus_t		hipblasStatus_t	

1.5.2 CUDA Data Types Reference

CUDA	Deprecated	HIP	Deprecated
CUDA_C_16F		HIPBLAS_C_16F	
CUDA_C_32F		HIPBLAS_C_32F	
CUDA_C_32I		HIPBLAS_C_32I	
CUDA_C_32U		HIPBLAS_C_32U	
CUDA_C_64F		HIPBLAS_C_64F	
CUDA_C_8I		HIPBLAS_C_8I	
CUDA_C_8U		HIPBLAS_C_8U	
CUDA_R_16F		HIPBLAS_R_16F	
CUDA_R_32F		HIPBLAS_R_32F	
CUDA_R_32I		HIPBLAS_R_32I	
CUDA_R_32U		HIPBLAS_R_32U	
CUDA_R_64F		HIPBLAS_R_64F	
CUDA_R_8I		HIPBLAS_R_8I	
CUDA_R_8U		HIPBLAS_R_8U	
cudaDataType		hipblasDatatype_t	
cudaDataType_t		hipblasDatatype_t	

1.5.3 CUBLAS Helper Function Reference

CUDA	Deprecated	HIP	Deprecated
cublasCreate		hipblasCreate	
cublasCreate_v2		hipblasCreate	
cublasDestroy		hipblasDestroy	
cublasDestroy_v2		hipblasDestroy	
cublasGetAtomicsMode		hipblasGetAtomicsMode	
cublasGetMatrix		hipblasGetMatrix	
cublasGetMatrixAsync		hipblasGetMatrixAsync	
cublasGetPointerMode		hipblasGetPointerMode	
cublasGetPointerMode_v2		hipblasGetPointerMode	
cublasGetStream		hipblasGetStream	
cublasGetStream_v2		hipblasGetStream	
cublasGetVector		hipblasGetVector	
cublasGetVectorAsync		hipblasGetVectorAsync	
cublasSetAtomicsMode		hipblasSetAtomicsMode	
cublasSetMatrix		hipblasSetMatrix	
cublasSetMatrixAsync		hipblasSetMatrixAsync	
cublasSetPointerMode		hipblasSetPointerMode	
cublasSetPointerMode_v2		hipblasSetPointerMode	
cublasSetStream		hipblasSetStream	
cublasSetStream_v2		hipblasSetStream	
cublasSetVector		hipblasSetVector	
cublasSetVectorAsync		hipblasSetVectorAsync	

1.5.4 CUBLAS Level-1 Function Reference

CUDA	Deprecated	HIP	Deprecated
cublasCaxpy		hipblasCaxpy	
cublasCaxpy_v2		hipblasCaxpy	
cublasCcopy		hipblasCcopy	
cublasCcopy_v2		hipblasCcopy	
cublasCdote		hipblasCdote	
cublasCdote_v2		hipblasCdote	
cublasCdotu		hipblasCdotu	
cublasCdotu_v2		hipblasCdotu	
cublasCrot		hipblasCrot	
cublasCrot_v2		hipblasCrot	
cublasCrotg		hipblasCrotg	
cublasCrotg_v2		hipblasCrotg	
cublasCscal		hipblasCscal	
cublasCscal_v2		hipblasCscal	
cublasCsrot		hipblasCsrot	
cublasCsrot_v2		hipblasCsrot	
cublasCsscal		hipblasCsscal	
cublasCsscal_v2		hipblasCsscal	
cublasCswap		hipblasCswap	
cublasCswap_v2		hipblasCswap	
cublasDasum		hipblasDasum	
cublasDasum_v2		hipblasDasum	
cublasDaxpy		hipblasDaxpy	

CUDA	Deprecated	HIP	Deprecated
cublasDaxpy_v2		hipblasDaxpy	
cublasDcopy		hipblasDcopy	
cublasDcopy_v2		hipblasDcopy	
cublasDdot		hipblasDdot	
cublasDdot_v2		hipblasDdot	
cublasDnrm2		hipblasDnrm2	
cublasDnrm2_v2		hipblasDnrm2	
cublasDrot		hipblasDrot	
cublasDrot_v2		hipblasDrot	
cublasDrotg		hipblasDrotg	
cublasDrotg_v2		hipblasDrotg	
cublasDrotm		hipblasDrotm	
cublasDrotm_v2		hipblasDrotm	
cublasDrotmg		hipblasDrotmg	
cublasDrotmg_v2		hipblasDrotmg	
cublasDscal		hipblasDscal	
cublasDscal_v2		hipblasDscal	
cublasDswap		hipblasDswap	
cublasDswap_v2		hipblasDswap	
cublasDzasum		hipblasDzasum	
cublasDzasum_v2		hipblasDzasum	
cublasDznrm2		hipblasDznrm2	
cublasDznrm2_v2		hipblasDznrm2	
cublasIcamax		hipblasIcamax	
cublasIcamax_v2		hipblasIcamax	
cublasIcamin		hipblasIcamin	
cublasIcamin_v2		hipblasIcamin	
cublasIdamax		hipblasIdamax	
cublasIdamax_v2		hipblasIdamax	
cublasIdamin		hipblasIdamin	
cublasIdamin_v2		hipblasIdamin	
cublasIsamax		hipblasIsamax	
cublasIsamax_v2		hipblasIsamax	
cublasIsamin		hipblasIsamin	
cublasIsamin_v2		hipblasIsamin	
cublasIzamax		hipblasIzamax	
cublasIzamax_v2		hipblasIzamax	
cublasIzamin		hipblasIzamin	
cublasIzamin_v2		hipblasIzamin	
cublasSasum		hipblasSasum	
cublasSasum_v2		hipblasSasum	
cublasSaxpy		hipblasSaxpy	
cublasSaxpy_v2		hipblasSaxpy	
cublasScasum		hipblasScasum	
cublasScasum_v2		hipblasScasum	
cublasScnrm2		hipblasScnrm2	
cublasScnrm2_v2		hipblasScnrm2	
cublasScopy		hipblasScopy	
cublasScopy_v2		hipblasScopy	
cublasSdot		hipblasSdot	
cublasSdot_v2		hipblasSdot	
cublasSnrm2		hipblasSnrm2	

CUDA	Deprecated	HIP	Deprecated
cublasSnrm2 v2		hipblasSnrm2	
cublasSrot		hipblasSrot	
cublasSrot v2		hipblasSrot	
cublasSrotg		hipblasSrotg	
cublasSrotg v2		hipblasSrotg	
cublasSrotm		hipblasSrotm	
cublasSrotm v2		hipblasSrotm	
cublasSrotmg		hipblasSrotmg	
cublasSrotmg v2		hipblasSrotmg	
cublasSscal		hipblasSscal	
cublasSscal v2		hipblasSscal	
cublasSswap		hipblasSswap	
cublasSswap v2		hipblasSswap	
cublasZaxpy		hipblasZaxpy	
cublasZaxpy v2		hipblasZaxpy	
cublasZcopy		hipblasZcopy	
cublasZcopy v2		hipblasZcopy	
cublasZdotc		hipblasZdotc	
cublasZdotc v2		hipblasZdotc	
cublasZdotu		hipblasZdotu	
cublasZdotu v2		hipblasZdotu	
cublasZdrot		hipblasZdrot	
cublasZdrot v2		hipblasZdrot	
cublasZdscal		hipblasZdscal	
cublasZdscal v2		hipblasZdscal	
cublasZrot		hipblasZrot	
cublasZrot v2		hipblasZrot	
cublasZrotg		hipblasZrotg	
cublasZrotg v2		hipblasZrotg	
cublasZscal		hipblasZscal	
cublasZscal v2		hipblasZscal	
cublasZswap		hipblasZswap	
cublasZswap v2		hipblasZswap	

1.5.5 CUBLAS Level-2 Function Reference

CUDA	Deprecated	HIP	Deprecated
cublasCgbmv		hipblasCgbmv	
cublasCgbmv v2		hipblasCgbmv	
cublasCgemv		hipblasCgemv	
cublasCgemv v2		hipblasCgemv	
cublasCgerc		hipblasCgerc	
cublasCgerc v2		hipblasCgerc	
cublasCgeru		hipblasCgeru	
cublasCgeru v2		hipblasCgeru	
cublasChbmv		hipblasChbmv	
cublasChbmv v2		hipblasChbmv	
cublasChemv		hipblasChemv	
cublasChemv v2		hipblasChemv	
cublasCher		hipblasCher	

CUDA	Deprecated	HIP	Deprecated
cublasCher2		hipblasCher2	
cublasCher2 v2		hipblasCher2	
cublasCher v2		hipblasCher	
cublasChpmv		hipblasChpmv	
cublasChpmv v2		hipblasChpmv	
cublasChpr		hipblasChpr	
cublasChpr2		hipblasChpr2	
cublasChpr2 v2		hipblasChpr2	
cublasChpr v2		hipblasChpr	
cublasCsymv		hipblasCsymv	
cublasCsymv v2		hipblasCsymv	
cublasCsyr		hipblasCsyr	
cublasCsyr2		hipblasCsyr2	
cublasCsyr2 v2		hipblasCsyr2	
cublasCsyr v2		hipblasCsyr	
cublasCtbmv		hipblasCtbmv	
cublasCtbmv v2		hipblasCtbmv	
cublasCtbsv		hipblasCtbsv	
cublasCtbsv v2		hipblasCtbsv	
cublasCtpmv		hipblasCtpmv	
cublasCtpmv v2		hipblasCtpmv	
cublasCtpsv		hipblasCtpsv	
cublasCtpsv v2		hipblasCtpsv	
cublasCtrmv		hipblasCtrmv	
cublasCtrmv v2		hipblasCtrmv	
cublasCtrsv		hipblasCtrsv	
cublasCtrsv v2		hipblasCtrsv	
cublasDgbmv		hipblasDgbmv	
cublasDgbmv v2		hipblasDgbmv	
cublasDgemv		hipblasDgemv	
cublasDgemv v2		hipblasDgemv	
cublasDger		hipblasDger	
cublasDger v2		hipblasDger	
cublasDsbgmv		hipblasDsbgmv	
cublasDsbgmv v2		hipblasDsbgmv	
cublasDspmv		hipblasDspmv	
cublasDspmv v2		hipblasDspmv	
cublasDspr		hipblasDspr	
cublasDspr2		hipblasDspr2	
cublasDspr2 v2		hipblasDspr2	
cublasDspr v2		hipblasDspr	
cublasDsymv		hipblasDsymv	
cublasDsymv v2		hipblasDsymv	
cublasDsyr		hipblasDsyr	
cublasDsyr2		hipblasDsyr2	
cublasDsyr2 v2		hipblasDsyr2	
cublasDsyr v2		hipblasDsyr	
cublasDtbmv		hipblasDtbmv	
cublasDtbmv v2		hipblasDtbmv	
cublasDtbsv		hipblasDtbsv	
cublasDtbsv v2		hipblasDtbsv	
cublasDtpmv		hipblasDtpmv	

CUDA	Deprecated	HIP	Deprecated
cublasDtpmv_v2		hipblasDtpmv	
cublasDtpsv		hipblasDtpsv	
cublasDtpsv_v2		hipblasDtpsv	
cublasDtrmv		hipblasDtrmv	
cublasDtrmv_v2		hipblasDtrmv	
cublasDtrsv		hipblasDtrsv	
cublasDtrsv_v2		hipblasDtrsv	
cublasSgbmv		hipblasSgbmv	
cublasSgbmv_v2		hipblasSgbmv	
cublasSgemv		hipblasSgemv	
cublasSgemv_v2		hipblasSgemv	
cublasSger		hipblasSger	
cublasSger_v2		hipblasSger	
cublasSsbmv		hipblasSsbmv	
cublasSsbmv_v2		hipblasSsbmv	
cublasSspmv		hipblasSspmv	
cublasSspmv_v2		hipblasSspmv	
cublasSspr		hipblasSspr	
cublasSspr2		hipblasSspr2	
cublasSspr2_v2		hipblasSspr2	
cublasSspr_v2		hipblasSspr	
cublasSsymv		hipblasSsymv	
cublasSsymv_v2		hipblasSsymv	
cublasSsyr		hipblasSsyr	
cublasSsyr2		hipblasSsyr2	
cublasSsyr2_v2		hipblasSsyr2	
cublasSsyr_v2		hipblasSsyr	
cublasStbmv		hipblasStbmv	
cublasStbmv_v2		hipblasStbmv	
cublasStbsv		hipblasStbsv	
cublasStbsv_v2		hipblasStbsv	
cublasStpmv		hipblasStpmv	
cublasStpmv_v2		hipblasStpmv	
cublasStpsv		hipblasStpsv	
cublasStpsv_v2		hipblasStpsv	
cublasStrmv		hipblasStrmv	
cublasStrmv_v2		hipblasStrmv	
cublasStrsv		hipblasStrsv	
cublasStrsv_v2		hipblasStrsv	
cublasZgbmv		hipblasZgbmv	
cublasZgbmv_v2		hipblasZgbmv	
cublasZgemv		hipblasZgemv	
cublasZgemv_v2		hipblasZgemv	
cublasZgerc		hipblasZgerc	
cublasZgerc_v2		hipblasZgerc	
cublasZgeru		hipblasZgeru	
cublasZgeru_v2		hipblasZgeru	
cublasZhbm		hipblasZhbm	
cublasZhbm_v2		hipblasZhbm	
cublasZhemv		hipblasZhemv	
cublasZhemv_v2		hipblasZhemv	

CUDA	Deprecated	HIP	Deprecated
cublasZher		hipblasZher	
cublasZher2		hipblasZher2	
cublasZher2 v2		hipblasZher2	
cublasZher v2		hipblasZher	
cublasZhpmv		hipblasZhpmv	
cublasZhpmv v2		hipblasZhpmv	
cublasZhpr		hipblasZhpr	
cublasZhpr2		hipblasZhpr2	
cublasZhpr2 v2		hipblasZhpr2	
cublasZhpr v2		hipblasZhpr	
cublasZsymv		hipblasZsymv	
cublasZsymv v2		hipblasZsymv	
cublasZsyr		hipblasZsyr	
cublasZsyr2		hipblasZsyr2	
cublasZsyr2 v2		hipblasZsyr2	
cublasZsyr v2		hipblasZsyr	
cublasZtbmv		hipblasZtbmv	
cublasZtbmv v2		hipblasZtbmv	
cublasZtbsv		hipblasZtbsv	
cublasZtbsv v2		hipblasZtbsv	
cublasZtpmv		hipblasZtpmv	
cublasZtpmv v2		hipblasZtpmv	
cublasZtpsv		hipblasZtpsv	
cublasZtpsv v2		hipblasZtpsv	
cublasZtrmv		hipblasZtrmv	
cublasZtrmv v2		hipblasZtrmv	
cublasZtrsv		hipblasZtrsv	
cublasZtrsv v2		hipblasZtrsv	

1.5.6 CUBLAS Level-3 Function Reference

CUDA	Deprecated	HIP	D
cublasCgemm		hipblasCgemm	
cublasCgemmBatched		hipblasCgemmBatched	
cublasCgemmStridedBatched		hipblasCgemmStridedBatched	
cublasCgemm v2		hipblasCgemm	
cublasChemmm		hipblasChemmm	
cublasChemmm v2		hipblasChemmm	
cublasCher2k		hipblasCher2k	
cublasCher2k v2		hipblasCher2k	
cublasCherk		hipblasCherk	
cublasCherk v2		hipblasCherk	
cublasCherkx		hipblasCherkx	
cublasCsymm		hipblasCsymm	
cublasCsymm v2		hipblasCsymm	
cublasCsyr2k		hipblasCsyr2k	
cublasCsyr2k v2		hipblasCsyr2k	
cublasCsyrk		hipblasCsyrk	
cublasCsyrk v2		hipblasCsyrk	
cublasCsyrkx		hipblasCsyrkx	
cublasCtrmm		hipblasCtrmm	

CUDA	Deprecated	HIP	D
cublasCtmm v2		hipblasCtmm	
cublasCtrsm		hipblasCtrsm	
cublasCtrsm v2		hipblasCtrsm	
cublasDgemm		hipblasDgemm	
cublasDgemmBatched		hipblasDgemmBatched	
cublasDgemmStridedBatched		hipblasDgemmStridedBatched	
cublasDgemm v2		hipblasDgemm	
cublasDsymm		hipblasDsymm	
cublasDsymm v2		hipblasDsymm	
cublasDsyr2k		hipblasDsyr2k	
cublasDsyr2k v2		hipblasDsyr2k	
cublasDsyrk		hipblasDsyrk	
cublasDsyrk v2		hipblasDsyrk	
cublasDsyrkx		hipblasDsyrkx	
cublasDtrmm		hipblasDtrmm	
cublasDtrmm v2		hipblasDtrmm	
cublasDtrsm		hipblasDtrsm	
cublasDtrsm v2		hipblasDtrsm	
cublasHgemm		hipblasHgemm	
cublasHgemmBatched		hipblasHgemmBatched	
cublasHgemmStridedBatched		hipblasHgemmStridedBatched	
cublasSgemm		hipblasSgemm	
cublasSgemmBatched		hipblasSgemmBatched	
cublasSgemmStridedBatched		hipblasSgemmStridedBatched	
cublasSgemm v2		hipblasSgemm	
cublasSsymm		hipblasSsymm	
cublasSsymm v2		hipblasSsymm	
cublasSsyr2k		hipblasSsyr2k	
cublasSsyr2k v2		hipblasSsyr2k	
cublasSsyrk		hipblasSsyrk	
cublasSsyrk v2		hipblasSsyrk	
cublasSsyrkx		hipblasSsyrkx	
cublasStrmm		hipblasStrmm	
cublasStrmm v2		hipblasStrmm	
cublasStrsm		hipblasStrsm	
cublasStrsm v2		hipblasStrsm	
cublasZgemm		hipblasZgemm	
cublasZgemmBatched		hipblasZgemmBatched	
cublasZgemmStridedBatched		hipblasZgemmStridedBatched	
cublasZgemm_v2		hipblasZgemm	
cublasZhemm		hipblasZhemm	
cublasZhemm_v2		hipblasZhemm	
cublasZher2k		hipblasZher2k	
cublasZher2k_v2		hipblasZher2k	
cublasZherk		hipblasZherk	
cublasZherk_v2		hipblasZherk	
cublasZherkx		hipblasZherkx	
cublasZsymm		hipblasZsymm	
cublasZsymm_v2		hipblasZsymm	
cublasZsyr2k		hipblasZsyr2k	
cublasZsyr2k_v2		hipblasZsyr2k	
cublasZsyrk		hipblasZsyrk	

CUDA	Deprecated	HIP	D
cublasZsyrk_v2		hipblasZsyrk	
cublasZsyrkx		hipblasZsyrkx	
cublasZtrmm		hipblasZtrmm	
cublasZtrmm_v2		hipblasZtrmm	
cublasZtrsm		hipblasZtrsm	
cublasZtrsm_v2		hipblasZtrsm	

1.5.7 BLAS-like Extension

CUDA	Deprecated	HIP	Deprecated
cublasCdggmm		hipblasCdggmm	
cublasCgeam		hipblasCgeam	
cublasCgeqrfBatched		hipblasCgeqrfBatched	
cublasCgetrfBatched		hipblasCgetrfBatched	
cublasCgetriBatched		hipblasCgetriBatched	
cublasCgetrsBatched		hipblasCgetrsBatched	
cublasCtrsmBatched		hipblasCtrsmBatched	
cublasDdggmm		hipblasDdggmm	
cublasDgeam		hipblasDgeam	
cublasDgeqrfBatched		hipblasDgeqrfBatched	
cublasDgetrfBatched		hipblasDgetrfBatched	
cublasDgetriBatched		hipblasDgetriBatched	
cublasDgetrsBatched		hipblasDgetrsBatched	
cublasDtrsmBatched		hipblasDtrsmBatched	
cublasGemmBatchedEx		hipblasGemmBatchedEx	
cublasGemmEx		hipblasGemmEx	
cublasGemmStridedBatchedEx		hipblasGemmStridedBatchedEx	
cublasSdggmm		hipblasSdggmm	
cublasSgeam		hipblasSgeam	
cublasSgeqrfBatched		hipblasSgeqrfBatched	
cublasSgetrfBatched		hipblasSgetrfBatched	
cublasSgetriBatched		hipblasSgetriBatched	
cublasSgetrsBatched		hipblasSgetrsBatched	
cublasStrsmBatched		hipblasStrsmBatched	
cublasZdggmm		hipblasZdggmm	
cublasZgeam		hipblasZgeam	
cublasZgeqrfBatched		hipblasZgeqrfBatched	
cublasZgetrfBatched		hipblasZgetrfBatched	
cublasZgetriBatched		hipblasZgetriBatched	
cublasZgetrsBatched		hipblasZgetrsBatched	
cublasZtrsmBatched		hipblasZtrsmBatched	

1.6 CURAND API Supported by HIP

1.6.1 CURAND Data Types

CUDA	Deprecated	HIP	Deprecated
CURAND_RNG_PSEUDO_DEFAULT		HIPRAND_RNG_PSEUDO_DEFAULT	
CURAND_RNG_PSEUDO_MRG32K3A		HIPRAND_RNG_PSEUDO_MRG32K3A	
CURAND_RNG_PSEUDO_MT19937		HIPRAND_RNG_PSEUDO_MT19937	
CURAND_RNG_PSEUDO_MTGP32		HIPRAND_RNG_PSEUDO_MTGP32	
CURAND_RNG_PSEUDO_PHILOX4_32_10		HIPRAND_RNG_PSEUDO_PHILOX4_32_10	
CURAND_RNG_PSEUDO_XORWOW		HIPRAND_RNG_PSEUDO_XORWOW	
CURAND_RNG_QUASI_DEFAULT		HIPRAND_RNG_QUASI_DEFAULT	
CURAND_RNG_QUASI_SCRAMBLE_D_SOBOL32		HIPRAND_RNG_QUASI_SCRAMBLE_D_SOBOL32	
CURAND_RNG_QUASI_SCRAMBLE_D_SOBOL64		HIPRAND_RNG_QUASI_SCRAMBLE_D_SOBOL64	
CURAND_RNG_QUASI_SOBOL32		HIPRAND_RNG_QUASI_SOBOL32	
CURAND_RNG_QUASI_SOBOL64		HIPRAND_RNG_QUASI_SOBOL64	
CURAND_RNG_TEST		HIPRAND_RNG_TEST	
CURAND_STATUS_ALLOCATION_FAILED		HIPRAND_STATUS_ALLOCATION_FAILED	
CURAND_STATUS_ARCH_MISMATCH		HIPRAND_STATUS_ARCH_MISMATCH	
CURAND_STATUS_DOUBLE_PRECISION_REQUIRED		HIPRAND_STATUS_DOUBLE_PRECISION_REQUIRED	
CURAND_STATUS_INITIALIZATION_FAILED		HIPRAND_STATUS_INITIALIZATION_FAILED	
CURAND_STATUS_INTERNAL_ERROR		HIPRAND_STATUS_INTERNAL_ERROR	
CURAND_STATUS_LAUNCH_FAILURE		HIPRAND_STATUS_LAUNCH_FAILURE	
CURAND_STATUS_LENGTH_NOT_MULTIPLE		HIPRAND_STATUS_LENGTH_NOT_MULTIPLE	
CURAND_STATUS_NOT_INITIALIZED		HIPRAND_STATUS_NOT_INITIALIZED	
CURAND_STATUS_OUT_OF_RANGE		HIPRAND_STATUS_OUT_OF_RANGE	
CURAND_STATUS_PREEXISTING_FAILURE		HIPRAND_STATUS_PREEXISTING_FAILURE	
CURAND_STATUS_SUCCESS		HIPRAND_STATUS_SUCCESS	
CURAND_STATUS_TYPE_ERROR		HIPRAND_STATUS_TYPE_ERROR	
CURAND_STATUS_VERSION_MISMATCH		HIPRAND_STATUS_VERSION_MISMATCH	
CURAND_VERSION		HIPRAND_VERSION	
curandDirectionVectors32_t		hiprandDirectionVectors32_t	
curandDiscreteDistribution_st		hiprandDiscreteDistribution_st	
curandDiscreteDistribution_t		hiprandDiscreteDistribution_t	
curandGenerator_st		hiprandGenerator_st	
curandGenerator_t		hiprandGenerator_t	
curandRngType		hiprandRngType_t	

CUDA	Deprecated	HIP	Deprecated
curandRngType_t		hiprandRngType_t	
curandState		hiprandState	
curandStateMRG32k3a		hiprandStateMRG32k3a	
curandStateMRG32k3a_t		hiprandStateMRG32k3a_t	
curandStateMtg32		hiprandStateMtg32	
curandStateMtg32_t		hiprandStateMtg32_t	
curandStatePhilox4_32_10		hiprandStatePhilox4_32_10	
curandStatePhilox4_32_10_t		hiprandStatePhilox4_32_10_t	
curandStateSobol32		hiprandStateSobol32	
curandStateSobol32_t		hiprandStateSobol32_t	
curandStateXORWOW		hiprandStateXORWOW	
curandStateXORWOW_t		hiprandStateXORWOW_t	
curandState_t		hiprandState_t	
curandStatus		hiprandStatus_t	
curandStatus_t		hiprandStatus_t	

1.6.2 Host API Functions

CUDA	Deprecated	HIP	Deprecated
curandCreateGenerator		hiprandCreateGenerator	
curandCreateGeneratorHost		hiprandCreateGeneratorHost	
curandCreatePoissonDistribution		hiprandCreatePoissonDistribution	
curandDestroyDistribution		hiprandDestroyDistribution	
curandDestroyGenerator		hiprandDestroyGenerator	
curandGenerate		hiprandGenerate	
curandGenerateLogNormal		hiprandGenerateLogNormal	
curandGenerateLogNormalDouble		hiprandGenerateLogNormalDouble	
curandGenerateNormal		hiprandGenerateNormal	
curandGenerateNormalDouble		hiprandGenerateNormalDouble	
curandGeneratePoisson		hiprandGeneratePoisson	
curandGenerateSeeds		hiprandGenerateSeeds	
curandGenerateUniform		hiprandGenerateUniform	
curandGenerateUniformDouble		hiprandGenerateUniformDouble	
curandGetVersion		hiprandGetVersion	
curandMakeMTGP32Constants		hiprandMakeMTGP32Constants	
curandMakeMTGP32KernelState		hiprandMakeMTGP32KernelState	
curandSetGeneratorOffset		hiprandSetGeneratorOffset	
curandSetPseudoRandomGeneratorSeed		hiprandSetPseudoRandomGeneratorSeed	
curandSetQuasiRandomGeneratorDimensions		hiprandSetQuasiRandomGeneratorDimensions	
curandSetStream		hiprandSetStream	

1.6.3 Device API Functions

CUDA	Deprecated	HIP	Deprecated
curand		hiprand	
curand_discrete		hiprand_discrete	
curand_discrete4		hiprand_discrete4	
curand_init		hiprand_init	
curand_log_normal		hiprand_log_normal	
curand_log_normal2		hiprand_log_normal2	
curand_log_normal2_double		hiprand_log_normal2_double	

CUDA	Deprecated	HIP	Deprecated
curand log normal4		hiprand log normal4	
curand log normal4 double		hiprand log normal4 double	
curand log normal double		hiprand log normal double	
curand normal		hiprand normal	
curand normal2		hiprand normal2	
curand normal2 double		hiprand normal2 double	
curand normal4		hiprand normal4	
curand normal4 double		hiprand normal4 double	
curand normal double		hiprand normal double	
curand poisson		hiprand poisson	
curand poisson4		hiprand poisson4	
curand uniform		hiprand uniform	
curand uniform2 double		hiprand uniform2 double	
curand uniform4		hiprand uniform4	
curand uniform4 double		hiprand uniform4 double	
curand uniform double		hiprand uniform double	

1.7 CUFFT API Supported by HIP

1.7.1 CUFFT Data Types

CUDA	Deprecated	HIP	Deprecated
CUFFT_ALLOC_FAILED		HIPFFT_ALLOC_FAILED	
CUFFT_C2C		HIPFFT_C2C	
CUFFT_C2R		HIPFFT_C2R	
CUFFT_D2Z		HIPFFT_D2Z	
CUFFT_EXEC_FAILED		HIPFFT_EXEC_FAILED	
CUFFT_FORWARD		HIPFFT_FORWARD	
CUFFT_INCOMPLETE_PARAMETER_LIST		HIPFFT_INCOMPLETE_PARAMETER_LIST	
CUFFT_INTERNAL_ERROR		HIPFFT_INTERNAL_ERROR	
CUFFT_INVALID_DEVICE		HIPFFT_INVALID_DEVICE	
CUFFT_INVALID_PLAN		HIPFFT_INVALID_PLAN	
CUFFT_INVALID_SIZE		HIPFFT_INVALID_SIZE	
CUFFT_INVALID_TYPE		HIPFFT_INVALID_TYPE	
CUFFT_INVALID_VALUE		HIPFFT_INVALID_VALUE	
CUFFT_INVERSE		HIPFFT_BACKWARD	
CUFFT_NOT_IMPLEMENTED		HIPFFT_NOT_IMPLEMENTED	
CUFFT_NOT_SUPPORTED		HIPFFT_NOT_SUPPORTED	
CUFFT_NO_WORKSPACE		HIPFFT_NO_WORKSPACE	
CUFFT_PARSE_ERROR		HIPFFT_PARSE_ERROR	
CUFFT_R2C		HIPFFT_R2C	
CUFFT_SETUP_FAILED		HIPFFT_SETUP_FAILED	
CUFFT_SUCCESS		HIPFFT_SUCCESS	
CUFFT_UNALIGNED_DATA		HIPFFT_UNALIGNED_DATA	
CUFFT_Z2D		HIPFFT_Z2D	
CUFFT_Z2Z		HIPFFT_Z2Z	
cufftComplex		hipfftComplex	
cufftDoubleComplex		hipfftDoubleComplex	

CUDA	Deprecated	HIP	Deprecated
cufftDoubleReal		hipfftDoubleReal	
cufftHandle		hipfftHandle	
cufftReal		hipfftReal	
cufftResult		hipfftResult	
cufftResult_t		hipfftResult_t	
cufftType		hipfftType	
cufftType_t		hipfftType_t	

1.7.2 CUFFT API Functions

CUDA	Deprecated	HIP	Deprecated
cufftCreate		hipfftCreate	
cufftDestroy		hipfftDestroy	
cufftEstimate1d		hipfftEstimate1d	
cufftEstimate2d		hipfftEstimate2d	
cufftEstimate3d		hipfftEstimate3d	
cufftEstimateMany		hipfftEstimateMany	
cufftExecC2C		hipfftExecC2C	
cufftExecC2R		hipfftExecC2R	
cufftExecD2Z		hipfftExecD2Z	
cufftExecR2C		hipfftExecR2C	
cufftExecZ2D		hipfftExecZ2D	
cufftExecZ2Z		hipfftExecZ2Z	
cufftGetProperty		hipfftGetProperty	
cufftGetSize		hipfftGetSize	
cufftGetSize1d		hipfftGetSize1d	
cufftGetSize2d		hipfftGetSize2d	
cufftGetSize3d		hipfftGetSize3d	
cufftGetSizeMany		hipfftGetSizeMany	
cufftGetSizeMany64		hipfftGetSizeMany64	
cufftGetVersion		hipfftGetVersion	
cufftMakePlan1d		hipfftMakePlan1d	
cufftMakePlan2d		hipfftMakePlan2d	
cufftMakePlan3d		hipfftMakePlan3d	
cufftMakePlanMany		hipfftMakePlanMany	
cufftMakePlanMany64		hipfftMakePlanMany64	
cufftPlan1d		hipfftPlan1d	
cufftPlan2d		hipfftPlan2d	
cufftPlan3d		hipfftPlan3d	
cufftPlanMany		hipfftPlanMany	
cufftSetAutoAllocation		hipfftSetAutoAllocation	
cufftSetStream		hipfftSetStream	
cufftSetWorkArea		hipfftSetWorkArea	

1.8 CUSPARSE API Supported by HIP

1.8.1 CUSPARSE Types References

CUDA	Deprecated	HIP	Deprecated
CUSPARSE_ACTION_NUMERIC		HIPSPARSE_ACTION_NUMERIC	
CUSPARSE_ACTION_SYMBOLIC		HIPSPARSE_ACTION_SYMBOLIC	
CUSPARSE_DIAG_TYPE_NON_UNIT		HIPSPARSE_DIAG_TYPE_NON_UNIT	
CUSPARSE_DIAG_TYPE_UNIT		HIPSPARSE_DIAG_TYPE_UNIT	
CUSPARSE_DIRECTION_COLUMN		HIPSPARSE_DIRECTION_COLUMN	
CUSPARSE_DIRECTION_ROW		HIPSPARSE_DIRECTION_ROW	
CUSPARSE_FILL_MODE_LOWER		HIPSPARSE_FILL_MODE_LOWER	
CUSPARSE_FILL_MODE_UPPER		HIPSPARSE_FILL_MODE_UPPER	
CUSPARSE_HYB_PARTITION_AUTO	✓	HIPSPARSE_HYB_PARTITION_AUTO	
CUSPARSE_HYB_PARTITION_MAX	✓	HIPSPARSE_HYB_PARTITION_MAX	
CUSPARSE_HYB_PARTITION_USER	✓	HIPSPARSE_HYB_PARTITION_USER	
CUSPARSE_INDEX_BASE_ONE		HIPSPARSE_INDEX_BASE_ONE	
CUSPARSE_INDEX_BASE_ZERO		HIPSPARSE_INDEX_BASE_ZERO	
CUSPARSE_MATRIX_TYPE_GENERAL		HIPSPARSE_MATRIX_TYPE_GENERAL	
CUSPARSE_MATRIX_TYPE_HERMITIAN		HIPSPARSE_MATRIX_TYPE_HERMITIAN	
CUSPARSE_MATRIX_TYPE_SYMMETRIC		HIPSPARSE_MATRIX_TYPE_SYMMETRIC	
CUSPARSE_MATRIX_TYPE_TRIANGULAR		HIPSPARSE_MATRIX_TYPE_TRIANGULAR	
CUSPARSE_OPERATION_CONJUGATE_TRANSPOSE		HIPSPARSE_OPERATION_CONJUGATE_TRANSPOSE	
CUSPARSE_OPERATION_NON_TRANSPOSE		HIPSPARSE_OPERATION_NON_TRANSPOSE	
CUSPARSE_OPERATION_TRANSPOSE		HIPSPARSE_OPERATION_TRANSPOSE	
CUSPARSE_POINTER_MODE_DEVICE		HIPSPARSE_POINTER_MODE_DEVICE	
CUSPARSE_POINTER_MODE_HOST		HIPSPARSE_POINTER_MODE_HOST	
CUSPARSE_SOLVE_POLICY_NO_LEVEL		HIPSPARSE_SOLVE_POLICY_NO_LEVEL	
CUSPARSE_SOLVE_POLICY_USE_LEVEL		HIPSPARSE_SOLVE_POLICY_USE_LEVEL	
CUSPARSE_STATUS_ALLOC_FAILED		HIPSPARSE_STATUS_ALLOC_FAILED	
CUSPARSE_STATUS_ARCH_MISMATCH		HIPSPARSE_STATUS_ARCH_MISMATCH	
CUSPARSE_STATUS_EXECUTION_FAILED		HIPSPARSE_STATUS_EXECUTION_FAILED	
CUSPARSE_STATUS_INTERNAL_ERROR		HIPSPARSE_STATUS_INTERNAL_ERROR	
CUSPARSE_STATUS_INVALID_VALUE		HIPSPARSE_STATUS_INVALID_VALUE	

CUDA	Deprecated	HIP	Deprecated
CUSPARSE_STATUS_MAPPING_ERROR		HIPSPARSE_STATUS_MAPPING_ERROR	
CUSPARSE_STATUS_MATRIX_TYPE_NOT_SUPPORTED		HIPSPARSE_STATUS_MATRIX_TYPE_NOT_SUPPORTED	
CUSPARSE_STATUS_NOT_INITIALIZED		HIPSPARSE_STATUS_NOT_INITIALIZED	
CUSPARSE_STATUS_SUCCESS		HIPSPARSE_STATUS_SUCCESS	
CUSPARSE_STATUS_ZERO_PIVOT		HIPSPARSE_STATUS_ZERO_PIVOT	
bsric02Info_t		bsric02Info_t	
bsrilu02Info_t		bsrilu02Info_t	
bsrsv2Info_t		bsrsv2Info_t	
csrgemm2Info_t		csrgemm2Info_t	
csrilu02Info_t		csrilu02Info_t	
csrsm2Info_t		csrsm2Info_t	
csrsv2Info_t		csrsv2Info_t	
cusparseAction_t		hipsparseAction_t	
cusparseDiagType_t		hipsparseDiagType_t	
cusparseDirection_t		hipsparseDirection_t	
cusparseFillMode_t		hipsparseFillMode_t	
cusparseHandle_t		hipsparseHandle_t	
cusparseHybMat_t	✓	hipsparseHybMat_t	
cusparseHybPartition_t	✓	hipsparseHybPartition_t	
cusparseIndexBase_t		hipsparseIndexBase_t	
cusparseMatDescr_t		hipsparseMatDescr_t	
cusparseMatrixType_t		hipsparseMatrixType_t	
cusparseOperation_t		hipsparseOperation_t	
cusparsePointerMode_t		hipsparsePointerMode_t	
cusparseSolvePolicy_t		hipsparseSolvePolicy_t	
cusparseStatus_t		hipsparseStatus_t	
pruneInfo_t		pruneInfo_t	

1.8.2 CUSPARSE Management Function Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCreate		hipsparseCreate	
cusparseDestroy		hipsparseDestroy	
cusparseGetPointerMode		hipsparseGetPointerMode	
cusparseGetStream		hipsparseGetStream	
cusparseGetVersion		hipsparseGetVersion	
cusparseSetPointerMode		hipsparseSetPointerMode	
cusparseSetStream		hipsparseSetStream	

1.8.3 CUSPARSE Helper Function Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCreateBsrlic02Info		hipsparseCreateBsrlic02Info	
cusparseCreateBsrilu02Info		hipsparseCreateBsrilu02Info	
cusparseCreateBsrsv2Info		hipsparseCreateBsrsv2Info	
cusparseCreateCsrgemm2Info	✓	hipsparseCreateCsrgemm2Info	
cusparseCreateCsrlic02Info		hipsparseCreateCsrlic02Info	

CUDA	Deprecated	HIP	Deprecated
cusparseCreateCsrlu02Info		hipsparseCreateCsrlu02Info	
cusparseCreateCsrm2Info		hipsparseCreateCsrm2Info	
cusparseCreateCsrsv2Info		hipsparseCreateCsrsv2Info	
cusparseCreateHybMat	✓	hipsparseCreateHybMat	
cusparseCreateMatDescr		hipsparseCreateMatDescr	
cusparseCreatePruneInfo		hipsparseCreatePruneInfo	
cusparseDestroyBsrlic02Info		hipsparseDestroyBsrlic02Info	
cusparseDestroyCsrlu02Info		hipsparseDestroyCsrlu02Info	
cusparseDestroyCsrsv2Info		hipsparseDestroyCsrsv2Info	
cusparseDestroyCsrgermm2Info	✓	hipsparseDestroyCsrgermm2Info	
cusparseDestroyCsric02Info		hipsparseDestroyCsric02Info	
cusparseDestroyCsrlu02Info		hipsparseDestroyCsrlu02Info	
cusparseDestroyCsrm2Info		hipsparseDestroyCsrm2Info	
cusparseDestroyCsrsv2Info		hipsparseDestroyCsrsv2Info	
cusparseDestroyHybMat	✓	hipsparseDestroyHybMat	
cusparseDestroyMatDescr		hipsparseDestroyMatDescr	
cusparseDestroyPruneInfo		hipsparseDestroyPruneInfo	
cusparseGetMatDiagType		hipsparseGetMatDiagType	
cusparseGetMatFillMode		hipsparseGetMatFillMode	
cusparseGetMatIndexBase		hipsparseGetMatIndexBase	
cusparseGetMatType		hipsparseGetMatType	
cusparseSetMatDiagType		hipsparseSetMatDiagType	
cusparseSetMatFillMode		hipsparseSetMatFillMode	
cusparseSetMatIndexBase		hipsparseSetMatIndexBase	
cusparseSetMatType		hipsparseSetMatType	

1.8.4 CUSPARSE Level 1 Function Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCaxpyi	✓	hipsparseCaxpyi	
cusparseCdotci	✓	hipsparseCdotci	
cusparseCdoti	✓	hipsparseCdoti	
cusparseCgthr	✓	hipsparseCgthr	
cusparseCgthrz	✓	hipsparseCgthrz	
cusparseCsctr	✓	hipsparseCsctr	
cusparseDaxpyi	✓	hipsparseDaxpyi	
cusparseDdoti	✓	hipsparseDdoti	
cusparseDgthr	✓	hipsparseDgthr	
cusparseDgthrz	✓	hipsparseDgthrz	
cusparseDrotri	✓	hipsparseDrotri	
cusparseDsctr	✓	hipsparseDsctr	
cusparseSaxpyi	✓	hipsparseSaxpyi	
cusparseSdoti	✓	hipsparseSdoti	
cusparseSgthr	✓	hipsparseSgthr	
cusparseSgthrz	✓	hipsparseSgthrz	
cusparseSrotri	✓	hipsparseSrotri	
cusparseSsctr	✓	hipsparseSsctr	
cusparseZaxpyi	✓	hipsparseZaxpyi	

CUDA	Deprecated	HIP	Deprecated
cusparseZdotci	✓	hipsparseZdotci	
cusparseZdoti	✓	hipsparseZdoti	
cusparseZgthr	✓	hipsparseZgthr	
cusparseZgthrz	✓	hipsparseZgthrz	
cusparseZsctr	✓	hipsparseZsctr	

1.8.5 CUSPARSE Level 2 Function Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCbsrmv		hipsparseCbsrmv	
cusparseCbsrsv2_analysis		hipsparseCbsrsv2_analysis	
cusparseCbsrsv2_bufferSize		hipsparseCbsrsv2_bufferSize	
cusparseCbsrsv2_bufferSizeExt		hipsparseCbsrsv2_bufferSizeExt	
cusparseCbsrsv2_solve		hipsparseCbsrsv2_solve	
cusparseCcsrmv	✓	hipsparseCcsrmv	
cusparseCcsrsv2_analysis		hipsparseCcsrsv2_analysis	
cusparseCcsrsv2_bufferSize		hipsparseCcsrsv2_bufferSize	
cusparseCcsrsv2_bufferSizeExt		hipsparseCcsrsv2_bufferSizeExt	
cusparseCcsrsv2_solve		hipsparseCcsrsv2_solve	
cusparseChybm	✓	hipsparseChybm	
cusparseDbsrmv		hipsparseDbsrmv	
cusparseDbsrsv2_analysis		hipsparseDbsrsv2_analysis	
cusparseDbsrsv2_bufferSize		hipsparseDbsrsv2_bufferSize	
cusparseDbsrsv2_bufferSizeExt		hipsparseDbsrsv2_bufferSizeExt	
cusparseDbsrsv2_solve		hipsparseDbsrsv2_solve	
cusparseDcsrmv	✓	hipsparseDcsrmv	
cusparseDcsrsv2_analysis		hipsparseDcsrsv2_analysis	
cusparseDcsrsv2_bufferSize		hipsparseDcsrsv2_bufferSize	
cusparseDcsrsv2_bufferSizeExt		hipsparseDcsrsv2_bufferSizeExt	
cusparseDcsrsv2_solve		hipsparseDcsrsv2_solve	
cusparseDhybm	✓	hipsparseDhybm	
cusparseSbsrmv		hipsparseSbsrmv	
cusparseSbsrsv2_analysis		hipsparseSbsrsv2_analysis	
cusparseSbsrsv2_bufferSize		hipsparseSbsrsv2_bufferSize	
cusparseSbsrsv2_bufferSizeExt		hipsparseSbsrsv2_bufferSizeExt	
cusparseSbsrsv2_solve		hipsparseSbsrsv2_solve	
cusparseScsrmv	✓	hipsparseScsrmv	
cusparseScsrsv2_analysis		hipsparseScsrsv2_analysis	
cusparseScsrsv2_bufferSize		hipsparseScsrsv2_bufferSize	
cusparseScsrsv2_bufferSizeExt		hipsparseScsrsv2_bufferSizeExt	
cusparseScsrsv2_solve		hipsparseScsrsv2_solve	
cusparseShybm	✓	hipsparseShybm	
cusparseXbsrsv2_zeroPivot		hipsparseXbsrsv2_zeroPivot	
cusparseXcsrsv2_zeroPivot		hipsparseXcsrsv2_zeroPivot	
cusparseZbsrmv		hipsparseZbsrmv	
cusparseZbsrsv2_analysis		hipsparseZbsrsv2_analysis	
cusparseZbsrsv2_bufferSize		hipsparseZbsrsv2_bufferSize	
cusparseZbsrsv2_bufferSizeExt		hipsparseZbsrsv2_bufferSizeExt	
cusparseZbsrsv2_solve		hipsparseZbsrsv2_solve	
cusparseZcsrmv	✓	hipsparseZcsrmv	

CUDA	Deprecated	HIP	Deprecated
cusparseZcsrsv2_analysis		hipsparsZcsrsv2_analysis	
cusparseZcsrsv2_bufferSize		hipsparsZcsrsv2_bufferSize	
cusparseZcsrsv2_bufferSizeExt		hipsparsZcsrsv2_bufferSizeExt	
cusparseZcsrsv2_solve		hipsparsZcsrsv2_solve	
cusparseZhybm	✓	hipsparsZhybm	

1.8.6 CUSPARSE Level 3 Function Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCbsrmm		hipsparsCbsrmm	
cusparseCcsrmm	✓	hipsparsCcsrmm	
cusparseCcsrmm2	✓	hipsparsCcsrmm2	
cusparseCcsrsm2_analysis		hipsparsCcsrsm2_analysis	
cusparseCcsrsm2_bufferSizeExt		hipsparsCcsrsm2_bufferSizeExt	
cusparseCcsrsm2_solve		hipsparsCcsrsm2_solve	
cusparseCgemmi	✓	hipsparsCgemmi	
cusparseDbsrmm		hipsparsDbsrmm	
cusparseDcsrmm	✓	hipsparsDcsrmm	
cusparseDcsrmm2	✓	hipsparsDcsrmm2	
cusparseDcsrsm2_analysis		hipsparsDcsrsm2_analysis	
cusparseDcsrsm2_bufferSizeExt		hipsparsDcsrsm2_bufferSizeExt	
cusparseDcsrsm2_solve		hipsparsDcsrsm2_solve	
cusparseDgemmi	✓	hipsparsDgemmi	
cusparseSbsrmm		hipsparsSbsrmm	
cusparseScsrmm	✓	hipsparsScsrmm	
cusparseScsrmm2	✓	hipsparsScsrmm2	
cusparseScsrsm2_analysis		hipsparsScsrsm2_analysis	
cusparseScsrsm2_bufferSizeExt		hipsparsScsrsm2_bufferSizeExt	
cusparseScsrsm2_solve		hipsparsScsrsm2_solve	
cusparseSgemmi	✓	hipsparsSgemmi	
cusparseXcsrsm2_zeroPivot		hipsparsXcsrsm2_zeroPivot	
cusparseZbsrmm		hipsparsZbsrmm	
cusparseZcsrmm	✓	hipsparsZcsrmm	
cusparseZcsrmm2	✓	hipsparsZcsrmm2	
cusparseZcsrsm2_analysis		hipsparsZcsrsm2_analysis	
cusparseZcsrsm2_bufferSizeExt		hipsparsZcsrsm2_bufferSizeExt	
cusparseZcsrsm2_solve		hipsparsZcsrsm2_solve	
cusparseZgemmi	✓	hipsparsZgemmi	

1.8.7 CUSPARSE Extra Function Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCcsrgeam	✓	hipsparsCcsrgeam	
cusparseCcsrgeam2		hipsparsCcsrgeam2	
cusparseCcsrgeam2_bufferSizeExt		hipsparsCcsrgeam2_bufferSizeExt	
cusparseCcsrgeamm	✓	hipsparsCcsrgeamm	
cusparseCcsrgeamm2	✓	hipsparsCcsrgeamm2	

CUDA	Deprecated	HIP	Deprecated
cusparseCcsrgemm2_bufferSizeExt	✓	hipsparseCcsrgemm2_bufferSizeExt	
cusparseDcsrgeam	✓	hipsparseDcsrgeam	
cusparseDcsrgeam2		hipsparseDcsrgeam2	
cusparseDcsrgeam2_bufferSizeExt		hipsparseDcsrgeam2_bufferSizeExt	
cusparseDcsrgemm	✓	hipsparseDcsrgemm	
cusparseDcsrgemm2	✓	hipsparseDcsrgemm2	
cusparseDcsrgemm2_bufferSizeExt	✓	hipsparseDcsrgemm2_bufferSizeExt	
cusparseScsrgeam	✓	hipsparseScsrgeam	
cusparseScsrgeam2		hipsparseScsrgeam2	
cusparseScsrgeam2_bufferSizeExt		hipsparseScsrgeam2_bufferSizeExt	
cusparseScsrgemm	✓	hipsparseScsrgemm	
cusparseScsrgemm2	✓	hipsparseScsrgemm2	
cusparseScsrgemm2_bufferSizeExt	✓	hipsparseScsrgemm2_bufferSizeExt	
cusparseXcsrgeam2Nnz		hipsparseXcsrgeam2Nnz	
cusparseXcsrgeamNnz	✓	hipsparseXcsrgeamNnz	
cusparseXcsrgemm2Nnz	✓	hipsparseXcsrgemm2Nnz	
cusparseXcsrgemmNnz	✓	hipsparseXcsrgemmNnz	
cusparseZcsrgeam	✓	hipsparseZcsrgeam	
cusparseZcsrgeam2		hipsparseZcsrgeam2	
cusparseZcsrgeam2_bufferSizeExt		hipsparseZcsrgeam2_bufferSizeExt	
cusparseZcsrgemm	✓	hipsparseZcsrgemm	
cusparseZcsrgemm2	✓	hipsparseZcsrgemm2	
cusparseZcsrgemm2_bufferSizeExt	✓	hipsparseZcsrgemm2_bufferSizeExt	

1.8.8 CUSPARSE Preconditioners Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCbsric02		hipsparseCbsric02	
cusparseCbsric02_analysis		hipsparseCbsric02_analysis	
cusparseCbsric02_bufferSize		hipsparseCbsric02_bufferSize	
cusparseCbsrilu02		hipsparseCbsrilu02	
cusparseCbsrilu02_analysis		hipsparseCbsrilu02_analysis	
cusparseCbsrilu02_bufferSize		hipsparseCbsrilu02_bufferSize	
cusparseCbsrilu02_numericBoost		hipsparseCbsrilu02_numericBoost	
cusparseCcsric02		hipsparseCcsric02	
cusparseCcsric02_analysis		hipsparseCcsric02_analysis	
cusparseCcsric02_bufferSize		hipsparseCcsric02_bufferSize	
cusparseCcsric02_bufferSizeExt		hipsparseCcsric02_bufferSizeExt	
cusparseCcsrilu02		hipsparseCcsrilu02	
cusparseCcsrilu02_analysis		hipsparseCcsrilu02_analysis	
cusparseCcsrilu02_bufferSize		hipsparseCcsrilu02_bufferSize	
cusparseCcsrilu02_bufferSizeExt		hipsparseCcsrilu02_bufferSizeExt	
cusparseCcsrilu02_numericBoost		hipsparseCcsrilu02_numericBoost	
cusparseDbsric02		hipsparseDbsric02	
cusparseDbsric02_analysis		hipsparseDbsric02_analysis	
cusparseDbsric02_bufferSize		hipsparseDbsric02_bufferSize	
cusparseDbsrilu02		hipsparseDbsrilu02	
cusparseDbsrilu02_analysis		hipsparseDbsrilu02_analysis	
cusparseDbsrilu02_bufferSize		hipsparseDbsrilu02_bufferSize	
cusparseDbsrilu02_numericBoost		hipsparseDbsrilu02_numericBoost	

CUDA	Deprecated	HIP	Deprecated
cusparseDcsric02		hipsparsDcsric02	
cusparseDcsric02_analysis		hipsparsDcsric02_analysis	
cusparseDcsric02_bufferSize		hipsparsDcsric02_bufferSize	
cusparseDcsric02_bufferSizeExt		hipsparsDcsric02_bufferSizeExt	
cusparseDcsrilu02		hipsparsDcsrilu02	
cusparseDcsrilu02_analysis		hipsparsDcsrilu02_analysis	
cusparseDcsrilu02_bufferSize		hipsparsDcsrilu02_bufferSize	
cusparseDcsrilu02_bufferSizeExt		hipsparsDcsrilu02_bufferSizeExt	
cusparseDcsrilu02_numericBoost		hipsparsDcsrilu02_numericBoost	
cusparseSbsric02		hipsparsSbsric02	
cusparseSbsric02_analysis		hipsparsSbsric02_analysis	
cusparseSbsric02_bufferSize		hipsparsSbsric02_bufferSize	
cusparseSbsrilu02		hipsparsSbsrilu02	
cusparseSbsrilu02_analysis		hipsparsSbsrilu02_analysis	
cusparseSbsrilu02_bufferSize		hipsparsSbsrilu02_bufferSize	
cusparseSbsrilu02_numericBoost		hipsparsSbsrilu02_numericBoost	
cusparseScsric02		hipsparsScsric02	
cusparseScsric02_analysis		hipsparsScsric02_analysis	
cusparseScsric02_bufferSize		hipsparsScsric02_bufferSize	
cusparseScsric02_bufferSizeExt		hipsparsScsric02_bufferSizeExt	
cusparseScsrilu02		hipsparsScsrilu02	
cusparseScsrilu02_analysis		hipsparsScsrilu02_analysis	
cusparseScsrilu02_bufferSize		hipsparsScsrilu02_bufferSize	
cusparseScsrilu02_bufferSizeExt		hipsparsScsrilu02_bufferSizeExt	
cusparseScsrilu02_numericBoost		hipsparsScsrilu02_numericBoost	
cusparseXbsric02_zeroPivot		hipsparsXbsric02_zeroPivot	
cusparseXbsrilu02_zeroPivot		hipsparsXbsrilu02_zeroPivot	
cusparseXcsric02_zeroPivot		hipsparsXcsric02_zeroPivot	
cusparseXcsrilu02_zeroPivot		hipsparsXcsrilu02_zeroPivot	
cusparseZbsric02		hipsparsZbsric02	
cusparseZbsric02_analysis		hipsparsZbsric02_analysis	
cusparseZbsric02_bufferSize		hipsparsZbsric02_bufferSize	
cusparseZbsrilu02		hipsparsZbsrilu02	
cusparseZbsrilu02_analysis		hipsparsZbsrilu02_analysis	
cusparseZbsrilu02_bufferSize		hipsparsZbsrilu02_bufferSize	
cusparseZbsrilu02_numericBoost		hipsparsZbsrilu02_numericBoost	
cusparseZcsric02		hipsparsZcsric02	
cusparseZcsric02_analysis		hipsparsZcsric02_analysis	
cusparseZcsric02_bufferSize		hipsparsZcsric02_bufferSize	
cusparseZcsric02_bufferSizeExt		hipsparsZcsric02_bufferSizeExt	
cusparseZcsrilu02		hipsparsZcsrilu02	
cusparseZcsrilu02_analysis		hipsparsZcsrilu02_analysis	
cusparseZcsrilu02_bufferSize		hipsparsZcsrilu02_bufferSize	
cusparseZcsrilu02_bufferSizeExt		hipsparsZcsrilu02_bufferSizeExt	
cusparseZcsrilu02_numericBoost		hipsparsZcsrilu02_numericBoost	

1.8.9 CUSPARSE Format Conversion Reference

CUDA	Deprecated	HIP	Deprecated
cusparseCbsr2csr		hipsparseCbsr2csr	
cusparseCcsc2dense	✓	hipsparseCcsc2dense	
cusparseCcsr2bsr		hipsparseCcsr2bsr	
cusparseCcsr2csc	✓	hipsparseCcsr2csc	
cusparseCcsr2csr_compress		hipsparseCcsr2csr_compress	
cusparseCcsr2dense	✓	hipsparseCcsr2dense	
cusparseCcsr2hyb	✓	hipsparseCcsr2hyb	
cusparseCdense2csc	✓	hipsparseCdense2csc	
cusparseCdense2csr	✓	hipsparseCdense2csr	
cusparseChyb2csr	✓	hipsparseChyb2csr	
cusparseCnnz		hipsparseCnnz	
cusparseCnnz_compress		hipsparseCnnz_compress	
cusparseCreateIdentityPermutation		hipsparseCreateIdentityPermutation	
cusparseDbsr2csr		hipsparseDbsr2csr	
cusparseDcsc2dense	✓	hipsparseDcsc2dense	
cusparseDcsr2bsr		hipsparseDcsr2bsr	
cusparseDcsr2csc	✓	hipsparseDcsr2csc	
cusparseDcsr2csr_compress		hipsparseDcsr2csr_compress	
cusparseDcsr2dense	✓	hipsparseDcsr2dense	
cusparseDcsr2hyb	✓	hipsparseDcsr2hyb	
cusparseDdense2csc	✓	hipsparseDdense2csc	
cusparseDdense2csr	✓	hipsparseDdense2csr	
cusparseDhyb2csr	✓	hipsparseDhyb2csr	
cusparseDnnz		hipsparseDnnz	
cusparseDnnz_compress		hipsparseDnnz_compress	
cusparseDpruneCsr2csr		hipsparseDpruneCsr2csr	
cusparseDpruneCsr2csrByPercentage		hipsparseDpruneCsr2csrByPercentage	
cusparseDpruneCsr2csrByPercentage_bufferSizeExt		hipsparseDpruneCsr2csrByPercentage_bufferSizeExt	
cusparseDpruneCsr2csrNnz		hipsparseDpruneCsr2csrNnz	
cusparseDpruneCsr2csrNnzByPercentage		hipsparseDpruneCsr2csrNnzByPercentage	
cusparseDpruneCsr2csr_bufferSizeExt		hipsparseDpruneCsr2csr_bufferSizeExt	
cusparseDpruneDense2csr		hipsparseDpruneDense2csr	
cusparseDpruneDense2csrByPercentage		hipsparseDpruneDense2csrByPercentage	
cusparseDpruneDense2csrByPercentage_bufferSizeExt		hipsparseDpruneDense2csrByPercentage_bufferSizeExt	
cusparseDpruneDense2csrNnz		hipsparseDpruneDense2csrNnz	
cusparseDpruneDense2csrNnzByPercentage		hipsparseDpruneDense2csrNnzByPercentage	
cusparseDpruneDense2csr_bufferSizeExt		hipsparseDpruneDense2csr_bufferSizeExt	
cusparseSbsr2csr		hipsparseSbsr2csr	
cusparseScsc2dense	✓	hipsparseScsc2dense	
cusparseScsr2bsr		hipsparseScsr2bsr	
cusparseScsr2csc	✓	hipsparseScsr2csc	
cusparseScsr2csr_compress		hipsparseScsr2csr_compress	
cusparseScsr2dense	✓	hipsparseScsr2dense	

CUDA	Deprecated	HIP	Deprecated
cusparseScsr2hyb	✓	hipsparseScsr2hyb	
cusparseSdense2csc	✓	hipsparseSdense2csc	
cusparseSdense2csr	✓	hipsparseSdense2csr	
cusparseShyb2csr	✓	hipsparseShyb2csr	
cusparseSnnz		hipsparseSnnz	
cusparseSnnz_compress		hipsparseSnnz_compress	
cusparseSpruneCsr2csr		hipsparseSpruneCsr2csr	
cusparseSpruneCsr2csrByPercentage		hipsparseSpruneCsr2csrByPercentage	
cusparseSpruneCsr2csrByPercentage_bufferSizeExt		hipsparseSpruneCsr2csrByPercentage_bufferSizeExt	
cusparseSpruneCsr2csrNnz		hipsparseSpruneCsr2csrNnz	
cusparseSpruneCsr2csrNnzByPercentage		hipsparseSpruneCsr2csrNnzByPercentage	
cusparseSpruneCsr2csr_bufferSizeExt		hipsparseSpruneCsr2csr_bufferSizeExt	
cusparseSpruneDense2csr		hipsparseSpruneDense2csr	
cusparseSpruneDense2csrByPercentage		hipsparseSpruneDense2csrByPercentage	
cusparseSpruneDense2csrByPercentage_bufferSizeExt		hipsparseSpruneDense2csrByPercentage_bufferSizeExt	
cusparseSpruneDense2csrNnz		hipsparseSpruneDense2csrNnz	
cusparseSpruneDense2csrNnzByPercentage		hipsparseSpruneDense2csrNnzByPercentage	
cusparseSpruneDense2csr_bufferSizeExt		hipsparseSpruneDense2csr_bufferSizeExt	
cusparseXcoo2csr		hipsparseXcoo2csr	
cusparseXcoosortByColumn		hipsparseXcoosortByColumn	
cusparseXcoosortByRow		hipsparseXcoosortByRow	
cusparseXcoosort_bufferSizeExt		hipsparseXcoosort_bufferSizeExt	
cusparseXcscsort		hipsparseXcscsort	
cusparseXcscsort_bufferSizeExt		hipsparseXcscsort_bufferSizeExt	
cusparseXcsr2bsrNnz		hipsparseXcsr2bsrNnz	
cusparseXcsr2coo		hipsparseXcsr2coo	
cusparseXcsrsort		hipsparseXcsrsort	
cusparseXcsrsort_bufferSizeExt		hipsparseXcsrsort_bufferSizeExt	
cusparseZbsr2csr		hipsparseZbsr2csr	
cusparseZcsc2dense	✓	hipsparseZcsc2dense	
cusparseZcsr2bsr		hipsparseZcsr2bsr	
cusparseZcsr2csc	✓	hipsparseZcsr2csc	
cusparseZcsr2csr_compress		hipsparseZcsr2csr_compress	
cusparseZcsr2dense	✓	hipsparseZcsr2dense	
cusparseZcsr2hyb	✓	hipsparseZcsr2hyb	
cusparseZdense2csc	✓	hipsparseZdense2csc	
cusparseZdense2csr	✓	hipsparseZdense2csr	
cusparseZhyb2csr	✓	hipsparseZhyb2csr	
cusparseZnnz		hipsparseZnnz	
cusparseZnnz_compress		hipsparseZnnz_compress	

1.8.10 Unsupported APIs

API	Unsupported	Deprecated
CUSPARSE Reorderings Reference	✓	
CUSPARSE Generic API Reference	✓	

1.9 CUDNN API Supported by HIP

1.9.1 CUDNN Data Types

CUDA	Deprecated	HIP	Deprecated
CUDNN_16BIT_INDICES		HIPDNN_16BIT_INDICES	
CUDNN_32BIT_INDICES		HIPDNN_32BIT_INDICES	
CUDNN_64BIT_INDICES		HIPDNN_64BIT_INDICES	
CUDNN_8BIT_INDICES		HIPDNN_8BIT_INDICES	
CUDNN_ACTIVATION_CLIPPED_RELU		HIPDNN_ACTIVATION_CLIPPED_RELU	
CUDNN_ACTIVATION_ELU		HIPDNN_ACTIVATION_ELU	
CUDNN_ACTIVATION_IDENTITY		HIPDNN_ACTIVATION_PATHTRU	
CUDNN_ACTIVATION_RELU		HIPDNN_ACTIVATION_RELU	
CUDNN_ACTIVATION_SIGMOID		HIPDNN_ACTIVATION_SIGMOID	
CUDNN_ACTIVATION_TANH		HIPDNN_ACTIVATION_TANH	
CUDNN_BATCHNORM_PER_ACTIVATION		HIPDNN_BATCHNORM_PER_ACTIVATION	
CUDNN_BATCHNORM_SPATIAL		HIPDNN_BATCHNORM_SPATIAL	
CUDNN_BATCHNORM_SPATIAL_PERSISTENT		HIPDNN_BATCHNORM_SPATIAL_PERSISTENT	
CUDNN_BIDIRECTIONAL		HIPDNN_BIDIRECTIONAL	
CUDNN_BN_MIN_EPSILON		HIPDNN_BN_MIN_EPSILON	
CUDNN_CONVOLUTION		HIPDNN_CONVOLUTION	
CUDNN_CONVOLUTION_BWD_DATA_ALGO_0		HIPDNN_CONVOLUTION_BWD_DATA_ALGO_0	
CUDNN_CONVOLUTION_BWD_DATA_ALGO_1		HIPDNN_CONVOLUTION_BWD_DATA_ALGO_1	
CUDNN_CONVOLUTION_BWD_DATA_ALGO_COUNT		HIPDNN_CONVOLUTION_BWD_DATA_ALGO_TRANSPOSE_GEMM	
CUDNN_CONVOLUTION_BWD_DATA_ALGO_FFT		HIPDNN_CONVOLUTION_BWD_DATA_ALGO_FFT	
CUDNN_CONVOLUTION_BWD_DATA_ALGO_FFT_TILING		HIPDNN_CONVOLUTION_BWD_DATA_ALGO_FFT_TILING	
CUDNN_CONVOLUTION_BWD_DATA_ALGO_WINOGRAD		HIPDNN_CONVOLUTION_BWD_DATA_ALGO_WINOGRAD	
CUDNN_CONVOLUTION_BWD_DATA_ALGO_WINOGRAD_NONFUSED		HIPDNN_CONVOLUTION_BWD_DATA_ALGO_WINOGRAD_NONFUSED	
CUDNN_CONVOLUTION_BWD_DATA_NO_WORKSPACE	✓	HIPDNN_CONVOLUTION_BWD_DATA_NO_WORKSPACE	
CUDNN_CONVOLUTION_BWD_DATA_PREFER_FASTEST	✓	HIPDNN_CONVOLUTION_BWD_DATA_PREFER_FASTEST	
CUDNN_CONVOLUTION_BWD_DATA_SPECIFY_WORKSPACE_LIMIT	✓	HIPDNN_CONVOLUTION_BWD_DATA_SPECIFY_WORKSPACE_LIMIT	

CUDA	Deprecated	HIP	Deprecated
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_0		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_0	
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_1		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_1	
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_3		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_3	
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_COUNT		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_COUNT	
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_FFT		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_FFT	
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_FFT_TILING		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_FFT_TILING	
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_WINOGRA		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_WINOGRA	
CUDNN_CONVOLUTION_BWD_FILTER_ALGO_WINOGRA_NONFUSED		HIPDNN_CONVOLUTION_BWD_FILTER_ALGO_WINOGRA_NONFUSED	
CUDNN_CONVOLUTION_BWD_FILTER_NO_WORKSPACE	✓	HIPDNN_CONVOLUTION_BWD_FILTER_NO_WORKSPACE	
CUDNN_CONVOLUTION_BWD_FILTER_PREFER_FASTEST	✓	HIPDNN_CONVOLUTION_BWD_FILTER_PREFER_FASTEST	
CUDNN_CONVOLUTION_BWD_FILTER_SPECIFY_WORKSPACE_LIMIT	✓	HIPDNN_CONVOLUTION_BWD_FILTER_SPECIFY_WORKSPACE_LIMIT	
CUDNN_CONVOLUTION_FWD_ALGO_COUNT		HIPDNN_CONVOLUTION_FWD_ALGO_COUNT	
CUDNN_CONVOLUTION_FWD_ALGO_DIRECT		HIPDNN_CONVOLUTION_FWD_ALGO_DIRECT	
CUDNN_CONVOLUTION_FWD_ALGO_FFT		HIPDNN_CONVOLUTION_FWD_ALGO_FFT	
CUDNN_CONVOLUTION_FWD_ALGO_FFT_TILING		HIPDNN_CONVOLUTION_FWD_ALGO_FFT_TILING	
CUDNN_CONVOLUTION_FWD_ALGO_GEMM		HIPDNN_CONVOLUTION_FWD_ALGO_GEMM	
CUDNN_CONVOLUTION_FWD_ALGO_IMPLICIT_GEMM		HIPDNN_CONVOLUTION_FWD_ALGO_IMPLICIT_GEMM	
CUDNN_CONVOLUTION_FWD_ALGO_IMPLICIT_PRECOMP_GEMM		HIPDNN_CONVOLUTION_FWD_ALGO_IMPLICIT_PRECOMP_GEMM	
CUDNN_CONVOLUTION_FWD_ALGO_WINOGRA		HIPDNN_CONVOLUTION_FWD_ALGO_WINOGRA	
CUDNN_CONVOLUTION_FWD_ALGO_WINOGRA_NONFUSED		HIPDNN_CONVOLUTION_FWD_ALGO_WINOGRA_NONFUSED	
CUDNN_CONVOLUTION_FWD_NO_WORKSPACE	✓	HIPDNN_CONVOLUTION_FWD_NO_WORKSPACE	
CUDNN_CONVOLUTION_FWD_PREFER_FASTEST	✓	HIPDNN_CONVOLUTION_FWD_PREFER_FASTEST	
CUDNN_CONVOLUTION_FWD_SPECIFY_WORKSPACE_LIMIT	✓	HIPDNN_CONVOLUTION_FWD_SPECIFY_WORKSPACE_LIMIT	
CUDNN_CROSS_CORRELATION		HIPDNN_CROSS_CORRELATION	
CUDNN_DATA_DOUBLE		HIPDNN_DATA_DOUBLE	
CUDNN_DATA_FLOAT		HIPDNN_DATA_FLOAT	
CUDNN_DATA_HALF		HIPDNN_DATA_HALF	
CUDNN_DATA_INT32		HIPDNN_DATA_INT32	
CUDNN_DATA_INT8		HIPDNN_DATA_INT8	

CUDA	Deprecated	HIP	Deprecated
CUDNN_DATA_INT8x4		HIPDNN_DATA_INT8x4	
CUDNN_DEFAULT_MATH		HIPDNN_DEFAULT_MATH	
CUDNN_GRU		HIPDNN_GRU	
CUDNN_LINEAR_INPUT		HIPDNN_LINEAR_INPUT	
CUDNN_LRN_CROSS_CHANNEL_DIM1		HIPDNN_LRN_CROSS_CHANNEL	
CUDNN_LSTM		HIPDNN_LSTM	
CUDNN_NOT_PROPAGATE_NAN		HIPDNN_NOT_PROPAGATE_NAN	
CUDNN_OP_TENSOR_ADD		HIPDNN_OP_TENSOR_ADD	
CUDNN_OP_TENSOR_MAX		HIPDNN_OP_TENSOR_MAX	
CUDNN_OP_TENSOR_MIN		HIPDNN_OP_TENSOR_MIN	
CUDNN_OP_TENSOR_MUL		HIPDNN_OP_TENSOR_MUL	
CUDNN_OP_TENSOR_SQRT		HIPDNN_OP_TENSOR_SQRT	
CUDNN_POOLING_AVERAGE_COUNT_EXCLUDE_PADDING		HIPDNN_POOLING_AVERAGE_COUNT_EXCLUDE_PADDING	
CUDNN_POOLING_AVERAGE_COUNT_INCLUDE_PADDING		HIPDNN_POOLING_AVERAGE_COUNT_INCLUDE_PADDING	
CUDNN_POOLING_MAX		HIPDNN_POOLING_MAX	
CUDNN_POOLING_MAX_DETERMINISTIC		HIPDNN_POOLING_MAX_DETERMINISTIC	
CUDNN_PROPAGATE_NAN		HIPDNN_PROPAGATE_NAN	
CUDNN_REDUCE_TENSOR_ADD		HIPDNN_REDUCE_TENSOR_ADD	
CUDNN_REDUCE_TENSOR_AMAX		HIPDNN_REDUCE_TENSOR_AMAX	
CUDNN_REDUCE_TENSOR_AVG		HIPDNN_REDUCE_TENSOR_AVG	
CUDNN_REDUCE_TENSOR_FLATTENED_INDICES		HIPDNN_REDUCE_TENSOR_FLATTENED_INDICES	
CUDNN_REDUCE_TENSOR_MAX		HIPDNN_REDUCE_TENSOR_MAX	
CUDNN_REDUCE_TENSOR_MIN		HIPDNN_REDUCE_TENSOR_MIN	
CUDNN_REDUCE_TENSOR_MUL		HIPDNN_REDUCE_TENSOR_MUL	
CUDNN_REDUCE_TENSOR_MUL_NO_ZEROS		HIPDNN_REDUCE_TENSOR_MUL_NO_ZEROS	
CUDNN_REDUCE_TENSOR_NORM1		HIPDNN_REDUCE_TENSOR_NORM1	
CUDNN_REDUCE_TENSOR_NORM2		HIPDNN_REDUCE_TENSOR_NORM2	
CUDNN_REDUCE_TENSOR_NO_INDICES		HIPDNN_REDUCE_TENSOR_NO_INDICES	
CUDNN_RNN_ALGO_PERSIST_DYNAMIC		HIPDNN_RNN_ALGO_PERSIST_DYNAMIC	
CUDNN_RNN_ALGO_PERSIST_STATIC		HIPDNN_RNN_ALGO_PERSIST_STATIC	
CUDNN_RNN_ALGO_PERSIST_STATIC_SMALL_H		HIPDNN_RNN_ALGO_PERSIST_STATIC_SMALL_H	
CUDNN_RNN_ALGO_STANDARD		HIPDNN_RNN_ALGO_STANDARD	
CUDNN_RNN_DOUBLE_BIAS		HIPDNN_RNN_WITH_BIAS	
CUDNN_RNN_NO_BIAS		HIPDNN_RNN_NO_BIAS	
CUDNN_RNN_RELU		HIPDNN_RNN_RELU	
CUDNN_RNN_SINGLE_INP_BIAS		HIPDNN_RNN_WITH_BIAS	
CUDNN_RNN_SINGLE_REC_BIAS		HIPDNN_RNN_WITH_BIAS	
CUDNN_RNN_TANH		HIPDNN_RNN_TANH	
CUDNN_SKIP_INPUT		HIPDNN_SKIP_INPUT	
CUDNN_SOFTMAX_ACCURATE		HIPDNN_SOFTMAX_ACCURATE	
CUDNN_SOFTMAX_FAST		HIPDNN_SOFTMAX_FAST	
CUDNN_SOFTMAX_LOG		HIPDNN_SOFTMAX_LOG	

CUDA	Deprecated	HIP	Deprecated
CUDNN_SOFTMAX_MODE_CHANNEL		HIPDNN_SOFTMAX_MODE_CHANNEL	
CUDNN_SOFTMAX_MODE_INSTANCE		HIPDNN_SOFTMAX_MODE_INSTANCE	
CUDNN_STATUS_ALLOC_FAILED		HIPDNN_STATUS_ALLOC_FAILED	
CUDNN_STATUS_ARCH_MISMATCH		HIPDNN_STATUS_ARCH_MISMATCH	
CUDNN_STATUS_BAD_PARAM		HIPDNN_STATUS_BAD_PARAM	
CUDNN_STATUS_EXECUTION_FAILED		HIPDNN_STATUS_EXECUTION_FAILED	
CUDNN_STATUS_INTERNAL_ERROR		HIPDNN_STATUS_INTERNAL_ERROR	
CUDNN_STATUS_INVALID_VALUE		HIPDNN_STATUS_INVALID_VALUE	
CUDNN_STATUS_LICENSE_ERROR		HIPDNN_STATUS_LICENSE_ERROR	
CUDNN_STATUS_MAPPING_ERROR		HIPDNN_STATUS_MAPPING_ERROR	
CUDNN_STATUS_NOT_INITIALIZED		HIPDNN_STATUS_NOT_INITIALIZED	
CUDNN_STATUS_NOT_SUPPORTED		HIPDNN_STATUS_NOT_SUPPORTED	
CUDNN_STATUS_RUNTIME_PREREQUISITE_MISSING		HIPDNN_STATUS_RUNTIME_PREREQUISITE_MISSING	
CUDNN_STATUS_SUCCESS		HIPDNN_STATUS_SUCCESS	
CUDNN_TENSOR_NCHW		HIPDNN_TENSOR_NCHW	
CUDNN_TENSOR_NCHW_VECT_C		HIPDNN_TENSOR_NCHW_VECT_C	
CUDNN_TENSOR_NHWC		HIPDNN_TENSOR_NHWC	
CUDNN_TENSOR_OP_MATH		HIPDNN_TENSOR_OP_MATH	
CUDNN_UNIDIRECTIONAL		HIPDNN_UNIDIRECTIONAL	
CUDNN_VERSION		HIPDNN_VERSION	
cudnnActivationDescriptor_t		hipdnnActivationDescriptor_t	
cudnnActivationMode_t		hipdnnActivationMode_t	
cudnnBatchNormMode_t		hipdnnBatchNormMode_t	
cudnnConvolutionBwdDataAlgoPerf_t		hipdnnConvolutionBwdDataAlgoPerf_t	
cudnnConvolutionBwdDataAlgo_t		hipdnnConvolutionBwdDataAlgo_t	
cudnnConvolutionBwdDataPreference_t	✓	hipdnnConvolutionBwdDataPreference_t	
cudnnConvolutionBwdFilterAlgoPerf_t		hipdnnConvolutionBwdFilterAlgoPerf_t	
cudnnConvolutionBwdFilterAlgo_t		hipdnnConvolutionBwdFilterAlgo_t	
cudnnConvolutionBwdFilterPreference_t	✓	hipdnnConvolutionBwdFilterPreference_t	
cudnnConvolutionDescriptor_t		hipdnnConvolutionDescriptor_t	
cudnnConvolutionFwdAlgoPerf_t		hipdnnConvolutionFwdAlgoPerf_t	
cudnnConvolutionFwdAlgo_t		hipdnnConvolutionFwdAlgo_t	
cudnnConvolutionFwdPreference_t	✓	hipdnnConvolutionFwdPreference_t	
cudnnConvolutionMode_t		hipdnnConvolutionMode_t	
cudnnDataType_t		hipdnnDataType_t	
cudnnDirectionMode_t		hipdnnDirectionMode_t	
cudnnDropoutDescriptor_t		hipdnnDropoutDescriptor_t	
cudnnFilterDescriptor_t		hipdnnFilterDescriptor_t	
cudnnHandle_t		hipdnnHandle_t	
cudnnIndicesType_t		hipdnnIndicesType_t	
cudnnLRNDescriptor_t		hipdnnLRNDescriptor_t	
cudnnLRNMode_t		hipdnnLRNMode_t	

CUDA	Deprecated	HIP	Deprecated
cudaMathType_t		hipdnnMathType_t	
cudaNanPropagation_t		hipdnnNanPropagation_t	
cudaOpTensorDescriptor_t		hipdnnOpTensorDescriptor_t	
cudaOpTensorOp_t		hipdnnOpTensorOp_t	
cudaPersistentRNNPlan_t		hipdnnPersistentRNNPlan_t	
cudaPoolingDescriptor_t		hipdnnPoolingDescriptor_t	
cudaPoolingMode_t		hipdnnPoolingMode_t	
cudaRNNAlgo_t		hipdnnRNNAlgo_t	
cudaRNNBiasMode_t		hipdnnRNNBiasMode_t	
cudaRNNDescriptor_t		hipdnnRNNDescriptor_t	
cudaRNNInputMode_t		hipdnnRNNInputMode_t	
cudaRNNMode_t		hipdnnRNNMode_t	
cudaReduceTensorDescriptor_t		hipdnnReduceTensorDescriptor_t	
cudaReduceTensorIndices_t		hipdnnReduceTensorIndices_t	
cudaReduceTensorOp_t		hipdnnReduceTensorOp_t	
cudaSoftmaxAlgorithm_t		hipdnnSoftmaxAlgorithm_t	
cudaSoftmaxMode_t		hipdnnSoftmaxMode_t	
cudaStatus_t		hipdnnStatus_t	
cudaTensorDescriptor_t		hipdnnTensorDescriptor_t	
cudaTensorFormat_t		hipdnnTensorFormat_t	

1.9.2 CUDNN Functions

CUDA	Deprecated	HIP	Deprecated
cudaActivationBackward		hipdnnActivationBackward	
cudaActivationForward		hipdnnActivationForward	
cudaAddTensor		hipdnnAddTensor	
cudaBatchNormalizationBackward		hipdnnBatchNormalizationBackward	
cudaBatchNormalizationForwardInference		hipdnnBatchNormalizationForwardInference	
cudaBatchNormalizationForwardTraining		hipdnnBatchNormalizationForwardTraining	
cudaConvolutionBackwardBias		hipdnnConvolutionBackwardBias	
cudaConvolutionBackwardData		hipdnnConvolutionBackwardData	
cudaConvolutionBackwardFilter		hipdnnConvolutionBackwardFilter	
cudaConvolutionForward		hipdnnConvolutionForward	
cudaCreate		hipdnnCreate	
cudaCreateActivationDescriptor		hipdnnCreateActivationDescriptor	
cudaCreateConvolutionDescriptor		hipdnnCreateConvolutionDescriptor	
cudaCreateDropoutDescriptor		hipdnnCreateDropoutDescriptor	
cudaCreateFilterDescriptor		hipdnnCreateFilterDescriptor	
cudaCreateLRNDescriptor		hipdnnCreateLRNDescriptor	
cudaCreateOpTensorDescriptor		hipdnnCreateOpTensorDescriptor	
cudaCreatePersistentRNNPlan	✓	hipdnnCreatePersistentRNNPlan	
cudaCreatePoolingDescriptor		hipdnnCreatePoolingDescriptor	
cudaCreateRNNDescriptor		hipdnnCreateRNNDescriptor	
cudaCreateReduceTensorDescriptor		hipdnnCreateReduceTensorDescriptor	
cudaCreateTensorDescriptor		hipdnnCreateTensorDescriptor	
cudaDeriveBNTensorDescriptor		hipdnnDeriveBNTensorDescriptor	
cudaDestroy		hipdnnDestroy	
cudaDestroyActivationDescriptor		hipdnnDestroyActivationDescriptor	
cudaDestroyConvolutionDescriptor		hipdnnDestroyConvolutionDescriptor	

CUDA	Deprecated	HIP	Deprecated
cudaDestroyDropoutDescriptor		hipDnnDestroyDropoutDescriptor	
cudaDestroyFilterDescriptor		hipDnnDestroyFilterDescriptor	
cudaDestroyLRNDescriptor		hipDnnDestroyLRNDescriptor	
cudaDestroyOpTensorDescriptor		hipDnnDestroyOpTensorDescriptor	
cudaDestroyPersistentRNNPlan	✓	hipDnnDestroyPersistentRNNPlan	
cudaDestroyPoolingDescriptor		hipDnnDestroyPoolingDescriptor	
cudaDestroyRNNDescriptor		hipDnnDestroyRNNDescriptor	
cudaDestroyReduceTensorDescriptor		hipDnnDestroyReduceTensorDescriptor	
cudaDestroyTensorDescriptor		hipDnnDestroyTensorDescriptor	
cudaDropoutGetStatesSize		hipDnnDropoutGetStatesSize	
cudaFindConvolutionBackwardDataAlgorithm		hipDnnFindConvolutionBackwardDataAlgorithm	
cudaFindConvolutionBackwardDataAlgorithmEx		hipDnnFindConvolutionBackwardDataAlgorithmEx	
cudaFindConvolutionBackwardFilterAlgorithm		hipDnnFindConvolutionBackwardFilterAlgorithm	
cudaFindConvolutionBackwardFilterAlgorithmEx		hipDnnFindConvolutionBackwardFilterAlgorithmEx	
cudaFindConvolutionForwardAlgorithm		hipDnnFindConvolutionForwardAlgorithm	
cudaFindConvolutionForwardAlgorithmEx		hipDnnFindConvolutionForwardAlgorithmEx	
cudaGetActivationDescriptor		hipDnnGetActivationDescriptor	
cudaGetConvolution2dDescriptor		hipDnnGetConvolution2dDescriptor	
cudaGetConvolution2dForwardOutputDim		hipDnnGetConvolution2dForwardOutputDim	
cudaGetConvolutionBackwardDataAlgorithm	✓	hipDnnGetConvolutionBackwardDataAlgorithm	
cudaGetConvolutionBackwardDataWorkspaceSize		hipDnnGetConvolutionBackwardDataWorkspaceSize	
cudaGetConvolutionBackwardFilterAlgorithm	✓	hipDnnGetConvolutionBackwardFilterAlgorithm	
cudaGetConvolutionBackwardFilterWorkspaceSize		hipDnnGetConvolutionBackwardFilterWorkspaceSize	
cudaGetConvolutionForwardAlgorithm	✓	hipDnnGetConvolutionForwardAlgorithm	
cudaGetConvolutionForwardWorkspaceSize		hipDnnGetConvolutionForwardWorkspaceSize	
cudaGetErrorString		hipDnnGetErrorString	
cudaGetFilter4dDescriptor		hipDnnGetFilter4dDescriptor	
cudaGetFilterNdDescriptor		hipDnnGetFilterNdDescriptor	
cudaGetLRNDescriptor		hipDnnGetLRNDescriptor	
cudaGetOpTensorDescriptor		hipDnnGetOpTensorDescriptor	
cudaGetPooling2dDescriptor		hipDnnGetPooling2dDescriptor	
cudaGetPooling2dForwardOutputDim		hipDnnGetPooling2dForwardOutputDim	
cudaGetRNNDescriptor	✓	hipDnnGetRNNDescriptor	
cudaGetRNNLinLayerBiasParams	✓	hipDnnGetRNNLinLayerBiasParams	
cudaGetRNNLinLayerMatrixParams	✓	hipDnnGetRNNLinLayerMatrixParams	
cudaGetRNNParamsSize	✓	hipDnnGetRNNParamsSize	
cudaGetRNNTrainingReserveSize	✓	hipDnnGetRNNTrainingReserveSize	
cudaGetRNNWorkspaceSize	✓	hipDnnGetRNNWorkspaceSize	

CUDA	Deprecated	HIP	Deprecated
cudaGetReduceTensorDescriptor		hipdnnGetReduceTensorDescriptor	
cudaGetReductionWorkspaceSize		hipdnnGetReductionWorkspaceSize	
cudaGetStream		hipdnnGetStream	
cudaGetTensor4dDescriptor		hipdnnGetTensor4dDescriptor	
cudaGetTensorNdDescriptor		hipdnnGetTensorNdDescriptor	
cudaGetVersion		hipdnnGetVersion	
cudaLRNCrossChannelBackward		hipdnnLRNCrossChannelBackward	
cudaLRNCrossChannelForward		hipdnnLRNCrossChannelForward	
cudaOpTensor		hipdnnOpTensor	
cudaPoolingBackward		hipdnnPoolingBackward	
cudaPoolingForward		hipdnnPoolingForward	
cudaRNNBackwardData	✓	hipdnnRNNBackwardData	
cudaRNNBackwardWeights	✓	hipdnnRNNBackwardWeights	
cudaRNNForwardInference	✓	hipdnnRNNForwardInference	
cudaRNNForwardTraining	✓	hipdnnRNNForwardTraining	
cudaReduceTensor		hipdnnReduceTensor	
cudaScaleTensor		hipdnnScaleTensor	
cudaSetActivationDescriptor		hipdnnSetActivationDescriptor	
cudaSetConvolution2dDescriptor		hipdnnSetConvolution2dDescriptor	
cudaSetConvolutionGroupCount		hipdnnSetConvolutionGroupCount	
cudaSetConvolutionMathType		hipdnnSetConvolutionMathType	
cudaSetConvolutionNdDescriptor		hipdnnSetConvolutionNdDescriptor	
cudaSetDropoutDescriptor		hipdnnSetDropoutDescriptor	
cudaSetFilter4dDescriptor		hipdnnSetFilter4dDescriptor	
cudaSetFilterNdDescriptor		hipdnnSetFilterNdDescriptor	
cudaSetLRNDescriptor		hipdnnSetLRNDescriptor	
cudaSetOpTensorDescriptor		hipdnnSetOpTensorDescriptor	
cudaSetPersistentRNNPlan	✓	hipdnnSetPersistentRNNPlan	
cudaSetPooling2dDescriptor		hipdnnSetPooling2dDescriptor	
cudaSetPoolingNdDescriptor		hipdnnSetPoolingNdDescriptor	
cudaSetRNNDescriptor	✓	hipdnnSetRNNDescriptor	
cudaSetRNNDescriptor_v5	✓	hipdnnSetRNNDescriptor_v5	
cudaSetRNNDescriptor_v6	✓	hipdnnSetRNNDescriptor_v6	
cudaSetReduceTensorDescriptor		hipdnnSetReduceTensorDescriptor	
cudaSetStream		hipdnnSetStream	
cudaSetTensor		hipdnnSetTensor	
cudaSetTensor4dDescriptor		hipdnnSetTensor4dDescriptor	
cudaSetTensor4dDescriptorEx		hipdnnSetTensor4dDescriptorEx	
cudaSetTensorNdDescriptor		hipdnnSetTensorNdDescriptor	
cudaSoftmaxBackward		hipdnnSoftmaxBackward	
cudaSoftmaxForward		hipdnnSoftmaxForward	

1.10 Stream Memory Operations

CUDA	Deprecaded	HIP	Deprecaded
cuStreamBatchMemOp			
cuStreamWaitValue32		hipStreamWaitValue32	
cuStreamWaitValue64		hipStreamWaitValue64	
cuStreamWriteValue32		hipStreamWriteValue32	
cuStreamWriteValue64		hipStreamWriteValue64	