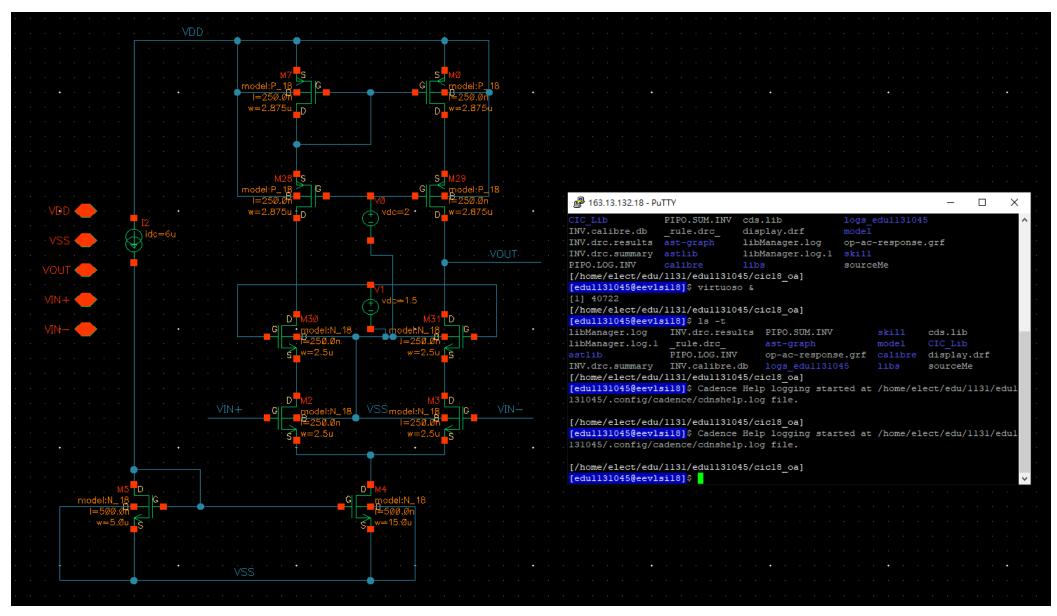
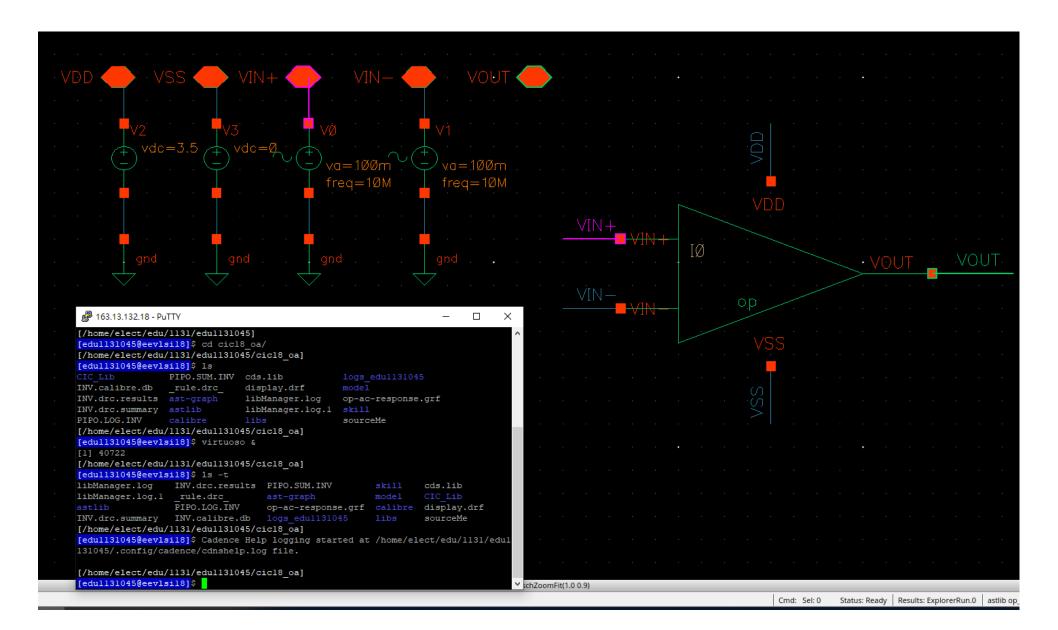
# Opamp模擬

411440117

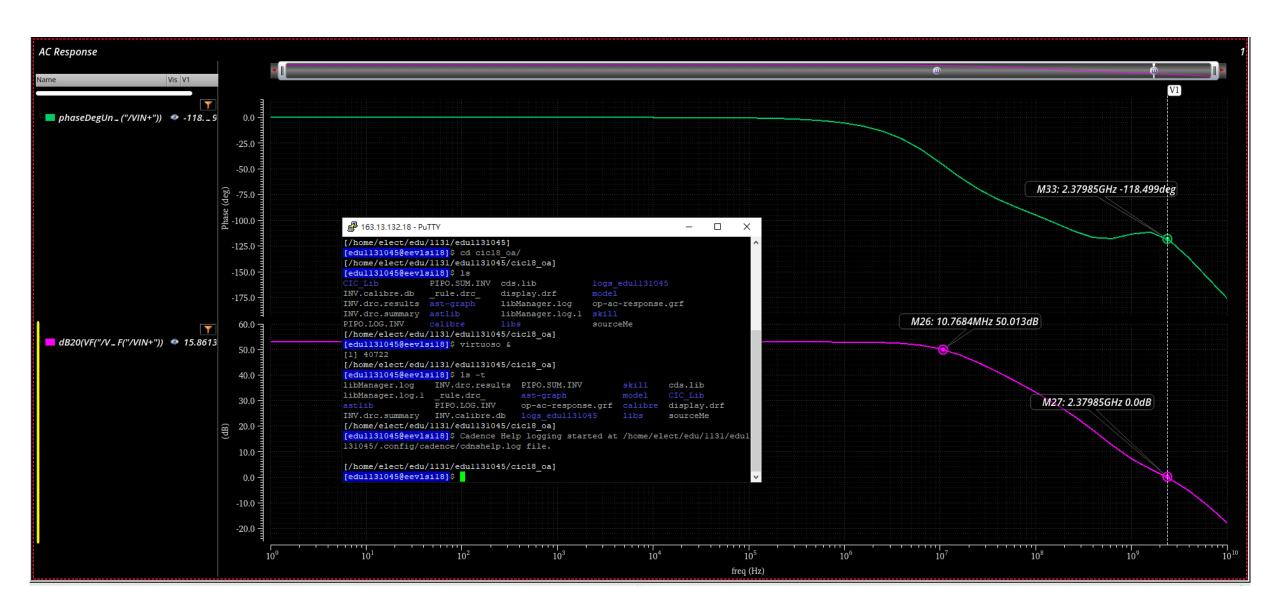
### Schematic



#### Test Bench



### 波德圖



# 功耗

Results Display Wir	ndow@eevlsi18				_		$\times$			
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pwr	-86.67u									
ľv	3.5									
_										7
₱ 163.13.132.18 - Pul	ΓTY					_			×	
[/home/elect/edu/	1131/edul13104	15]								
[edul131045@eevls	il8]\$ cd cicl8	oa,	/							
[/home/elect/edu/	1131/edul13104	5/ci	ic18_oa]							
[edul131045@eevls	i18]\$ ls									
CIC_Lib	PIPO.SUM.INV	cds.	.lib	logs_	edul13104	5				
INV.calibre.db	_rule.drc_	display.drf		model	del					
INV.drc.results				op-ac-response.grf						
INV.drc.summary	astlib	libManager.log.l		skill						
	calibre	libs		sourc	eMe					
[/home/elect/edu/1131/edul131045/cicl8_oa]										
[edul131045@eevlsi18]\$ virtuoso &										
[1] 40722										
[/home/elect/edu/1131/edu1131045/cic18_oa]										
[edul131045@eevls										
libManager.log					skill	cds.l				
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astlib			op-ac-respons			displa	_	Ē		
INV.drc.summary			logs_edul1310	45	libs	source	eMe			
[/home/elect/edu/1131/edul131045/cicl8_oa]										
[edull31045@eevlsi18]\$ Cadence Help logging started at /home/elect/edu/ll31/edul 131045/.config/cadence/cdnshelp.log file.										
131045/.config/ca	dence/cdnshelp	0.100	g file.							
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[edul131045@eevls										,
47 HelpAction										

# 規格表

	規格	This work
增益	≥40dB	50db
頻寬	≥10M Hz	10.7M Hz
Phase Margin	>60 degree	61.5 degree
功耗	<100u W	86.67u W