

Meeting 9

What I did

- Skimmed the C codebase
 - It's essentially identical to the pseudo-code
 - Not very important to hardware implementation, I think.
 - Will be useful for data verification.
- Read the CRYSTALS-Kyber math paper
 - I tried but the math proofs are a bit too hard
- Read some hardware implementation of CRYSTALS-Kyber
 - Not finished

About the CRYSTALS-Kyber paper...

CRYSTALS – Kyber: a CCA-secure module-lattice-based KEM

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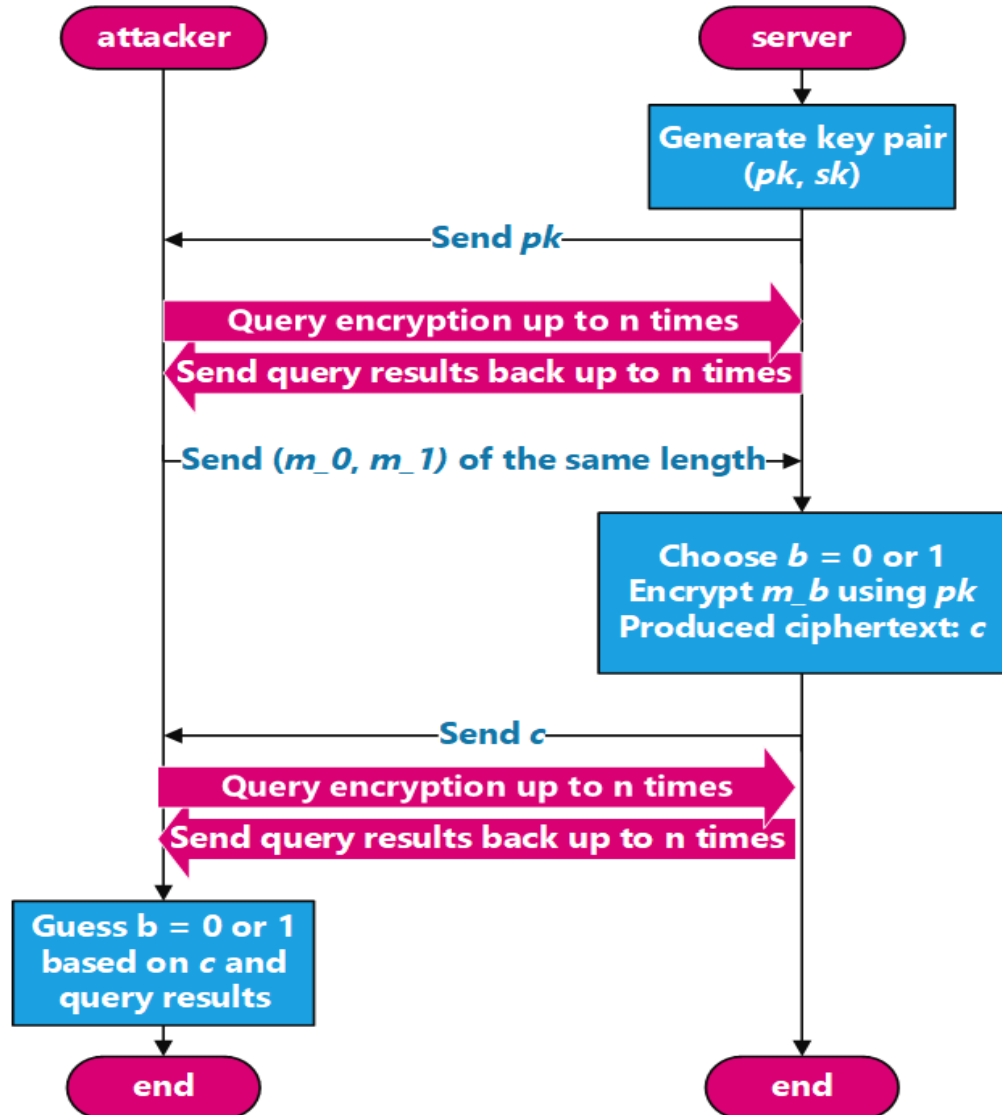
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IND-CPA (In-distinguish-ability under Chosen Plaintext Attack)



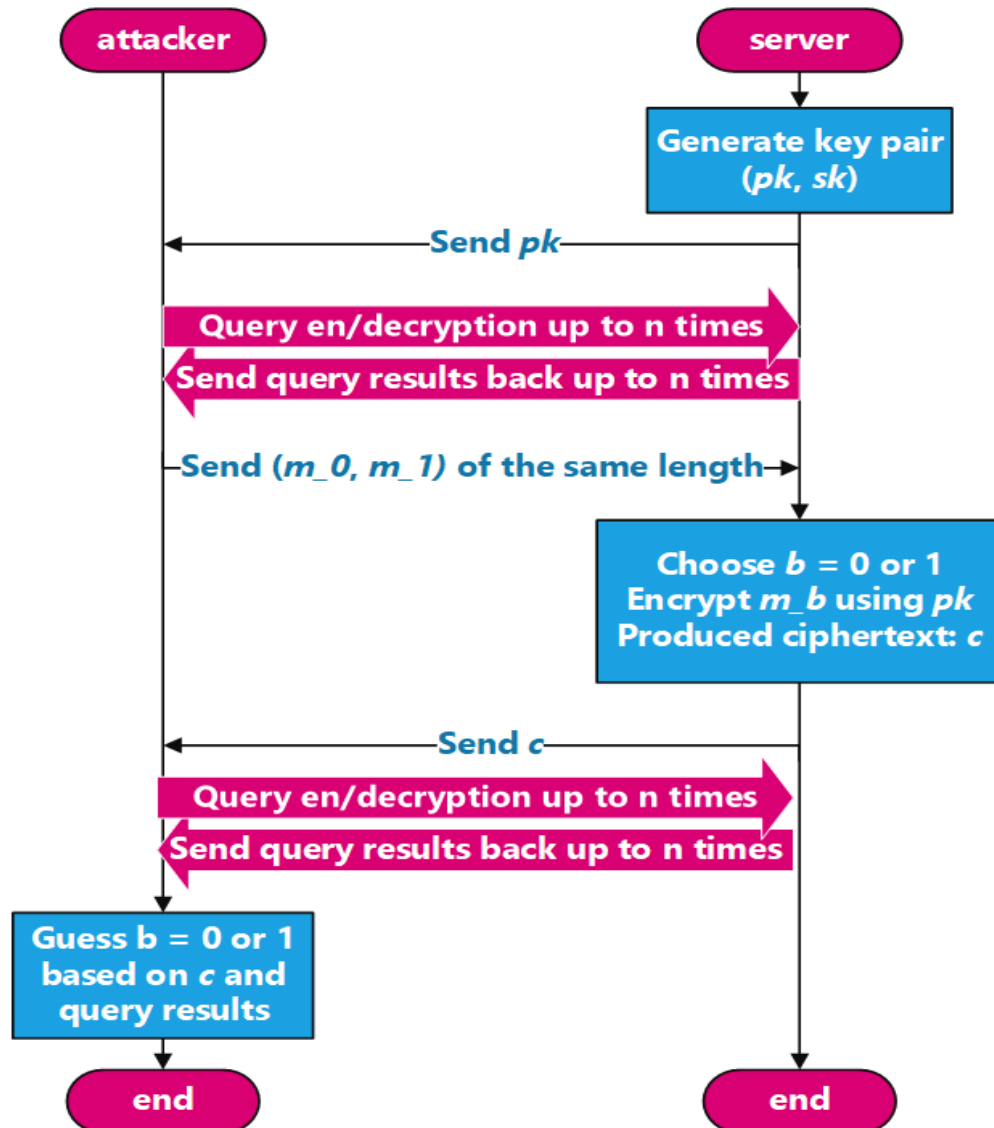
$\text{Adv}_{\text{PKE}}^{\text{cpa}}(A)$

$$\Pr \left[b = b' : \begin{array}{l} (pk, sk) \leftarrow \text{KeyGen}(); \\ (m_0, m_1, s) \leftarrow A(\text{ })(pk); \\ b \leftarrow \{0, 1\}; c^* \leftarrow \text{Enc}(pk, m_b); \\ b' \leftarrow A(\text{ })(s, c^*) \end{array} \right] - \frac{1}{2}$$

Algorithm 2 Kyber.CPA.Enc($pk = (\mathbf{t}, \rho), m \in \mathcal{M}$): encryption

- 1: $r \leftarrow \{0, 1\}^{256}$
- 2: $\mathbf{t} := \text{Decompress}_q(\mathbf{t}, d_t)$
- 3: $\mathbf{A} \sim R_q^{k \times k} := \text{Sam}(\rho)$
- 4: $(\mathbf{r}, \mathbf{e}_1, \mathbf{e}_2) \sim \beta_\eta^k \times \beta_\eta^k \times \beta_\eta := \text{Sam}(r)$
- 5: $\mathbf{u} := \text{Compress}_q(\mathbf{A}^T \mathbf{r} + \mathbf{e}_1, d_u)$
- 6: $v := \text{Compress}_q(\mathbf{t}^T \mathbf{r} + \mathbf{e}_2 + \lceil \frac{q}{2} \rceil \cdot m, d_v)$
- 7: **return** $c := (\mathbf{u}, v)$

IND-CCA? (In-distinguish-ability under Chosen Ciphertext Attack)



$$\text{Adv}_{\text{PKE}}^{\text{cca}}(A) =$$

$$\Pr \left[b = b' : \begin{array}{l} (pk, sk) \leftarrow \text{KeyGen}(); \\ (m_0, m_1, s) \leftarrow A^{\text{DEC}(\cdot)}(pk); \\ b \leftarrow \{0, 1\}; c^* \leftarrow \text{Enc}(pk, m_b); \\ b' \leftarrow A^{\text{DEC}(\cdot)}(s, c^*) \end{array} \right] - \frac{1}{2}$$

Algorithm 3 Kyber.CPA.Dec($sk = s, c = (u, v)$): decryption

- 1: $u := \text{Decompress}_q(u, d_u)$
 - 2: $v := \text{Decompress}_q(v, d_v)$
 - 3: **return** $\text{Compress}_q(v - s^T u, 1)$
-

secret key leaked ☹️

To achieve CCA-secure \rightarrow KEM (Key Encapsulation Mechanism)

Algorithm 5 Kyber.Decaps($sk = (s, z, t, \rho), c = (u, v)$)

```
1:  $m' := \text{Kyber.CPA.Dec}(s, (u, v))$ 
2:  $(\hat{K}', r') := G(H(pk), m')$ 
3:  $(u', v') := \text{Kyber.CPA.Enc}((t, \rho), m'; r')$ 
4: if  $(u', v') = (u, v)$  then
5:   return  $K := H(\hat{K}', H(c))$ 
6: else
7:   return  $K := H(z, H(c))$ 
8: end if
```

$\text{Adv}_{\text{KEM}}^{\text{cca}}(A) =$

$$\left| \Pr \left[b = b' : \begin{array}{l} (pk, sk) \leftarrow \text{KeyGen}(); \\ b \leftarrow \{0, 1\}; \\ (c^*, K_0^*) \leftarrow \text{Encaps}(pk); \\ K_1^* \leftarrow \mathcal{K}; \\ b' \leftarrow A^{\text{DECAPS}(\cdot)}(pk, c^*, K_b^*) \end{array} \right] - \frac{1}{2} \right|$$

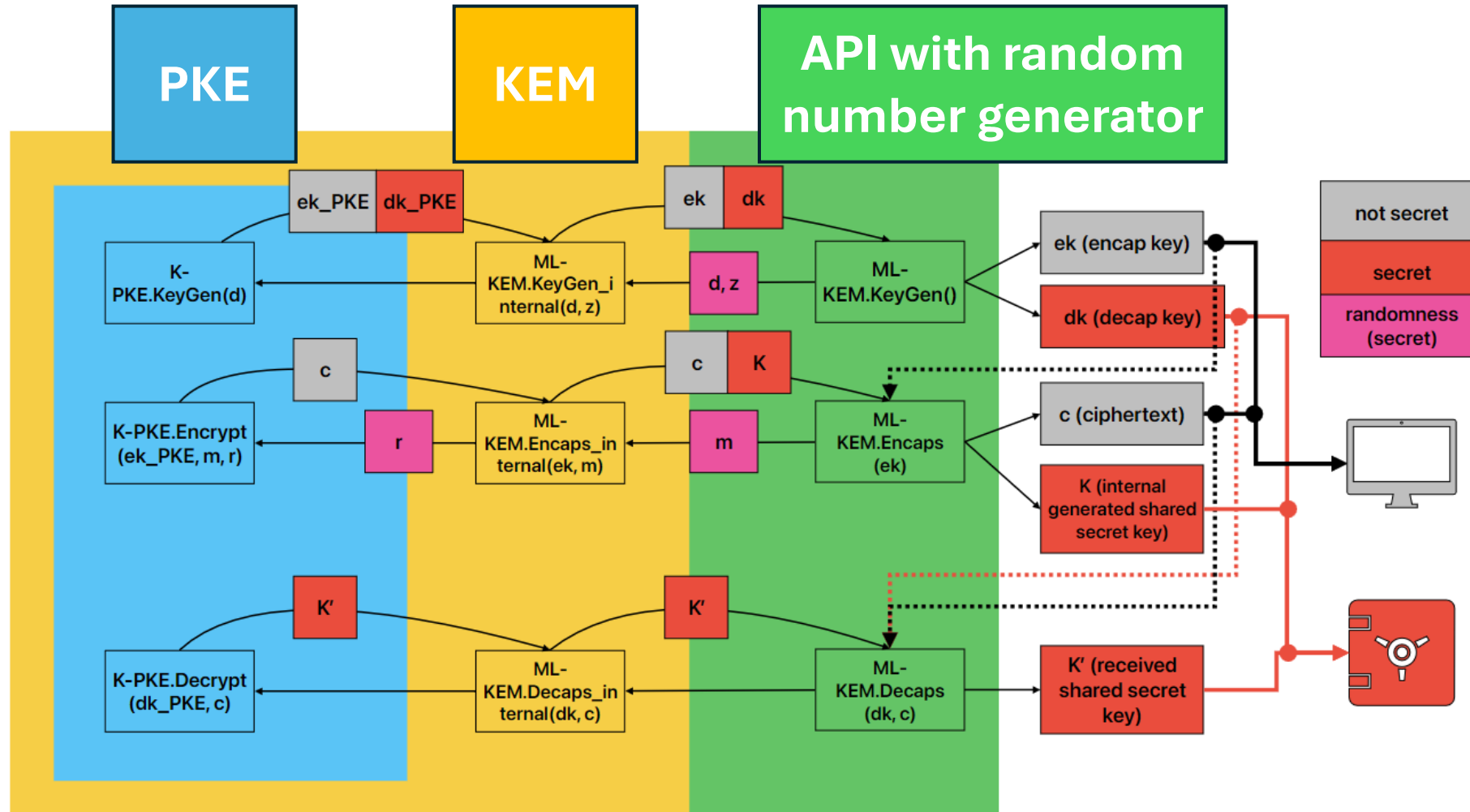
Bad ciphertext c
that will cause
decryption failure

re-encryption: c'
(line 3)

Very likely: $c \neq c'$
Output a “not key” (line 7)

Implicitly rejected
ciphertext without
exposing the secret key

Thus, the layers for ML-KEM in FIPS 203



About Number Theoretic Transform (NTT)

$$\left\{ \begin{array}{l} R_q := \mathbb{Z}_q[X]/(X^{256} + 1) \\ f = f_0 + f_1X + \cdots + f_{255}X^{255} \in R_q \\ X^{256} + 1 = \prod_{i=0}^{127} (X^2 - \zeta^{2\text{BitRev}_7(i)+1}). \end{array} \right.$$

$f_1 \times f_2: O(256^2)$ complexity

$$\left\{ \begin{array}{l} T_q := \bigoplus_{i=0}^{127} \mathbb{Z}_q[X]/(X^2 - \zeta^{2\text{BitRev}_7(i)+1}). \\ \hat{g} = (\hat{g}_{0,0} + \hat{g}_{0,1}X, \hat{g}_{1,0} + \hat{g}_{1,1}X, \dots, \hat{g}_{127,0} + \hat{g}_{127,1}X) \in T_q. \end{array} \right.$$

$\hat{g}_1 \times \hat{g}_2: O(256 \log 256)$
complexity $\rightarrow \text{😊}$

The algorithm for NTT

Algorithm 9 $\text{NTT}(f)$

Computes the NTT representation \hat{f} of the given polynomial $f \in R_q$.

Input: array $f \in \mathbb{Z}_q^{256}$.

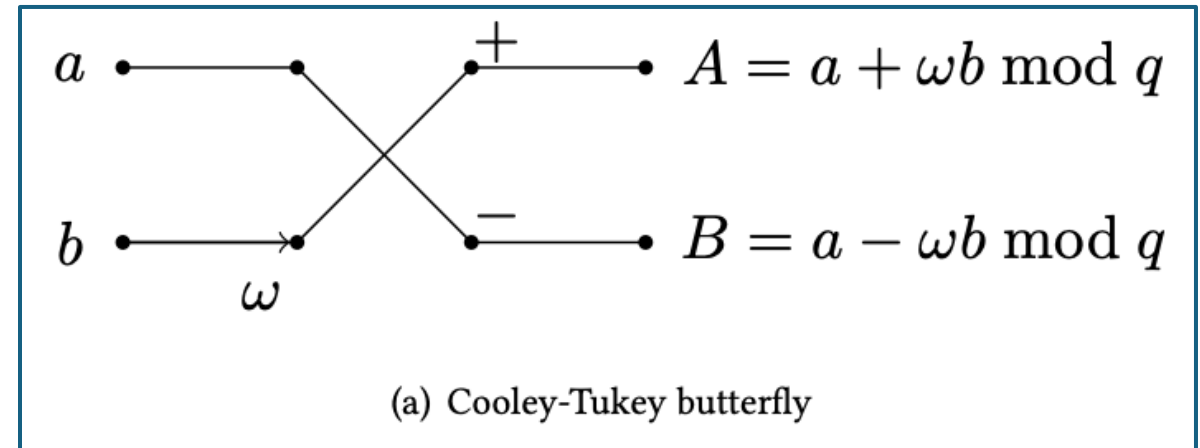
▷ the coefficients of the input polynomial

Output: array $\hat{f} \in \mathbb{Z}_q^{256}$.

▷ the coefficients of the NTT of the input polynomial

▷ will compute in place on a copy of input array

```
1:  $\hat{f} \leftarrow f$ 
2:  $i \leftarrow 1$ 
3: for ( $len \leftarrow 128$ ;  $len \geq 2$ ;  $len \leftarrow len/2$ )
4:   for ( $start \leftarrow 0$ ;  $start < 256$ ;  $start \leftarrow start + 2 \cdot len$ )
5:      $zeta \leftarrow \zeta^{\text{BitRev}_7(i)} \bmod q$ 
6:      $i \leftarrow i + 1$ 
7:     for ( $j \leftarrow start$ ;  $j < start + len$ ;  $j++$ )
8:        $t \leftarrow zeta \cdot \hat{f}[j + len]$ 
9:        $\hat{f}[j + len] \leftarrow \hat{f}[j] - t$ 
10:       $\hat{f}[j] \leftarrow \hat{f}[j] + t$ 
11:     end for
12:   end for
13: end for
14: return  $\hat{f}$ 
```



The algorithm for inverse NTT

Algorithm 10 $\text{NTT}^{-1}(\hat{f})$

Computes the polynomial $f \in R_q$ that corresponds to the given NTT representation $\hat{f} \in T_q$.

Input: array $\hat{f} \in \mathbb{Z}_q^{256}$.

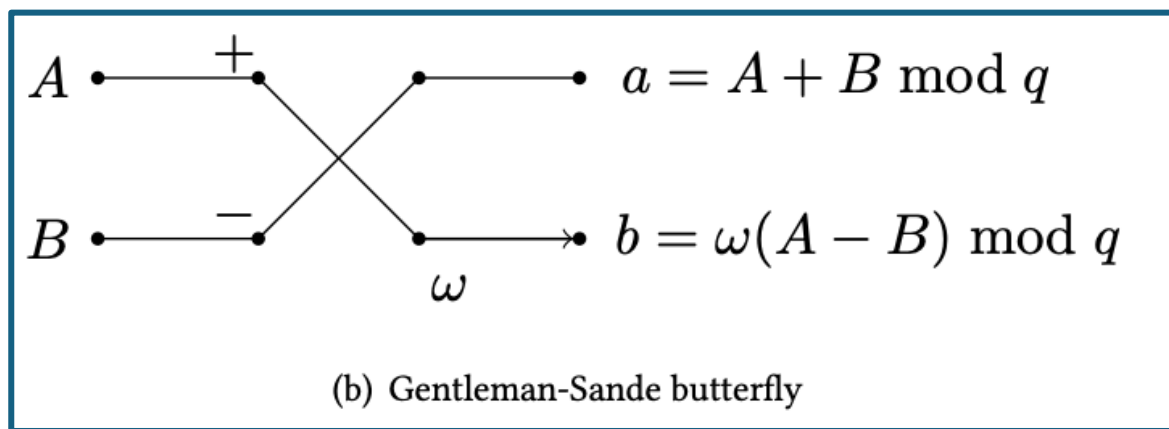
Output: array $f \in \mathbb{Z}_q^{256}$.

▷ the coefficients of input NTT representation

▷ the coefficients of the inverse NTT of the input

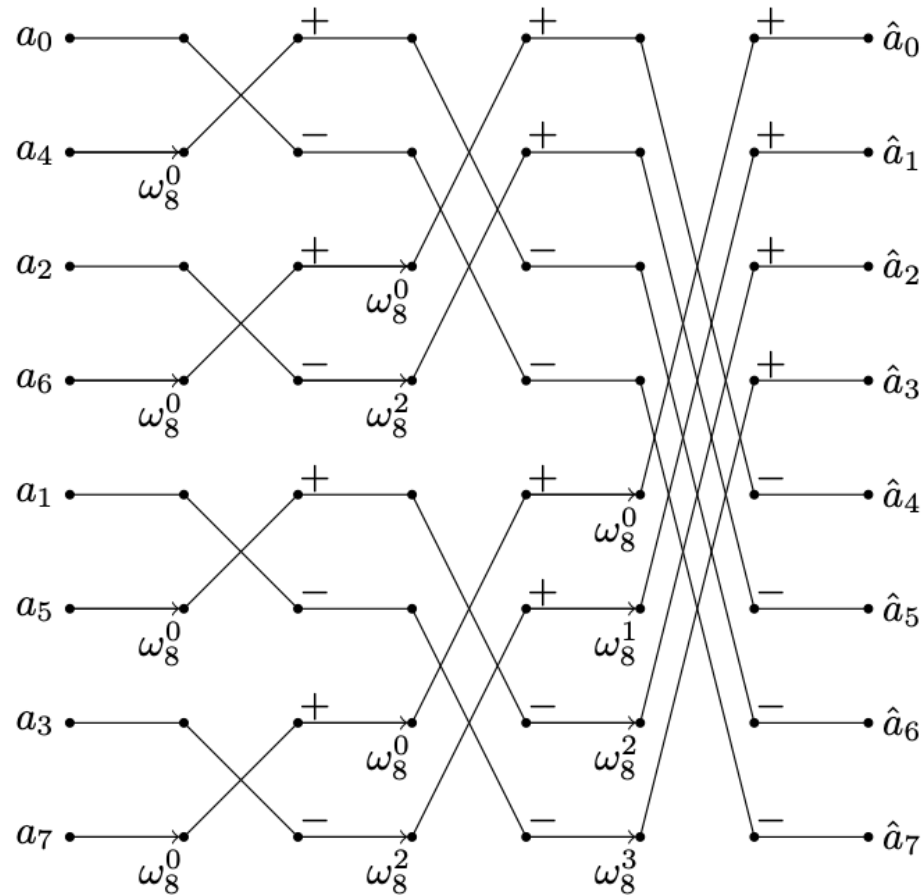
▷ will compute in place on a copy of input array

```
1:  $f \leftarrow \hat{f}$ 
2:  $i \leftarrow 127$ 
3: for ( $len \leftarrow 2$ ;  $len \leq 128$ ;  $len \leftarrow 2 \cdot len$ )
4:   for ( $start \leftarrow 0$ ;  $start < 256$ ;  $start \leftarrow start + 2 \cdot len$ )
5:      $zeta \leftarrow \zeta^{\text{BitRev}_7(i)} \bmod q$ 
6:      $i \leftarrow i - 1$ 
7:     for ( $j \leftarrow start$ ;  $j < start + len$ ;  $j++$ )
8:        $t \leftarrow f[j]$ 
9:        $f[j] \leftarrow t + f[j + len]$ 
10:       $f[j + len] \leftarrow zeta \cdot (f[j + len] - t)$ 
11:     end for
12:   end for
13: end for
14:  $f \leftarrow f \cdot 3303 \bmod q$ 
15: return  $f$ 
```

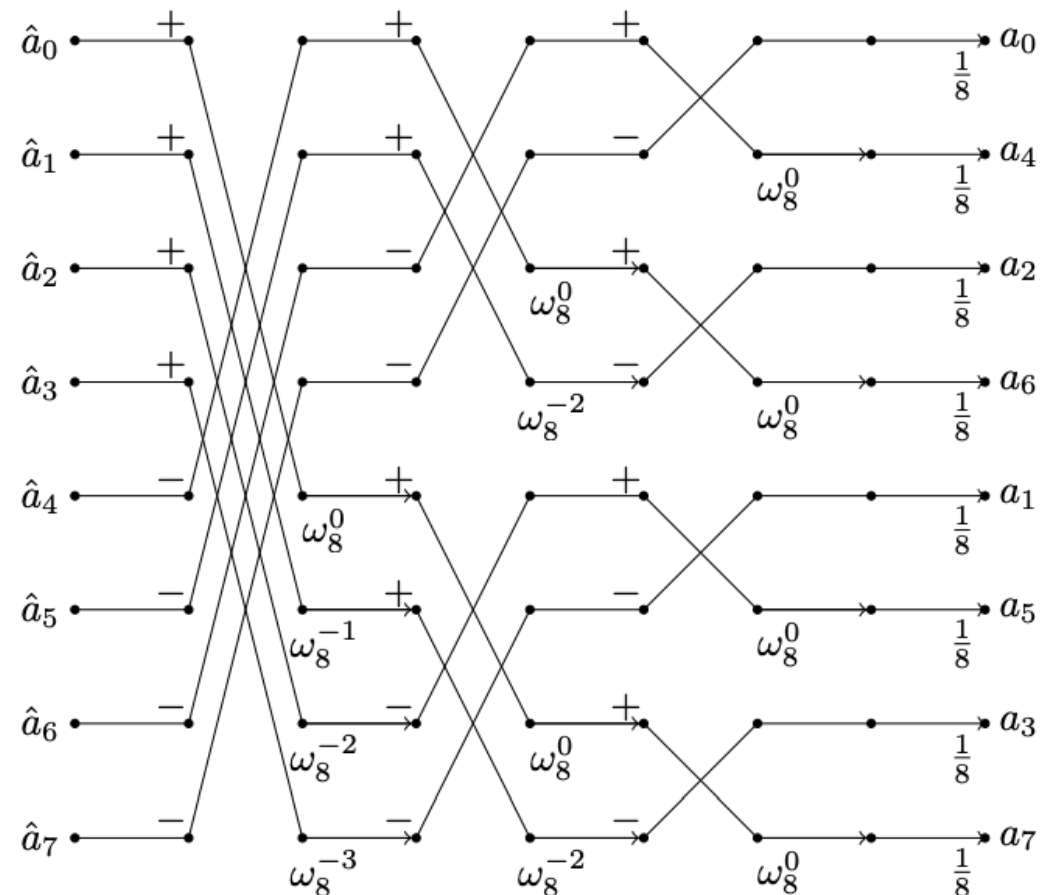


▷ multiply every entry by $3303 \equiv 128^{-1} \bmod q$

Cooley-Tukey and Gentleman-Sande “butterfly”



(a) $\text{NTT}_{bo \rightarrow no}^{CT}$



(d) $\text{INTT}_{no \rightarrow bo}^{GS}$

The hardware implementation paper that I'm still reading

A Configurable CRYSTALS-Kyber Hardware Implementation with Side-Channel Protection

ARPAN JATI, NTU, Singapore and IIT Delhi

NAINA GUPTA and ANUPAM CHATTOPADHYAY, NTU

SOMITRA KUMAR SANADHYA, IIT Jodhpur

What needs to be done next

- Hardware modules for the math operations (NTT, modulo, etc)
- Read more about past hardware implementations
- Attacks and mitigations on hardware implementations

Thanks for listening

:P