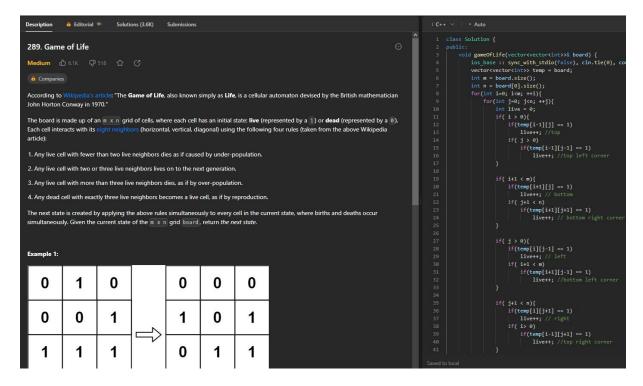
Introduction to Embedded Systems EHB 326E #HW2

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040200252 - Game of Life



Above, there is the description of the problem that I am supposed to solve using picoblaze and assembly. Basically given a grid, you should find the next state of the input matrix according to the rules.

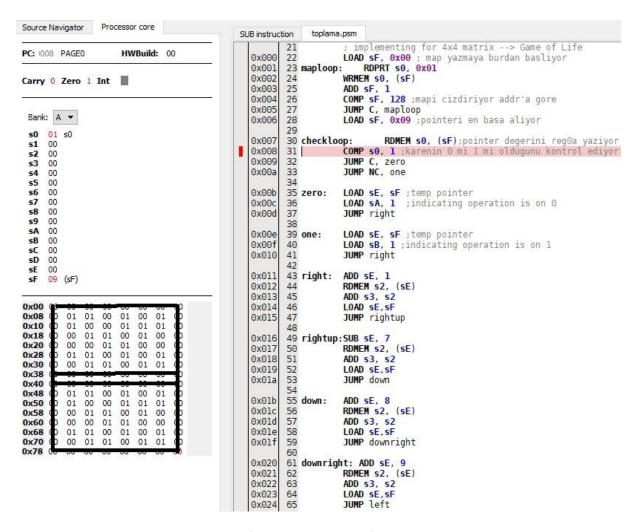
The rules: If a live cell (1) has 2 or 3 neighbors it keeps living, otherwise it dies (0).

If a dead cell (0) has 3 neighbors it revives (1), otherwise keeps its state.

The algorithm in C++ traverses through every square in the grid and checks its 8-way neighbors in order to determine its next state. However, to not cause confusion it copies the existing input grid to a temporary one to implement the algorithm on for printing the next state of the grid. This way the algorithm is not disrupted by previous outputs on previous squares.



 $\stackrel{\checkmark}{}$ This is the input that I apply to the port 0x01 in order to write the grid.



Above you are seeing the initial state of my assembly code for implementing this problem. Since memory is limited with Picoblaze, I chose a 6x6 approach for my grid to implement my algorithm on. In the bottom left you can see the grid printed along with its copy at the bottom. All changes will be done to the bottom one using a temporary pointer "sE" while the upper one will remain as my original input.

I chose register "sF" as a pointer to the grid addresses to write the map onto the board and register "s0" acts as a port reader which reads the data from the input stream from the port 0x01 for keeping track of which number (0 or 1) to write to the grid. By initializing a loop and setting the counter at 128 I wrote the grid onto the scratchpad.

Since I had a 150 line limit due to the policy of Fidex IDE, to keep things simples I left a barriers of zeros outside the grid thus removing the need of separately writing conditions for corners and edges. Having zeros as a neighbor does not affect the rules at all therefore its implementable.

Then the process is pretty simple. In checkloop, it checks whether the current square has a dead cell or a live cell (0 or 1). Then it jumps to the according loop. For both cases I attended sA and sB registers for keeping track of the current data being a zero or a one. This will come in handy when the change loop based on the rules activates because it has different actions for a dead cell and a live cell.

Then the next four loop you see in the upper picture which are named:

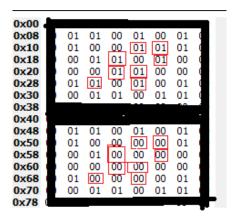
"right","rightup","down",downup". They check the neighbors of the current square. Every square has 8 neighbors so we need 8 directions to check. Therefore I wrote 8 loops like that in total, the rest of them you can check in the full code at the bottom. In these directional check functions I used the register "s3" for keeping track of how many live neighbors the current cell has.

```
up:
         SUB sE,8
         RDMEM s2, (sE)
         ADD s3, s2
         LOAD SE, SF
         COMP sA, 1 ;hangisinin sonucuna gidecegini seciyor
         JUMP Z, resultzero
        COMP sB, 1
JUMP Z, resultone
resultzero: COMP s3, 3
              JUMP Z, effectzero
             ADD sF, 1
             LOAD s3, 0
             LOAD sA, 0
JUMP checkloop
resultone:
             COMP s3, 2
              JUMP Z, effectone
             COMP s3, 3
JUMP Z, effectone
ADD sE, 64
              WRMEM s9, (sE)
              LOAD SE, 0
              ADD sF, 1
             LOAD s3, 0
             LOAD sB, 0
JUMP checkloop
effectzero: ADD sE, 64 ;sifirda degisim varsa burda
              LOAD SE, 1
             ADD sF, 1
LOAD s3, 0
             LOAD SA, 0
              JUMP checkloop
effectone:
             ADD sF, 1 ;birde degisim yoksa burda
             LOAD s3, 0
             LOAD sB, 0
             JUMP checkloop
```

In these last 5 functions the outcome for the current cell will be determined and pointer will move to the next address (cell). In the last directional check which is "up", by using the sA and sB registers the code moves to the outcome of either zero or one. Then in these resulting loops outcomes are determined according to the rules of the game. For example in resultone, if the current cell has 2 or 3 live neighbors it moves to effectone which does not change the current state and moves on to the next cell. However if it does not have 2 or 3 live neighbors, it kills the current cell then moves on to the next one. Of course before moving to the next cell, previously used registers like s3, sA, sB, sE are resetted.

In conclusion by doing the to every cell in the original grid (upper) then implementing the changes in the temporary one (bottom). We acquire the solution to the problem.

The ultimate result and changes are highlighted.



Full Code:

```
implementing for 4x4 matrix --> Game of Life
       21
                  LOAD sF, 0x00; map yazmaya burdan basliyor
RDPRT s0, 0x01
0x000
       22
0x001
       23 maploop:
0x002
                  WRMEM s0, (sF)
       24
0x003
                  ADD sF, 1
       25
0x004
       26
                  COMP sF, 128 :mapi cizdiriyor addr'a gore
0x005
       27
                  JUMP C, maploop
0x006
                  LOAD sF, 0x09 ;pointeri en basa aliyor
       28
       29
0x007
       30 checkloop:
                           RDMEM s0, (sF); pointer degerini reg0a yaziyor
                  COMP s0, 1 ;karenin 0 mi 1 mi oldugunu kontrol ediyor
0x008
       31
                  JUMP C, zero
0x009
       32
0x00a
                  JUMP NC, one
       33
       34
0x00b
       35 zero:
                  LOAD sE, sF ; temp pointer
0x00c
       36
                  LOAD sA, 1 ;indicating operation is on 0
0x00d
       37
                  JUMP right
       38
0x00e
       39 one:
                  LOAD sE, sF ; temp pointer
                  LOAD sB, 1 ; indicating operation is on 1
0x00f
       40
0x010
       41
                  JUMP right
       42
0x011
       43 right:
                  ADD sE, 1
0x012
       44
                  RDMEM s2, (sE)
                  ADD s3, s2
0x013
       45
0x014
       46
                  LOAD SE, SF
                  JUMP rightup
0x015
       47
       48
0x016
       49 rightup: SUB sE, 7
0x017
       50
                  RDMEM s2, (sE)
0x018
       51
                  ADD s3, s2
0x019
       52
                  LOAD SE, SF
0x01a
       53
                  JUMP down
       54
       55 down:
0x01b
                  ADD sE, 8
0x01c
       56
                  RDMEM s2, (sE)
0x01d
       57
                  ADD s3, s2
0x0le
       58
                  LOAD SE, SF
0x01f
       59
                  JUMP downright
       60
0x020
       61 downright: ADD sE, 9
                  RDMEM s2, (sE)
0x021
       62
0x022
                  ADD s3, s2
       63
                  LOAD SE, SF
0x023
       64
0x024
       65
                  JUMP left
       66
```

```
67 left:
0x025
                   SUB sE, 1
                  RDMEM s2, (sE)
0x026 68
0x027
       69
                   ADD s3, s2
       70
0x028
                   LOAD SE, SF
                   JUMP downleft
0x029
       71
       72
0x02a
       73 downleft: ADD sE, 7
       74
                   RDMEM s2, (sE)
0x02b
0x02c
       75
                   ADD s3, s2
0x02d
       76
                   LOAD SE, SF
                   JUMP leftup
0x02e
       77
       78
0x02f
       79 leftup: SUB sE, 9
0x030
       80
                   RDMEM s2, (sE)
0x031
       81
                   ADD s3, s2
0x032
       82
                   LOAD SE, SF
0x033
       83
                   JUMP up
       84
0x034
       85 up:
                   SUB sE,8
0x035
       86
                   RDMEM s2, (sE)
0x036
       87
                   ADD s3, s2
0x037
                   LOAD SE, SF
       88
                   COMP sA, 1 ; hangisinin sonucuna gidecegini seciyor
0x038
       89
                   JUMP Z, resultzero
0x039
       90
                   COMP sB, 1
0x03a
       91
0x03b
       92
                   JUMP Z, resultone
       93
0x03c
       94 resultzero: COMP s3, 3
                       JUMP Z, effectzero
ADD sF, 1
0x03d
       95
0x03e
       96
0x03f
       97
                       LOAD s3, 0
0x040
                       LOAD SA, 0
       98
                       JUMP checkloop
0x041 99
      100
0x042 101 resultone: COMP s3, 2
0x043 102
                       JUMP Z, effectone
0x045 102
0x045 104
0x046 105
                       COMP s3, 3
                       JUMP Z, effectone
ADD sE, 64
0x047 106
                       WRMEM s9, (sE)
0x048 107
                      LOAD SE, 0
0x049 108
                       ADD sF, 1
0x04a 109
                       LOAD s3, 0
0x04b 110
                       LOAD sB, 0
0x04c 111
                       JUMP checkloop
     112
0x04d 113 effectzero: ADD sE, 64 ;sifirdadegisim varsa burda
0x04e 114
                       LOAD SE, 1
                       ADD sF, 1
0x04f 115
0x050 116
0x051 117
                       LOAD s3, 0
                       LOAD SA, 0
0x052 118
                       JUMP checkloop
      119
0x053 120 effectone: ADD sF, 1 ;birde degisim yoksa burda
0x054 121
                       LOAD s3, 0
0x055 122
                       LOAD sB, 0
                       JUMP checkloop
0x056 123
```