Altaher Abdussalam Altaher Saleh

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Objective

Aspiring Computer Engineer focused on Artificial Intelligence, Deep Learning, and Computer Vision. Passionate about developing innovative solutions through research and gaining valuable experience in cutting-edge technologies. Eager to contribute to the technology industry by solving real-world challenges.

Education

B.Sc. in Computer Engineering

University of Tripoli, Faculty of Engineering

Expected Graduation: September 2025

Currently ranked among the top 3 students in class with excellent grades. Aiming to graduate in 8 semesters instead of 10.

Ranked among the Top 3 students in several courses, including:

- EC313 Electronic Circuits (1st)
- EC383 Digital Systems
- EC483 Microprocessor
- EC312 Electronic Circuits

Languages

- Arabic (Native)
- English (Fluent)
- Spanish (Beginner, 1%)

Work Experience

Oloom Alahia (Sole Agent for Bioscientia Laboratories, Germany)

April 2021 - July 2021

Worked in a reception role, helping patients with their test results and answering questions. Gained hands-on experience in a clinical environment, improving communication and organizational skills.

Volunteering

Volunteered in various student-led campaigns at university, providing lectures and assistance to fellow students in courses I previously completed.

Academic Projects

Deep Learning (EE569 – Special Topics in Control Systems)

Supervised by: Dr. Nuri Benbarka

- MLP Classifier for Handwritten Digits: Implemented a Multi-Layer Perceptron neural network using NumPy.
- NLP Chatbot for Ahkam Sharia: Developed a Retrieval-Augmented Generation chatbot using OpenAI APIs.

- RL Car Racing: Trained a Deep Q Network (DQN) agent to autonomously complete laps.
- Instance Segmentation and Gender Classification: Built a computer vision model using Detectron2.

Pattern Recognition (EC557)

Supervised by: Dr. Nabil Drawil

- Statistical Model Estimation: Applied parametric and non-parametric methods, including MED, GED, MAP, NN, and kNN.
- Built 2D Classification Models: Used Maximum Likelihood Estimation to develop models.

Digital Systems 2 (EC383)

Supervised by: Yusra Maatug

- Booth Multiplier Project: Designed and implemented a Booth multiplier using VHDL, completing all requirements successfully.
- Gained extensive experience in hardware description languages (VHDL) and digital design principles.

Graduation Project:

A Portable Brain-Computer Interface with NPU-Accelerated On-Device Machine Learning for Real-Time EEG Signal Classification

This project designs a real-time Brain-Computer Interface using a microcontroller with NPU capabilities. It explores machine learning and deep learning models for EEG signal classification, eliminating the need for external servers.

Skills

- Programming Languages: academic knowledge in Python, MATLAB, C, OOP.
- Tools Libraries: TensorFlow, Keras, PyTorch, OpenCV, scikit-learn, VHDL
- Other: Data Acquisition (ADS1299), Embedded Systems, Signal Processing, Neural Networks, Machine Learning

Awards Achievements

- Top 3 student in multiple courses at the University of Tripoli.
- Recognized for academic excellence in Deep Learning, Pattern Recognition, and Digital Systems courses.

References

Available upon request.