**NMOS**

V\_th=0.67

T\_ox=1.39e-8

Epsilon=3.9\*8.85e-12

Un=458.44

K\_n = 1.138e-4\*(W/L)

**PMOS**V\_th=-0.92

T\_ox=1.29e-8

Epsilon=3.9\*8.85e-12

Up=212.02

K\_p = 0.568e-4\*(W/L)

Assuming Lambda=0

**Single Stage OTA**

To bias o/p CM at Vdd/2=2.5V, I=0.85mA

Thus, V\_b for tail current source=1.785V

**Two Stage OTA**

Bias voltage=1V

Open loop works perfectly

Oscillations seen in closed loop

**Folded Cascode Amplifier**

Biasing??

I\_p=0.1m🡪V\_b1=3.31V

Gm (diff pair) =0.1mS🡪I\_tail=2\*(0.044m)🡪 v\_b=1.03V

I\_n=0.1-0.044=0.056m🡪v\_b3=2.33

V\_b2=2.54