



Vivekanand Education Society's Institute Of Technology

Hashu Advani Memorial Complex, Collectors Colony, Chembur, Mumbai

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CERTIFICATE OF INTERNSHIP

This certificate is presented to

Atharva Vishwas Sarang

For successfully completing the summer internship in “VLSI-Cadence EDA Tool”, organized by the **Department of Electronics and Computer Science** and the **Department of Automation & Robotics**, in association with **VESIT-IIC & IQAC** and **Panache Digilife Limited**, from **30th May to 15th July 2024**, under **Chips to Startup (C2S)** Program.

Mr Abhijit Shete
Internship Co-ordinator

Dr.(Mrs) Jayamala Adsul
Co-Chief Investigator
C2S Project

Dr (Mrs) Nilima Warke
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Dr.(Mr) P.P.Vaidya
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Dr.(Mrs) J.M.Nair
Principal
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