

Thadomal Shahani Engineering College

Bandra (W.), Mumbai- 400 050.

❧ CERTIFICATE ❧

Certify that Mr./Miss Amrta Yadav
of IT Department, Semester IV with
Roll No. 124 has completed a course of the necessary
experiments in the subject MPL under my
supervision in the Thadomal Shahani Engineering College
Laboratory in the year 20 - 20



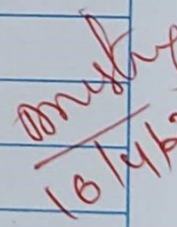

Teacher in-Charge

Head of the Department

Date 16/04/2024

Principal

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Sr. No.	EXPERIMENTS	PAGE NO.	DATE	TEACHERS SIGN.
1.	Study of PC motherboard technology (North and south bridge), internal components and connections used in computer system (LO1)	LO1	18.1.24	 16/4/24
2	verify the truth table of various logic gates (basic and universal gates) (LO1)	LO2	25.1.24	
3	Realize half adder and full adder (LO2)	LO2	1.02.24	
4	Implementation of Mux and Demux (LO2)	LO2	8.02.24	
5	Write a 8086 program for 16 bit	LO3	7.03.24	 16/4/24
6	Write a 8086 program to convert 2 digit packed Bcd to unpacked Bcd (LO3)	LO3	18.03.24	
7	Write a 8086 program to move set of numbers from one memory block to another (LO4)	LO4	21.3.24	
8	Write 8086 program to count number of 1's 0's in 8 bit number (LO4)	LO4	28.3.24	
9.	Write 8086 program to find even odd numbers from a given list (LO4)	LO4	2.4.24	 16/4/24
10.	Write 8086 program to check whether a given string is a palindrome or not	LO5	2.4.24	
11.	Write 8086 program to compute fact	LO5	8.4.24	
12.	Write 8086 program to interfacing seven segment display (LO6)	LO6	8.4.24	
13.	Write an assg I based on the system	CO1	12.4.24	
14.	Write an assg II both's Alg and division method	CO4	12.4.24	