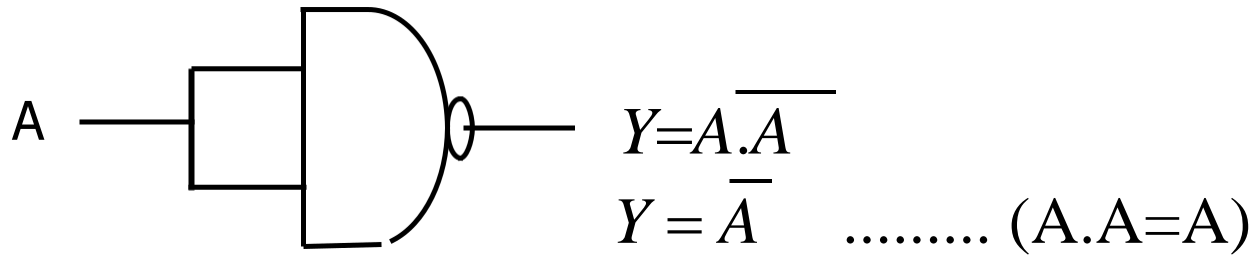
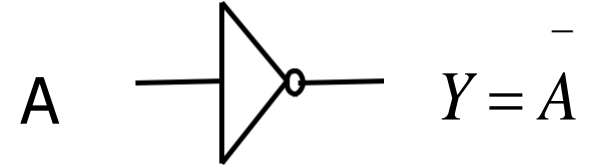


# Universal Gate

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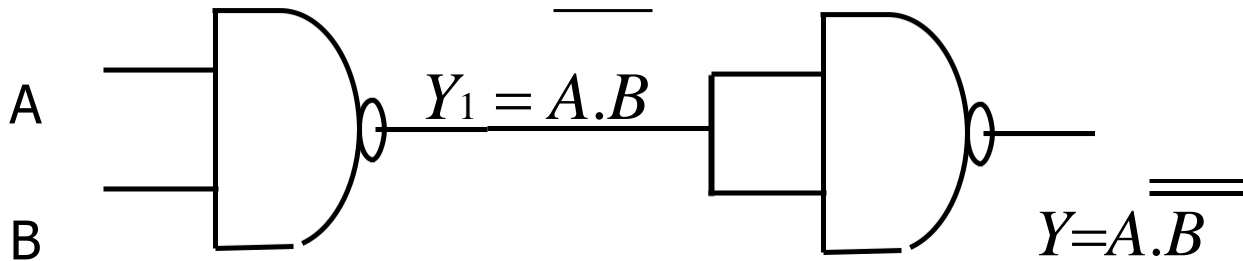
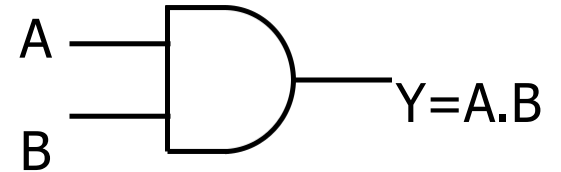
## NOT Gate using NAND Gate



# Universal Gate

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## AND Gate using NAND Gate

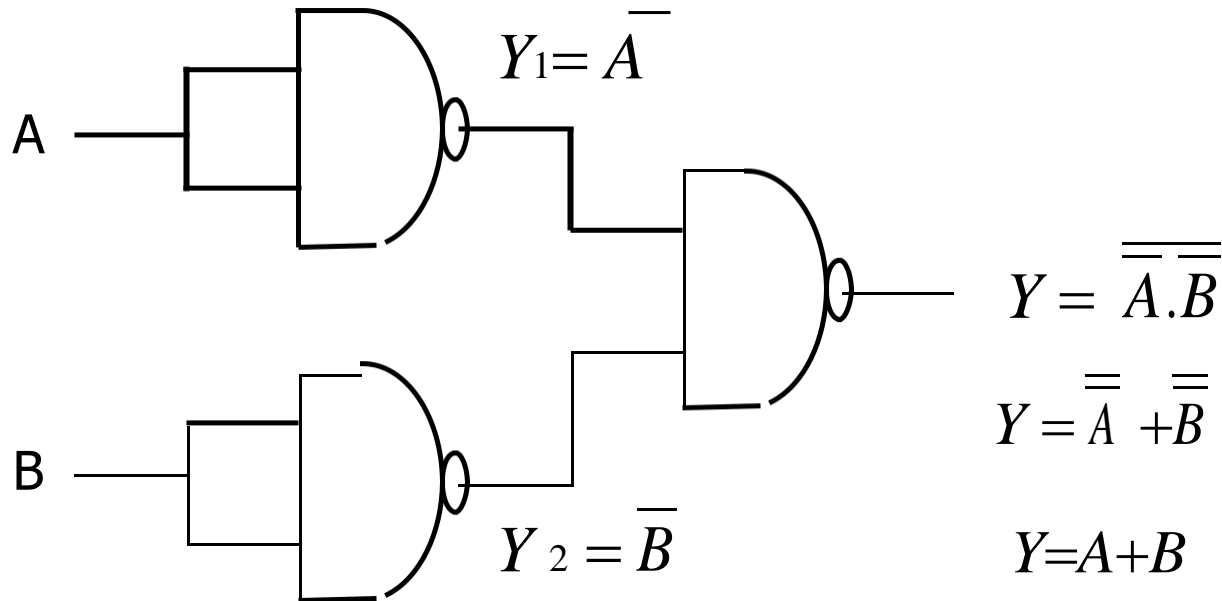
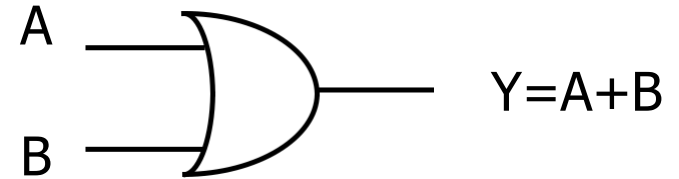


$$Y = A.B \because \overline{\overline{A}} = A$$

# Universal Gate

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## OR Gate using NAND Gate



$$Y = \overline{\bar{A} \cdot \bar{B}}$$

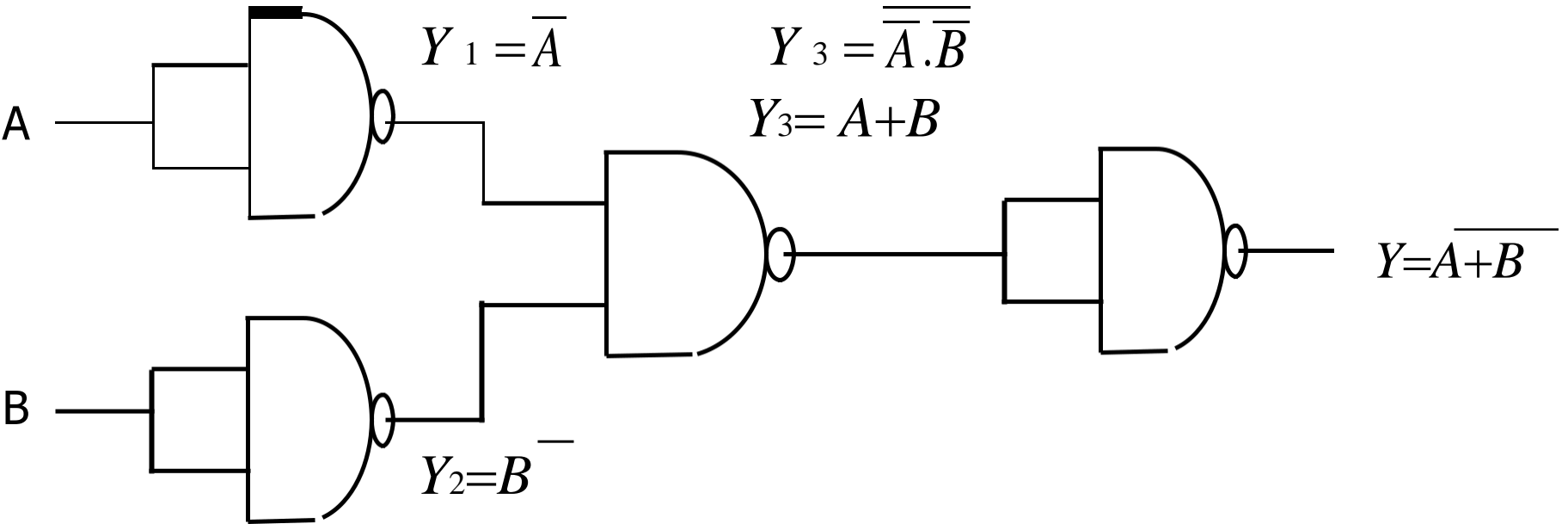
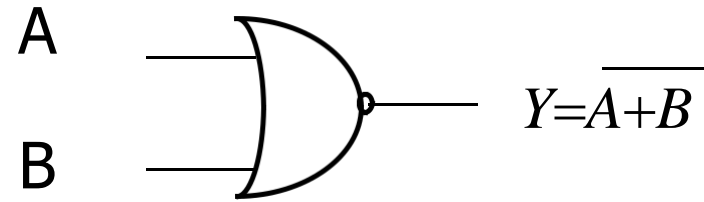
$$Y = A + B$$

( $\because$  Demorgan's Theorem)

# Universal Gate

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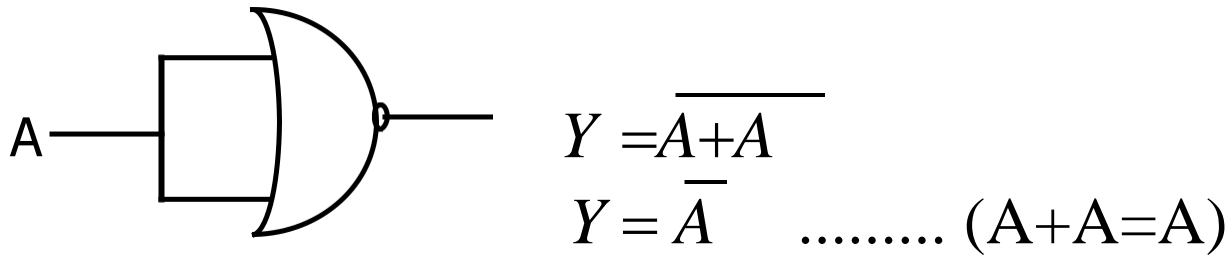
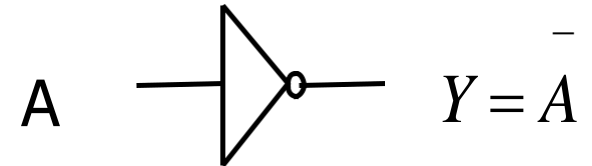
## NOR Gate using NAND Gate



# Universal Gate

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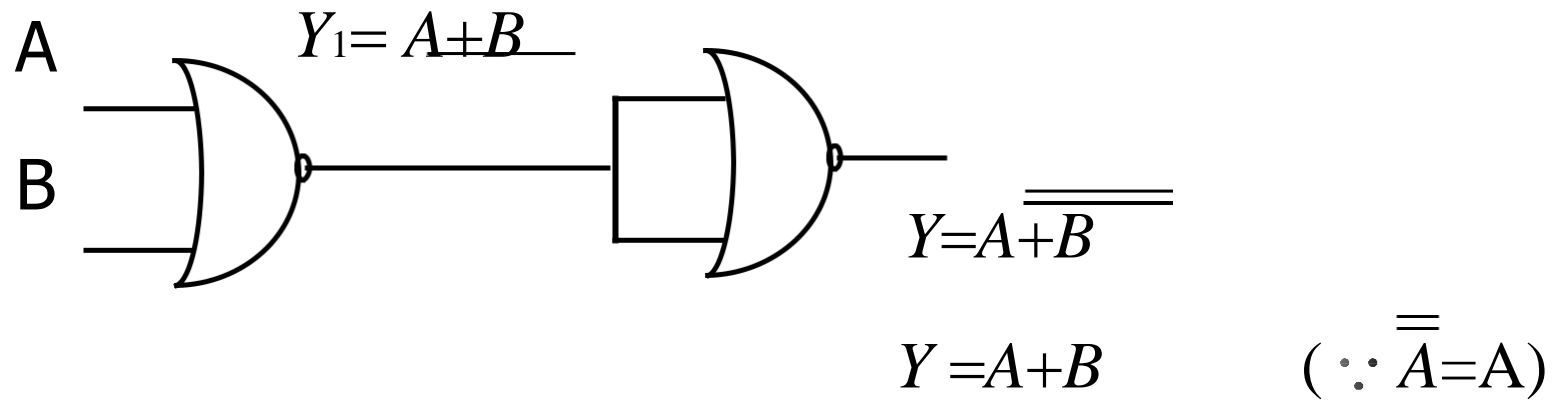
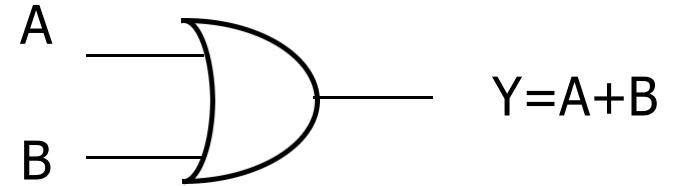
NOT Gate using NOR Gate



# Universal Gate

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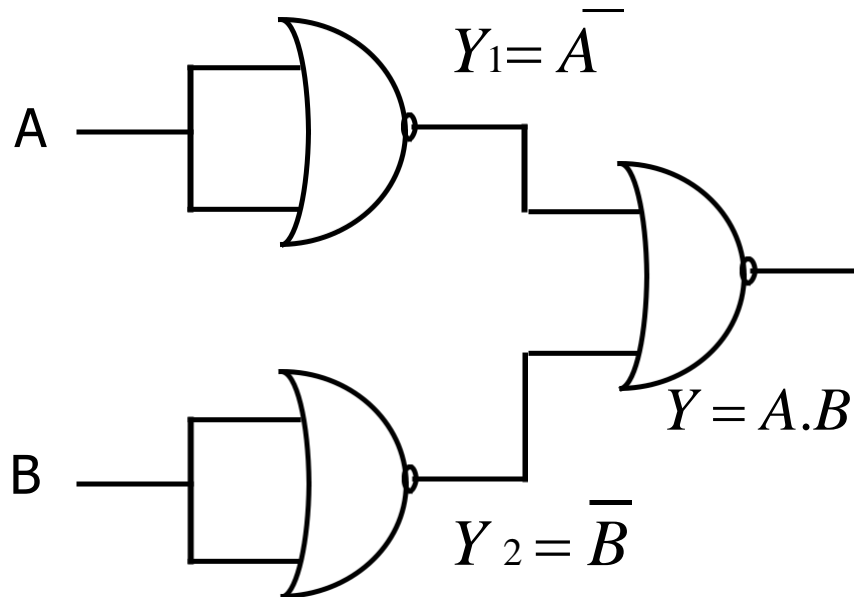
## OR Gate using NOR Gate



# Universal Gate

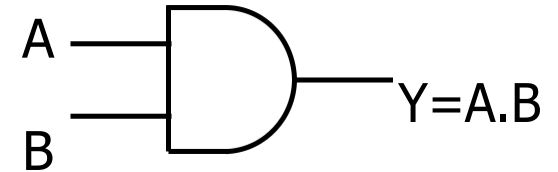
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## AND Gate using NOR Gate



$$Y = \overline{\overline{A} + \overline{B}}$$

$$Y = A.B$$



(  $\therefore$  Demorgan's Theorem)

# Universal Gate

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## NAND Gate using NOR Gate

