## Register Allocation and Assignment

- A Register allocation means, what value in a should neside in a oregister.
- Register Assignment means in which negister, each value should neside.
- a for Register Allocation we have three stratagies
  - 1. Global oregister Allocation neve:
    - 2. Register Allocation for outer loops
    - 3. Register Allocation for graph colouring

- Global Register Allocation > All Live variables should be stored at the end of
- > Allocation of variables to specific enegisters, that consist across the block.

## Register Allocation for outer loops

> If outer loop Li contains inner loop Le names allocated negister in Lz did need not be allocated negister in LiL2

## Register Allocation for Graph Colouring

-> Graph colouring is simple systematic technique

>> Here, initially the target machine instructions are selected

=> Each procedure, a register interference graph is constructed.

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