

MISCELLANEOUS

6.1 Makefiles

The directory `$UWHPSC/codes/fortran/multifile1` contains a Fortran code `fullcode.f90` that consists of a main program and two subroutines:

```

1  ! $UWHPSC/codes/fortran/multifile1/fullcode.f90
2
3  program demo
4      print *, "In main program"
5      call sub1 ()
6      call sub2 ()
7  end program demo
8
9  subroutine sub1 ()
10     print *, "In sub1"
11 end subroutine sub1
12
13 subroutine sub2 ()
14     print *, "In sub2"
15 end subroutine sub2

```

To illustrate the construction of a Makefile, we first break this up into three separate files:

```

1  ! $UWHPSC/codes/fortran/multifile1/main.f90
2
3  program demo
4      print *, "In main program"
5      call sub1 ()
6      call sub2 ()
7  end program demo
8
9  ! $UWHPSC/codes/fortran/multifile1/sub1.f90
10
11 subroutine sub1 ()
12     print *, "In sub1"
13 end subroutine sub1
14
15 ! $UWHPSC/codes/fortran/multifile1/sub2.f90
16
17 subroutine sub2 ()
18     print *, "In sub2"
19 end subroutine sub2

```

The directory `$UWHPSC/codes/fortran/multifile1` contains several Makefiles that get successively more sophisticated to compile the codes in this directory.

In the first version we write out explicitly what to do for each file:

```
1 # $UWHPSC/codes/fortran/multifile1/Makefile
2
3 output.txt: main.exe
4     ./main.exe > output.txt
5
6 main.exe: main.o sub1.o sub2.o
7     gfortran main.o sub1.o sub2.o -o main.exe
8
9 main.o: main.f90
10    gfortran -c main.f90
11 sub1.o: sub1.f90
12    gfortran -c sub1.f90
13 sub2.o: sub2.f90
14    gfortran -c sub2.f90
```

In the second version there is a general rule for creating *.o* files from *.f90* files:

```
1 # $UWHPSC/codes/fortran/multifile1/Makefile2
2
3 output.txt: main.exe
4     ./main.exe > output.txt
5
6 main.exe: main.o sub1.o sub2.o
7     gfortran main.o sub1.o sub2.o -o main.exe
8
9 %.o : %.f90
10    gfortran -c $<
```

In the third version we define a macro *OBJECTS* so we only have to write out this list once, which minimizes the chance of introducing errors:

```
1 # $UWHPSC/codes/fortran/multifile1/Makefile3
2
3 OBJECTS = main.o sub1.o sub2.o
4
5 output.txt: main.exe
6     ./main.exe > output.txt
7
8 main.exe: $(OBJECTS)
9     gfortran $(OBJECTS) -o main.exe
10
11 %.o : %.f90
12    gfortran -c $<
```

In the fourth version, we add a Fortran compile flag (for level 3 optimization) and an linker flag (blank in this example):

```
1 # $UWHPSC/codes/fortran/multifile1/Makefile4
2
3 FC = gfortran
4 FFLAGS = -O3
5 LFLAGS =
6 OBJECTS = main.o sub1.o sub2.o
7
8 output.txt: main.exe
9     ./main.exe > output.txt
10
11 main.exe: $(OBJECTS)
12     $(FC) $(LFLAGS) $(OBJECTS) -o main.exe
13
14 %.o : %.f90
15     $(FC) $(FFLAGS) -c $<
```

Next we add a *phony* target *clean* that removes the files created when compiling the code in order to facilitate

cleanup. It is *phony* because it does not create a file named *clean*.

```

1 # $UWHPSC/codes/fortran/multifile1/Makefile5
2
3 OBJECTS = main.o sub1.o sub2.o
4 .PHONY: clean
5
6 output.txt: main.exe
7     ./main.exe > output.txt
8
9 main.exe: $(OBJECTS)
10    gfortran $(OBJECTS) -o main.exe
11
12 %.o : %.f90
13    gfortran -c $<
14
15 clean:
16    rm -f $(OBJECTS) main.exe

```

Finally we add a help message so that *make help* says something useful:

```

1 # $UWHPSC/codes/fortran/multifile1/Makefile6
2
3 OBJECTS = main.o sub1.o sub2.o
4 .PHONY: clean help
5
6 output.txt: main.exe
7     ./main.exe > output.txt
8
9 main.exe: $(OBJECTS)
10    gfortran $(OBJECTS) -o main.exe
11
12 %.o : %.f90
13    gfortran -c $<
14
15 clean:
16    rm -f $(OBJECTS) main.exe
17
18 help:
19    @echo "Valid targets:"
20    @echo "  main.exe"
21    @echo "  main.o"
22    @echo "  sub1.o"
23    @echo "  sub2.o"
24    @echo "  clean:  removes .o and .exe files"

```

Fancier things are also possible, for example automatically detecting all the *.f90* files in the directory to construct the list of *SOURCES* and *OBJECTS*:

```

1 # $UWHPSC/codes/fortran/multifile1/Makefile7
2
3 SOURCES = $(wildcard *.f90)
4 OBJECTS = $(subst .f90,.o,$(SOURCES))
5
6 .PHONY: test
7
8 test:
9     @echo "Sources are: " $(SOURCES)
10    @echo "Objects are: " $(OBJECTS)

```

6.1.1 Further reading

- http://software-carpentry.org/4_0/make/