





P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = 25°C
	$80m\Omega @ V_{GS} = 4.5V$	-2.7A
-20V	110m Ω @ V _{GS} = 2.5V	-2.1A

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 standards for High Reliability

Description and Applications

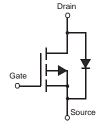
This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

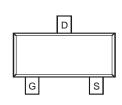
- Backlighting
- **Power Management Functions**
- DC-DC Converters
- Motor control

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (approximate)







Internal Schematic

TOP VIEW

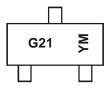
Ordering Information (Note 3)

Part Number	Case	Packaging
DMG2301U-7	SOT-23	3000/Tape & Reel

Notes:

- 1. No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
 For packaging details, go to our website at http://www.diodes.com

Marking Information



G21 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009)M = Month (ex: 9 = September)

Date Code Key

Year	200	9	2010 2011		20	2012 2013			2014	2	2015		
Code	W		Χ		Y		7 -	Α		В		С	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Units	
Drain-Source Voltage		V_{DSS}	-20	V	
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 4) V _{GS} = -4.5V	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I _D	-2.7 -2.1	Α
Continuous Drain Current (Note 4) V _{GS} = -2.5V	Steady State	I _D	-2.1 -1.7	А	
Pulsed Drain Current (Note 5)			I _{DM}	-27	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P _D	0.8	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 4)	$R_{ heta JA}$	157	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

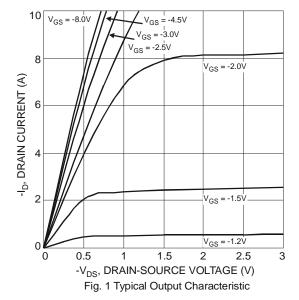
Electrical Characteristics @T_A = 25°C unless otherwise specified

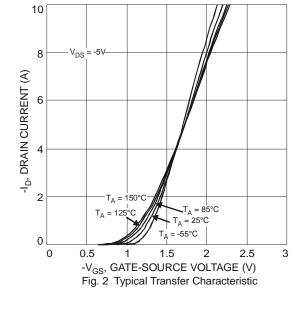
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)			-	-		_
Drain-Source Breakdown Voltage	BV_{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	_	_	-1.0	μΑ	$V_{DS} = -16V, V_{GS} = 0V$
Gate-Source Leakage	I_{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.45	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	D			80	mΩ	$V_{GS} = -4.5V$, $I_{D} = -2.8A$
Static Drain-Source On-Resistance	R _{DS} (ON)		_	110	11177	$V_{GS} = -2.5V$, $I_{D} = -2.0A$
Forward Transfer Admittance	Y _{fs}	_	10	_	S	$V_{DS} = -5V, I_{D} = -2.8A$
Diode Forward Voltage	V_{SD}	_	-0.75	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 7)			_	_	_	
Input Capacitance	C _{iss}	_	608	_	pF	V 6V V 6V
Output Capacitance	Coss	_	82	_	pF	$V_{DS} = -6V, V_{GS} = 0V$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	72	_	pF	1 - 1.000112
Gate Resistance	R _G	_	44.9	_	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$
Total Gate Charge	Q_g	_	6.5		nC	
Gate-Source Charge	Q_{gs}	_	0.9	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_{D} = -3A$
Gate-Drain Charge	Q_{gd}	_	1.5		nC	
Turn-On Delay Time	t _{D(on)}	_	12.5	_	ns	
Turn-On Rise Time	tr	_	10.3	_	ns	$V_{DS} = -10V$, $V_{GS} = -4.5V$,
Turn-Off Delay Time	t _{D(off)}	_	46.5	_	ns	$R_L = 10\Omega$, $R_G = 1.0\Omega$, $I_D = -1A$
Turn-Off Fall Time	t _f	_	22.2	_	ns	

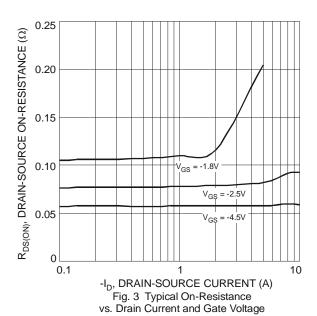
Notes:

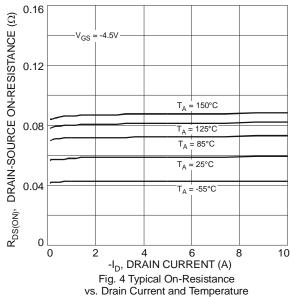
- 4. Device mounted on FR-4 PCB with minimum recommended pad layout.
- Repetitive rating, pulse width limited by junction temperature..
 Short duration pulse test used to minimize self-heating effect.
- 4. Guaranteed by design. Not subject to production testing.

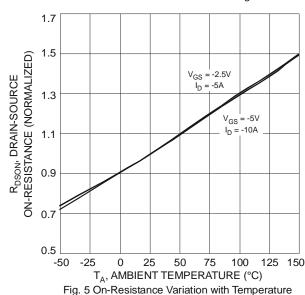


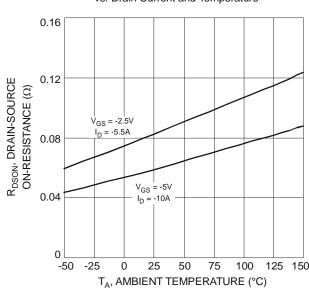














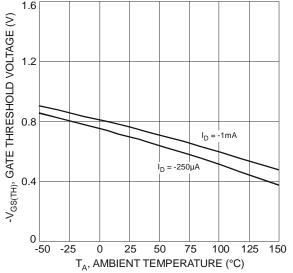
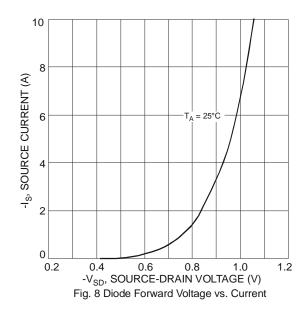
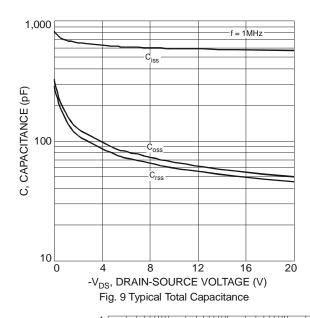


Fig. 7 Gate Threshold Variation vs. Ambient Temperature





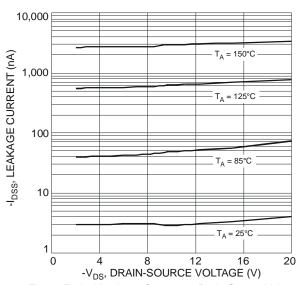


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

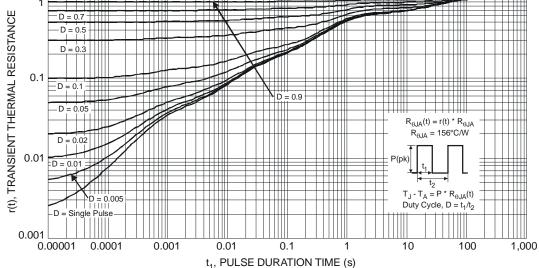
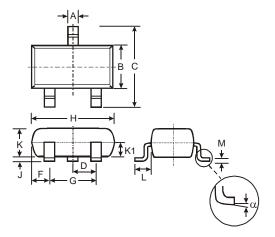


Fig. 11 Transient Thermal Response

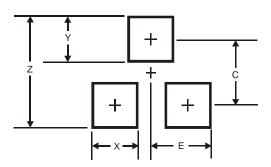


Package Outline Dimensions



SOT-23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
H	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.903	1.10	1.00				
K1	-	-	0.400				
٦	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
Е	1.35



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