

Certificate of Completion



This is to certify that

MR. ASHUTOSH MALLICK

of 4th semester of Electronics & Communication Engineering branch, bearing
SIC No. 190410045 has successfully completed the Summer Internship Course-2021
on “Digital VLSI Design and Verification: A Verilog HDL Approach”
from 17.05.2021 to 05.06.2021 at Silicon Institute of Technology, Bhubaneswar.

Course Coordinator

Principal