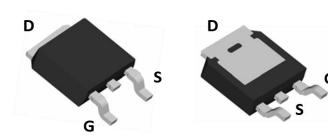


**TO-252** 

### **YJD15N10A**



# **N-Channel Enhancement Mode Field Effect Transistor**



#### **Product Summary**

• V<sub>DS</sub> 100V • I<sub>D</sub> 15A

• R<sub>DS(ON)</sub>( at V<sub>GS</sub>=10V) <110 mohm • R<sub>DS(ON)</sub>( at V<sub>GS</sub>=4.5V) <120 mohm

• 100% UIS Tested

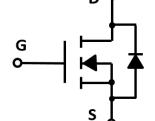
100% ∇V<sub>DS</sub> Tested



• Trench Power MV MOSFET technology

• Excellent package for heat dissipation

• High density cell design for low R<sub>DS(ON)</sub>



#### **Applications**

- DC-DC Converters
- Power management functions

■ Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

ı	Parameter	Symbol	Limit	Unit	
Drain-source Voltage		$V_{DS}$	100	V	
Gate-source Voltage		$V_{GS}$	±20	V	
Drain Current	T <sub>C</sub> =25°C		15	А	
Drain Current	T <sub>C</sub> =100°C	I <sub>D</sub>	10.5		
Pulsed Drain Current <sup>A</sup>		I <sub>DM</sub>	60	Α	
Single Pulse Avalanche Energy		E <sub>AS</sub>	9	mJ	
Total Power Dissipation	T <sub>C</sub> =25℃		45	W	
	T <sub>C</sub> =100℃	$P_{D}$	22.5	W	
Thermal Resistance Junction-to-Case <sup>B</sup>		R <sub>eJC</sub>	3.3	°C/W	
Junction and Storage Temperature Range		$T_J,T_STG$	-55∼+175	°C	

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJD15N10A	F1	YJD15N10A	2500	2500	25000	13" reel



# YJD15N10A

■ Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Conditions		Min	Тур	Max	Units
Static Parameter							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA		100			V
		V <sub>DS</sub> =100V,V <sub>GS</sub> =0V	T <sub>J</sub> =25°C			1	- μΑ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ = $\pm 20V$ , $V_{DS}$	<sub>S</sub> =0V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$		1	1.8	3.0	V
Olatic Paris Oceans Oc Paristone	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> =12A			95	110	mΩ
Static Drain-Source On-Resistance		V <sub>GS</sub> = 4.5V, I <sub>D</sub> =8A			100	120	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =15A,V <sub>GS</sub> =0V			0.8	1.2	V
Maximum Body-Diode Continuous Current	Is					15	А
Dynamic Parameters							
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V,f=1MHZ			785		pF
Output Capacitance	C <sub>oss</sub>				38		
Reverse Transfer Capacitance	C <sub>rss</sub>				30		
Switching Parameters							
Total Gate Charge	Qg	V <sub>GS</sub> =10V,V <sub>DS</sub> =50V,I <sub>D</sub> =10A			16		
Gate-Source Charge	$Q_{gs}$				2.5		nC
Gate-Drain Charge	$Q_{gd}$				2.6		
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}$ =10V, $V_{DD}$ =50V, $R_L$ =6.4 $\Omega$			5		
Turn-on Rise Time	t <sub>r</sub>				40		ns
Turn-off Delay Time	$t_{D(off)}$				20		
Turn-off fall Time	t <sub>f</sub>				7		

A. Pulse Test: Pulse Width  $\leq$  300us, Duty cycle  $\leq$  2%.

B.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta JC}$  is guaranteed by design, while  $R_{\theta JA}$  is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.



### ■ Typical Performance Characteristics

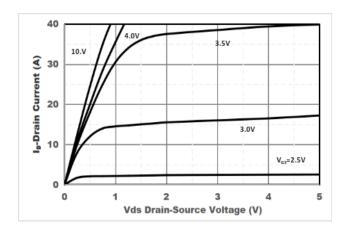


Figure 1. Output Characteristics

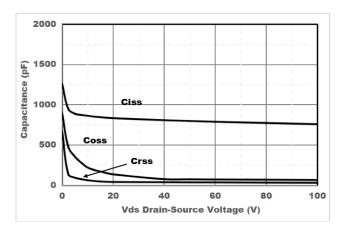


Figure 3. Capacitance Characteristics

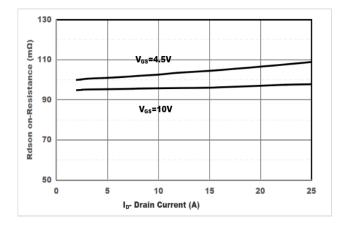


Figure 5. Drain-Source on Resistance

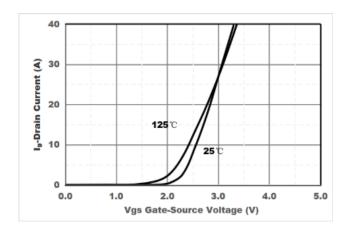


Figure 2. Transfer Characteristics

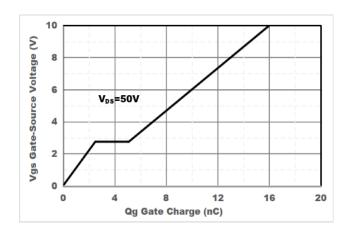


Figure 4. Gate Charge

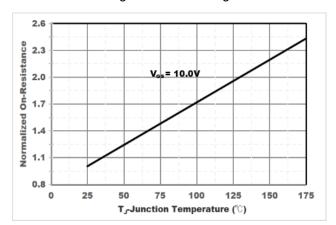


Figure 6. Drain-Source on Resistance





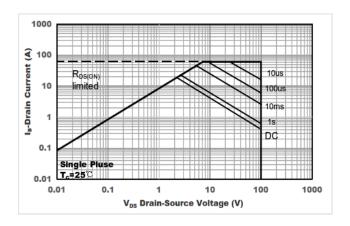


Figure 7. Safe Operation Area

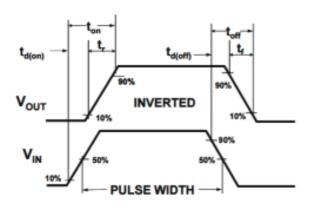
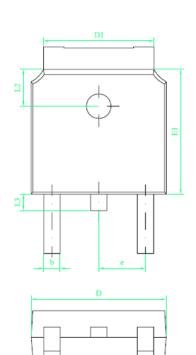


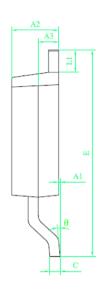
Figure8. Switching wave

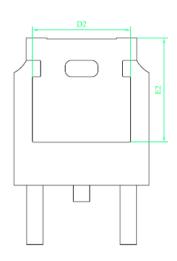




# ■ TO-252 Package information







符号	尺寸					
10 9	min	nom	max			
A1	0		0.10			
A2	2.20	2.30	2.40			
A3	0.90	1.00	1.10			
b	0.75		0.85			
с	0.50		0.60			
D	6.50	6.60	6.70			
D1	5.30	5.40	5.50			
D2	4.70	4.80	4.90			
Е	9.90	10.10	10.30			
E1	6.00	6.10	6.20			
E2	5.20	5.30	5.40			
c	2.20	2.286	2.40			
L1	0.90		1.25			
L2	1.70	1.80	1.90			
L3	0.60	0.80	1.00			
θ	0°		8°			

#### 技术要求:

- 1. 树脂体不应有崩裂、缺损等缺陷:
- 2. 树脂上下部X、Y方向偏差不超过0. 20;
- 3. 胶体两端留废胶总和宽度不超过0.50;
- 4. 所有单位为mm;



#### YJD15N10A

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