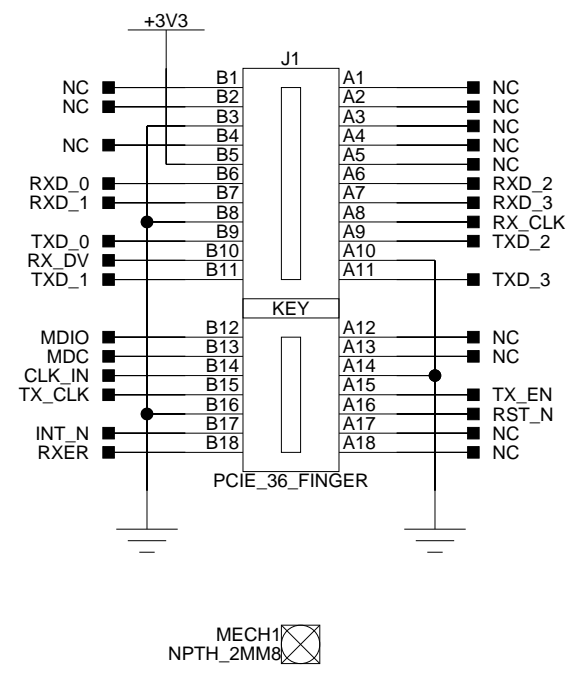


MODE[2:0] = 111 => All capable. Auto-negotiation enabled
PHYAD[2:0] = 000 => SMI Address = 0
RMIISEL = 0 => MII Mode
REGOFF = 0 => USE INTERNAL REG
nINTSEL_N = 1 => nINT/TXER/TXD4 is nINT



<div>XMOS</div> <div>Copyright (c) 2012 XMOS Ltd.</div>		
PROJECT NAME		
XA-SK-E100		
SIZE	SHEET NAME	REV
A3	TOP_LEVEL	1v1
DATE	10 APR 2012	SHEET 1 OF 1