Introduction to Subroutines

Benjamin Kong — 1573684
Lora Ma ———— 1570935

ECE 212 Lab Section H11

March 30, 2020

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1 Introduction

In a program, we often need to perform various sub-tasks many times. For example, we may need to sort numbers in an integer array. We could write the block of instructions that performs this sub-task over and over again, but this would be tedious and a waste of memory space. Therefore, we code these blocks of repeated instructions somewhere in memory and any time we need to use this block of instructions, we simply tell the program to branch to the location of the block of instructions. This block of instructions or sub-tasks are called *subroutines*. The instruction that branches to the subroutine is called a *call* instruction. Furthermore, the calling program is called the *caller* and the subroutine itself is called the *callee*. Once we reach the last instruction in a subroutine (called the *return* instruction), the subroutine returns to the program that called it.

In this lab, our objective was to gain experience using subroutines by writing subroutines for a statistics program. In specific, we wanted to gain experience

1. using the STACK (Push and Pop),
2. dividing up existing code into subroutines,
3. calling subroutines/functions, and
4. using basic parameter passing techniques.

The lab was broken up into three parts. For part A of the lab, we created a program that prompts the user to enter numbers using the keyboard. We also checked that the input met certain restrictions, namely

- the number of entries must be between 3 and 15,
- the divisor must be between 2 and 5, and
- the values entered must be positive.

For part B of the lab, we created a subroutine that, based on the input, finds the min, max, mean, and how many numbers were divisible by the divisor and what those numbers were.

For part C of the lab, we created a subroutine that displayed the results from part B on the MTTTY. Furthermore, we made the subroutine redisplay all the numbers that the user input at the beginning.

2 Design

2.1 Part A

2.2 Part B

2.3 Part C

3 Testing

3.1 Part A

3.2 Part B

3.3 Part C

4 Questions

5 Conclusion

6 Appendix

6.1 Part A Assembler Code

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1
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6.2 Part B Assembler Code

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1
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6.3 Part C Assembler Code

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1
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