

About this circuit:

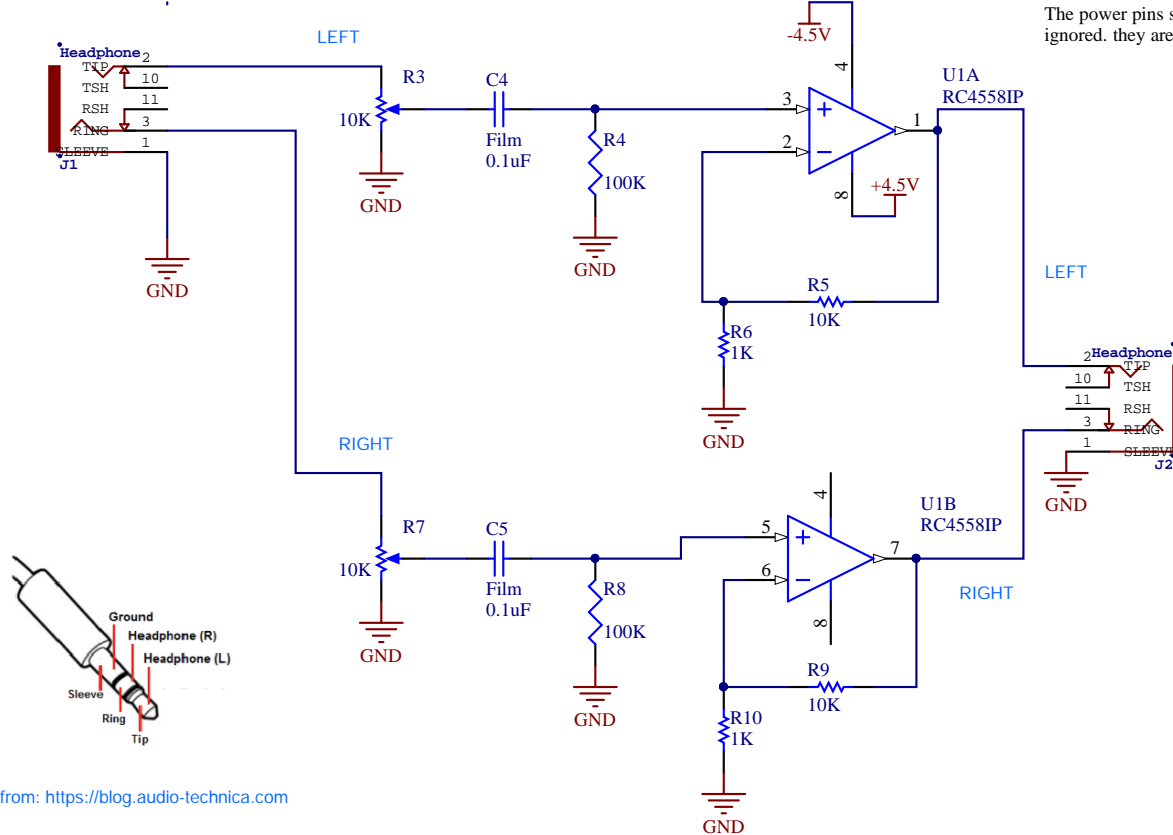
Copied from the design provided at <https://tangentsoft.net/audio/cmoy/>

This is a single-rail op-amp buffer circuit that can drive lower impedance loads like headphones. Some op-amps may need a resistor in series with the output (pins 1 and 7) to operate properly, 10 ohms may be sufficient in many cases.

The breadboard kit doesn't include a dual gang pot so two regular pot are used instead.

Resistors are used to create a V/2 "bias" so that a single battery can be used. In the CMoy design a virtual ground is used, so to keep things clear the supply rails are labeled as +4.5V and -4.5V referenced to that virtual ground. Low frequency or DC currents in the ground will change the actual voltages.

The power pins shown on U1B are the same pins as on U1A and can be ignored. they are an artifact of the schematic tool.



jack from: <https://blog.audio-technica.com>


<http://bostonaes.org/>
Boston Chapter AES



Audio Builders Workshop



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Title <i>CMoy headphone amp</i>			<i>Clockworks Signal Processing LLC</i> <i>http://clk.works</i>	
Size: A	Number:	Revision: 2.1		
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