Lime microsystems
LimeSDR_v1.2
ALTERA CYCLONE IV
FPGA MSEL
3210
LMS7002M
RXRF1_H
RXRF1_L
RXRF1_LA
RFRX2_H
RXRF2_L
RXRF2_H
TXRF2_B
TXRF1_B
TXRF2_A
TXRF1_A
FPGA JTAG
FPGA GPIO
FX3 JTAG
FX3 RST
FX3 BOOT
USB3.0
GND VCC VCC
EXT INT
3.3V FAN
J19,J20
FPGA MSEL
Components
Unpopulated
DDR2 x2