```
th behave
+ clk0 period
+ clk1_period
+ clk2 period
+ clk0
+ clk1
+ clk2
+ reset n
+ smpl nr delay
+ sample width
+ mode
and 29 more...
+ clock0()
+ clock()
+ clock2()
+ res()
+ PROCESS 560()
+ PROCESS 561()
+ PROCESS 562()
+ PROCESS 563()
+ PROCESS 564()
+ PROCESS 565()
```