```
pll top
+ N PLL
+ TXPLL BANDWIDTH TYPE
+ TXPLL CLK0 DIVIDE BY
+ TXPLL CLK0 DUTY CYCLE
+ TXPLL CLK0 MULTIPLY BY
+ TXPLL CLK0 PHASE SHIFT
+ TXPLL CLK1 DIVIDE BY
+ TXPLL CLK1 DUTY CYCLE
+ TXPLL CLK1 MULTIPLY BY
+ TXPLL CLK1 PHASE SHIFT
```

and 53 more...