

arch

- + my_sig_name
- + en_reg
- + en_synch
- + rstn_with_synch_en
- + inst1_diq
- + inst1_fifo_wr
- + inst2_wrfull
- + inst2_q
- + inst2_rdempty
- + inst2_rdusedw
- + inst3_rdreq
- + inst3_diq0
- + diq2_samples
- + fifo_inst
- + rx_pct_data_v2

+ PROCESS_282()