

arch

- + inst0_diq_out_h
- + inst0_diq_out_l
- + inst0_reset_n
- + inst2_data_h
- + inst2_data_l
- + inst3_reset_n
- + mux0_diq_h
- + mux0_diq_l
- + mux0_diq_h_reg
- + mux0_diq_l_reg

+ PROCESS_348()