```
tb behave
+ clk0 period
+ clk1_period
+ smpl nr delay
+ clk0
+ clk1
+ reset n
+ dut0 pct size
+ dut0_pct_data
+ dut0 in_pct_wrreq
+ dut0 in pct last
+ dut0 sample nr
+ pct cnt
+ pct cnt reg
+ my array
+ smpl nr array
+ clock0()
+ clock()
+ res()
+ PROCESS 520()
+ proc name()
```