```
arch
+ inst rea
+ inst_reg_en
+ din rea
+ din reg en
+ dout reg
+ dout_reg_sen
+ dout reg len
+ mem
+ mem we
+ oe
+ spi config data rev
+ mcfg components
+ GW TEST RES
+ inst reg proc()
+ din_reg_proc()
+ dout reg proc()
+ ram()
+ PROCESS 398()
+ inst reg proc()
+ din reg proc()
+ dout reg proc()
+ ram()
+ PRÖCESS 410()
```