```
ddr2 phy alt mem phy seq
+ FAMILY
+ MEM IF MEMTYPE
+ SPEED GRADE
+ FAMILYGROUP ID
+ MEM IF DOS WIDTH
+ MEM IF DWIDTH
+ MEM IF DM WIDTH
+ MEM IF DQ PER DQS
+ DWIDTH RATIO
+ CLOCK INDEX WIDTH
and 121 more...
             +dgwb report prefix
           rtΙ
+ t dawb state
+ c seg addr cmd config
+ sig addr cmd
+ sig dgwb state
+ sig dgwb last state
+ access complete
+ generate wdata
+ current cs
+ sig ctrl dawb
+ C_BURST T
and 6 more...
+ dqs pattern()
+ current cs proc()
+ master dgwb state proc()
+ set wlat dq rep width()
+ ac write proc()
+ ac handshake proc()
```