```
singl clk with ref test
+ refclk
+ reset n
+ clk0
+ test_en
+ test cnt0
+ test complete
+ test_pass_fail
+ ieee
+ std logic 1164
+ numeric std
      clock test
  + FX3 clk
  + reset n
  + test_en
  + test frc err
  + test cmplt
  + test rez
  + Si5351C clk 0
  + Si5351C clk 1
  + Si5351C clk 2
  + Si5351C clk 3
```

and 18 more...