```
ddr2_phy_alt_mem_phy_seq
  + FAMILY
  + MEM IF MEMTYPE
  + SPEED GRADE
  + FAMILYGROUP ID
  + MEM IF DQS WIDTH
  + MEM IF DWIDTH
  + MEM IF DM WIDTH
  + MEM IF DQ PER DQS
  + DWIDTH RATIO
  + CLOCK INDEX WIDTH
  and 121 more...
               +admin_report_prefix
           struct
+ c_max_mode_reg_index
+ c_init_prech_delay
+ c trp in clks
+ c tmrd in clks
+ c tmod in clks
+ c trrd min in clks
+ c trcd min in clks
+ c trfc min in clks
+ c trefi min in clks
+ c max num stacked refreshes
and 37 more...
+ PROCESS 9()
+ PROCESS 10()
+ PROCESS 11()
+ PROCESS 12()
+ PROCESS 13()
+ PROCESS 14()
+ PROCESS 15()
+ PROCESS
           16()
+ PROCESS 17()
+ PROCESS 18()
+ PROCESS
           19()
+ PROCESS 20()
+ PROCESS 21()
+ PROCESS 22()
```

+ PROCESS 23()