```
arch
+ en reg
+ phase reg
+ cnt reg
+ updown reg
+ busy reg
+ current phase step cnt
+ phase step cnt
+ check phase done cnt
+ pll phasedone neg reg
+ pll phasedone pos reg
and 7 more...
+ PROCESS 220()
+ PROCESS 221()
+ PROCESS 222()
+ fsm f()
+ fsm()
+ PROCESS 223()
+ PROCESS 224()
```