```
fifo inst(2)
+ dev family
+ wrwidth
+ wrusedw witdth
+ rdwidth
+ rdusedw width
+ show_ahead
+ reset n
+ wrclk
+ wrreq
+ data
and 13 more...
  sync fifo rw
+ dev family
+ data w
+ wclk
+ rclk
+ reset n
+ sync_en
+ sync_data
+ sync_q
+ ieee
+ std logic 1164
+ numeric std
```