```
ddr2 mem model ram
        module
+ ARRAY_DEPTH
+ data
+ rdaddress
+ wraddress
+ wrclock
+ wren
+ q
+ altera
+ altera europa support lib
+ altera mf
+ altera_mf_components
+ ieee
+ std_logic_1164
+ std_logic_arith
+ std logic unsigned
    ddr2 mem model
    + mem addr
    + mem ba
    + mem cas n
    + mem_cke
    + mem clk
    + mem clk n
    + mem_cs_n
    + mem dm
    + mem odt
    + mem_ras_
    and 16 more...
  ddr2 example top tb
+ gMEM_CHIPSELS
+ gMEM CS PER RANK
+ gMEM_NUM_RANKS
+ gMEM_BANK_BITS
+ gMEM ROW BITS
+ gMEM_COL_BITS
+ gMEM_ADDR_BITS
+ gMEM DQ PER DQS
+ DWIDTH RATIO
+ DM_DQS_WIDTH
and 27 more...
```