

diq1_test_data

- + dev_family
- + iq_width
- + clk
- + reset_n
- + trxiqpulse
- + ddr_en
- + mimo_en
- + ch_en
- + fidm
- + fifo_wrfull
- + fifo_wrreq
- + fifo_q
- + ieee
- + std_logic_1164
- + numeric_std