```
ddr2_mem_model_ram
_module
+ ARRAY_DEPTH
+ data
+ rdaddress
+ wraddress
+ wrclock
```

+ wren

+ q + altera + altera_europa_support_lib + altera_mf

+ altera_mf_components + ieee

+ std_logic_1164 + std_logic_arith

+ std_logic_unsigned

ddr2_mem_model
+ mem_addr
+ mem_ba
+ mem_cas_n
+ mem_cke
+ mem_clk
+ mem_clk
+ mem_ddn
+ mem_odt
+ mem_odt
+ mem_ras_n
and 16 more...