```
arch
+ test_ptrn_en sync
+ mux sel sync
+ isnt0 diq h
+ isnt0 dia I
+ inst1 data h
+ inst1 data I
+ mux0 dig h
+ mux0 dig I
+ mux0 dia h rea
+ mux0 dig I reg
and 8 more...
+ mux0 reg()
+ mux1_reg()
+ mux2 reg()
```