```
arch
+ cnt ref clk
+ cnt ref clk en
+ cnt clk0
+ cnt clk en
+ test en reg
+ cnt clk0 en reg0
+ cnt clk0 en reg1
+ state type
+ current state
+ next state
+ PROCESS 391()
+ PROCESS 392()
+ PROCESS 393()
+ fsm f()
+ fsm()
+ PROCESS 394()
+ PROCESS 395()
```