```
ddr2_phy_alt_mem_phy_seq
+ FAMILY
+ MEM IF MEMTYPE
+ SPEED GRADE
+ FAMILYGROUP ID
+ MEM IF DOS WIDTH
+ MEM IF
          DWIDTH
+ MEM IF DM WIDTH
+ MEM IF DQ PER DQS
+ DWIDTH RATIO
+ CLOCK INDEX WIDTH
and 121 more...
             +ac report prefix
  ddr2 phy alt mem phy
     addr cmd pkg
  + ieee
  + std logic 1164
  + numeric std
  + c max addr bits
  + c_max_ba bits
  + c max ranks
  + c max mode reg bit
  + c max cmds per clk
  + t addr cmd
  + addr
  and 22 more...
  + defaults()
  + reset()
  + int pup reset()
  + deselect()
  + precharge all()
  + precharge all()
  + precharge bank()
  + activate()
  + write()
  + read()
  and 20 more...
```