```
ddr2 full mem model
       ram module
+ data
+ rdaddress
+ rdclken

    wraddress

+ wrclock
+ wren
+ a
+ altera
+ altera europa support lib
+ altera mf
+ altera mf components
+ ieee
+ std_logic_1164
+ std logic arith
+ std logic unsigned
  ddr2 full mem model
  + mem addr
  + mem ba
  + mem cas n
  + mem cke
  + mem clk
  + mem clk n
  + mem cs n
  + mem dm
```

+ mem_odt + mem_ras_n and 16 more...