```
ddr2 phy alt mem phy seq
+ FAMII Y
+ MEM IF MEMTYPE
+ SPEED GRADE
+ FAMILYGROUP ID
+ MEM IF DOS WIDTH
+ MEM IF DWIDTH
+ MEM IF DM WIDTH
+ MEM IF DQ PER DQS
+ DWIDTH RATIO
+ CLOCK INDEX WIDTH
and 121 more...
            +mmi_report_prefix
         struct
 + c pll 360 sweeps
 + c response lat
 + c codeword
 + c int iram start size
 + c slv hl stage enable
 + c hl stage enable
 + int wdata
 + int rdata
 + int address
 + int read
 and 33 more...
 + max()
 + PROCESS 1()
 + PROCESS 2()
 + PROCESS 3()
 + PROCESS
             4()
 + PROCESS 5()
 + PROCESS 6()
 + PROCESS 7()
 + PROCESS 8()
```