```
arch

+ inst0_reset_n
+ inst0_fifo_wrreq
+ inst0_fifo_data
+ inst0_tx_fifo_wr
+ inst0_tx_fifo_data
+ inst0_wfm_data
+ inst0_wfm_fifo_wr
+ inst1_reset_n
```

+ inst1_DIQ_h + inst1_DIQ_I and 16 more...

+ PROCESS 375()