```
clk no ref test
+ clk
+ reset n
+ test_en
+ test_cnt
+ test complete
+ test pass fail
+ ieee
+ std logic 1164
+ numeric std
   clock test
+ FX3 clk
+ reset n
+ test en
+ test frc err
+ test cmplt
+ test rez
+ Si5351C_clk_0
+ Si5351C_clk_
+ Si5351C
           clk
+ Si5351C clk 3
and 18 more...
```