```
ddr2 phy alt mem phy seq
+ FAMILY
+ MEM IF MEMTYPE
+ SPEED GRADE
+ FAMILYGROUP ID
+ MEM IF DQS WIDTH
+ MEM_IF_DWIDTH
+ MEM IF DM WIDTH
+ MEM IF DQ PER DQS
+ DWIDTH RATIO
+ CLOCK INDEX WIDTH
and 121 more...
             +ctrl report prefix
          struct
 + c done timeout count
 + c ack timeout count
 + c ticks per ms
 + c ticks per 10us
 + int ctl init fail
 + int ctl init success
 state
 + last state
 + dis state
 + hold state
 and 34 more...
 + find dis bit()
 + find cmd()
 + mcs rw state()
 + PROCESS 37()
 + PROCESS 38()
 + PROCESS 39()
 + PROCESS 40()
 + PROCESS 41()
 + PROCESS 42()
 + PROCESS 43()
 and 10 more...
```