```
arch
+ inst0 beat
+ inst1 board gpio ovrd
+ inst1 board gpio rd
+ inst1 board gpio dir
+ inst1 board gpio val
+ inst1 periph input rd 0
+ inst1 periph input rd 1
+ inst1 periph output
ovrd 0
+ inst1 periph output
val 0
+ inst1 periph output
ovrd 1
and 7 more...
+ PROCESS 273()
+ PROCESS 274()
+ PROCESS 275()
```