```
th behave
+ clk0 period
+ clk1 period
+ clk0
+ clk1
+ reset n
+ dut0 data in
+ dut0 data in valid
+ dut0 sample width
+ dut0 data out
+ dut0 data out valid
and 26 more...
+ clock0()
+ clock()
+ res()
+ DATA12 CNT()
+ DATA14 CNT()
+ DATA16 CNT()
+ proc name()
+ PROCESS 475()
+ PROCESS 476()
+ PROCESS 477()
```