```
arch
+ data in reg
+ d out
+ d out reg
+ smpl cnt
+ smpl cnt max
+ compr array
+ compr array reg
+ tst out
+ tst msb
+ tst Isb
+ wreq_sig
+ wreg cnt
+ wreq cnt max
+ data in valid reg
+ PROCESS 284()
```

+ PROCESS_285() + PROCESS_286() + PROCESS_287()