```
ddr2 controller phy
+ das delay ctrl import
+ das offset delay ctrl
+ global reset n
+ hc_scan_ck
+ hc_scan_din
+ hc scan enable access
+ hc scan enable dm
+ hc scan enable da
+ hc_scan enable das
+ hc scan enable dgs
config
and 75 more...
          ddr2
    + local address
    + local write req
    + local read req
    + local_burstbegin
    + local wdata
    + local be
    + local size
    + global reset n
    + pll ref clk
    + soft reset n
    and 25 more...
```