```
arch
+ inst0 fifo wrreq
+ inst0 fifo wdata
+ inst0 reset n
+ inst1 fifo wrreq
+ inst1 fifo wdata
+ inst1 reset n
+ mux fifo wrrea
+ mux fifo wdata
+ fifo wrrea rea
+ fifo wdata rea
+ inst0 reset proc()
+ inst1 reset proc()
```

+ PROCESS 349()