

## ddr2\_phy

- + pll\_ref\_clk
- + global\_reset\_n
- + soft\_reset\_n
- + ctl\_dqs\_burst
- + ctl\_wdata\_valid
- + ctl\_wdata
- + ctl\_dm
- + ctl\_addr
- + ctl\_ba
- + ctl\_cas\_n
- and 48 more...