```
arch
+ state type
+ current state
+ next state
+ smpl nr array type
+ smpl nr array
+ pct smpl nr less
+ pct smpl nr sync dis
+ pct buff rdy reg0
+ pct buff rdy reg1
+ pct buff rdy reg2
+ pct buff rdy reg3
+ lpm compare
+ PROCESS 522()
```

+ PROCESS\_523() + PROCESS\_524()