```
arch
+ inst0 data64 out
+ inst0 data48 in
+ inst0 data out valid
+ inst1 data64 out
+ inst1 data56 in
+ inst1 data out valid
+ mux0 data64
+ mux0 data out valid
+ mux0 sel
+ mux0 data64 reg
and 8 more...
+ PROCESS 196()
+ PROCESS 197()
+ PROCESS 306()
+ PROCESS 307()
```