```
rt۱
+ ddr2 mem model
+ SAMPLE DELAY
+ GATE BOARD DOS DELAY
+ GATE BOARD CLK DELAY
+ aMEM IF TRCD PS
+ aMEM IF TWTR PS
+ gMEM IF TRFC_PS
+ aMEM IF TRP PS
+ CLOCK TICK IN NS
+ aMEM TDQSQ NS
and 42 more
+ auk to integer()
+ auk to string()
+ auk to string()
+ "="()
+ bool to integer()
+ PROCESS 76()
+ PROCESS 77()
+ endit()
+ PROCESS 78()
+ PROCESS 79()
and 30 more...
```