```
arch
+ in pct data rea
+ wr cnt
+ wr cnt end
+ pct hdr 0 rea
+ pct hdr 0 valid reg
+ pct hdr 1 rea
+ pct hdr 1 valid rea
+ pct data wrreg int
+ buff sel
+ next buff sel cnt
and 12 more
+ PROCESS 542()
+ PROCESS 543()
+ PROCESS_544()
+ PROCESS 545()
+ PROCESS 546()
+ fsm f()
+ fsm()
+ in reg proc()
+ pct count proc()
+ hdr0 reg()
and 6 more...
```