```
periphcfq pkg
+ ieee
+ std logic 1164
+ numeric std
+ t FROM PERIPHCEG
+ BOARD GPIO OVRD
+ BOARD GPIO DIR
+ BOARD GPIO VAL
+ PERIPH OUTPUT OVRD 0
+ PERIPH OUTPUT VAL 0
+ PERIPH OUTPUT OVRD 1
+ PERIPH OUTPUT VAL 1
+ t TO PERIPHCEG
+ BOARD GPIO RD
+ PERIPH INPUT RD 0
+ PERIPH INPUT RD 1
```