

wr_rx_fifo_v2

- + sample_width
- + clk
- + reset_n
- + fr_start
- + mimo_en
- + ch_en
- + en
- + diq_h
- + diq_l
- + diq
- + fifo_wr
- + fifo_wfull
- + ieee
- + std_logic_1164
- + numeric_std