

ddr2_mem_model_ram
_module

- + ARRAY_DEPTH
- + data
- + rdaddress
- + wraddress
- + wrclock
- + wren
- + q
- + altera
- + altera_europa_support_lib
- + altera_mf
- + altera_mf_components
- + ieee
- + std_logic_1164
- + std_logic_arith
- + std_logic_unsigned