

arch

- + test_ptrn_en_sync
- + reset_n_sync
- + tx_pct_loss_sync
- + tx_pct_loss_clr_sync
- + sample_width_sync
- + mode_sync
- + trxiqpulse_sync
- + ddr_en_sync
- + mimo_en_sync
- + ch_en_sync
- and 24 more...

- + ftr1_trigger_hold()
- + delay_registers()