```
arch
+ inst0 data48 out
+ inst0 data out valid
+ inst1 data56 out
+ inst1 data out valid
+ inst2 data64 out
+ inst2 data out valid
+ mux0 data128
+ mux0 data out valid
+ mux0 sel
+ mux0 data128 reg
and 6 more...
```

+ PROCESS_480() + PROCESS_481()