```
arch
+ inst0 data64 out
+ inst0 data out valid
+ inst1 data64 out
+ inst1 data out valid
+ inst2 data64 out
+ inst2 data out valid
+ mux0 data64
+ mux0 data out valid
+ mux0 sel
+ mux0 data64 rea
```

and 6 more...

+ PROCESS_478() + PROCESS_479()