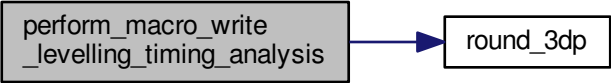


perform\_macro\_write  
\_levelling\_timing\_analysis



```
graph LR; A[perform_macro_write  
_levelling_timing_analysis] --> B[round_3dp]
```

round\_3dp