```
ddr2 phy alt mem phy
      iram addr pkg
+ ieee
+ std logic 1164
+ numeric std
+ c ihi size
+ t base hdr addresses
+ base hdr
+ rrp
+ safe dummy
+ required addr bits
+ defaults()
+ rrp pll phase mult()
+ iram wd for full rrp()
+ iram wd for one pin rrp()
+ calc iram addresses()
```