```
I FSR
+ reg with
+ seed
+ clk
+ reset n
+ en
+ data
+ ieee
+ std logic 1164
+ numeric std
ddr2 cmd fifo tst
+ cntrl rate
+ cntrl bus size
+ addr size
+ Icl burst length
+ cmd fifo size
+ test length
+ clk
+ reset n
+ en
+ test type
and 14 more...
```