```
ddr2 phy alt mem phy seq
+ FAMILY
+ MEM IF MEMTYPE
+ SPEED GRADE
+ FAMILYGROUP ID
+ MEM IF DQS WIDTH
+ MEM IF DWIDTH
+ MEM IF DM WIDTH
+ MEM IF DQ PER DQS
+ DWIDTH RATIO
+ CLOCK INDEX WIDTH
and 121 more...
             +regs report prefix
 ddr2 phy alt mem phy
        regs pkg
 + ieee
 + std logic 1164
 + numeric std
 + work
 + ddr2 phy alt mem phy
 record pkg
 + ddr2_phy_alt_mem_phy
 constants pkg
 + t cal status
 + iram addr width
 + out of mem
 + contested access
 and 56 more...
 + defaults()
 + defaults()
 + read()
 + defaults()
 + defaults()
 + read()
 + defaults()
 + defaults()
 + read()
 + defaults()
 and 37 more...
```