

## wr\_rx\_fifo\_v3

- + sample\_width
- + clk
- + reset\_n
- + fr\_start
- + mimo\_en
- + ch\_en
- + en
- + diq\_h
- + diq\_l
- + diq
- + fifo\_wr
- + fifo\_wfull
- + ieee
- + std\_logic\_1164
- + numeric\_std