```
ddr2 controller phy
+ das delay ctrl_import
+ das offset delay ctrl
+ global reset n
+ hc_scan_ck
+ hc scan din
+ hc scan enable access
+ hc_scan enable dm
+ hc scan enable da
+ hc_scan_enable_dqs
+ hc scan enable dgs
config
and 75 more...
           ddr2
    + local address
    + local write req
    + local read req
    + local burstbegin
    + local wdata
    + local be

    + local size

    + global reset n

    + pll ref clk
    + soft reset n
    and 25 more...
            Λ
       ddr2 tester
     + global reset n
     + pll ref clk
     + soft reset n
    + begin_test
    + insert error
     + mem_odt
     + mem cs n
     + mem cke
     + mem addr
     + mem ba
     and 16 more...
```