

/home/erik/git/dev
/LimeSDR-USB_GW/ip/i2c
_opencores_v13/HAL/src
/i2c_opencores.c

```
graph TD; A["/home/erik/git/dev  
/LimeSDR-USB_GW/ip/i2c_opencores_v13/HAL/src  
/i2c_opencores.c"] --> B["alt_types.h"]; A --> C["i2c_opencores_regs.h"]; A --> D["i2c_opencores.h"];
```

alt_types.h

i2c_opencores_regs.h

i2c_opencores.h