lime::FPGA

connection

+ FPGA()
+ ~FPGA()
+ SetConnection()
+ GetConnection()
+ StartStreaming()
+ StopStreaming()
+ ResetTimestamp()
+ UploadWFM()
+ SetInterfaceFreq()
+ SetInterfaceFreq()
+ DetectRefClk()
+ FPGAPacketPayload2Samples()
+ Samples2FPGAPacketPayload()
# SetPllFrequency()
# SetDirectClocking()
- ReadRawStreamData()
- SetPllClock()

lime::FPGA_Mini

+ FPGA_Mini()
+ ~FPGA_Mini()
+ SetInterfaceFreq()
+ SetInterfaceFreq()
+ UploadWFM()
- ReadRawStreamData()