Milestone2

Group 40
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For milestone2 I finished calculate and write the first block of data to SRAM.You can see the value at time 17760000ps,at state S_M2_40 I start to write the first value to the first SRAM address, and keep writing it until address 1123 where the last line of first block end.

I have two RAM,RAM0 and RAM1. RAM0 have two ports called read_addressa0 and read_addressb0.RAM1 have two ports called read_addressa1 and read_addressb1.I save C value in both RAM0 and RAM1 from address 64 to 127.

First ,I fetch all first block S' data and save it to RAM0 address 0 to 63. And then I compute T value and write it in RAM1 address 0 to 63. Then I read two C^t data and one T from RAM0 and RAM1 to compute data S, And save S to RAM0 address 0 to 63. In the end I write the first block of S back to SRAM two per one address. When I run the simulation it's shows the correct value and no mismatches.