#### **Computer System**

1) Figure 1 shows a high level system block diagram of the computer system used for the case study.

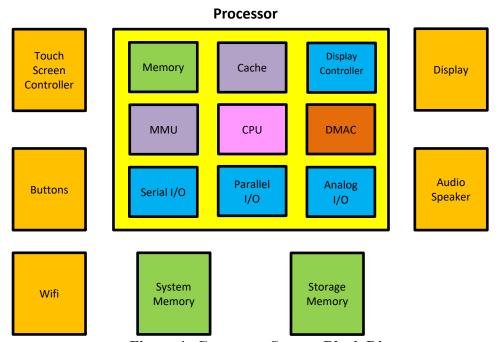


Figure 1: Computer System Block Diagram

**Table 1: Processor Information** 

Overview	Peripheral	Electrical Interface
Von-Neumann Architecture	• SPI	• V(OH):
• 12 Data Registers (32-bit)	Max clock speed: 50 Mhz	3V(max), 2.4V(min)
• 256 Kbyte on-chip SRAM,	• UART	• V(OL):
512 Kbyte on-chip Flash,	Max baud rate: 115200 Kbps	0.8V(max), 0V(min)
• 2 Kbyte on-chip EEPROM	• I2C	• V(IH):
• 4 Kbyte Cache (Data and	Max clock rate: 400 Khz	3V(max), 1.8V(min)
Instruction)	• USB 2.0 Host	• V(IL):
• 400 Mhz CPU clock	Max bitrate: 480Mbps	1.2V(max), 0V(min)
• 200 Mhz External Parallel	• ADC	
Bus interface (16-bits)	Max Sampling rate: 48 Khz	
DMA Controller	Data width: 16bits	
Memory Management Unit	• DAC	
	Max Sampling rate: 48 Khz	
	Data width: 16bits	
	• Display Controller	
	24-bit bus	
	Maximum speed 270 Mhz	

#### **Device/Module Information**

# 2) Touch Screen Controller

(a) Part Number: TS001SP

Overview	SPI	Electrical Interface
Maximum screen size	Max Clock rate: 10Mhz	• V(OH):
supported = $1080x720$	• Full duplex operation	5V(max), 4.2V(min)
• 8Kbyte on-chip memory		• V(OL):
• 200Hz position report		1.3V(max), 0V(min)
rate for $(x,y)$		• V(IH):
coordinates		5V(max), 3.6V(min)
• SPI interface		• V(IL):
		1.8V(max), 0V(min)

(b) Part Number: TS002UART

Overview	UART	Electrical Interface
Maximum screen size	Max Baud rate:	• V(OH):
supported = $960x640$	115200bps	3V(max), 2.4V(min)
• 4Kbyte on-chip memory	• Full duplex operation	• V(OL):
• 100Hz position report		0.8V(max), 0V(min)
rate for $(x,y)$		• V(IH):
coordinates		3V(max), 1.8V(min)
• UART interface		• V(IL):
		1.2V(max), 0V(min)

# (c) Part Number: TS003UAR

Overview	UART	Electrical Interface
• Maximum screen size	Max Baud rate:	• V(OH):
supported = $960x640$	115200bps	5V(max), 4.2V(min)
• 4Kbyte on-chip memory	• Full duplex operation	• V(OL):
• 100Hz position report		1.3V(max), 0V(min)
rate for $(x,y)$		• V(IH):
coordinates		5V(max), 3.6V(min)
• UART interface		• V(IL):
		1.8V(max), 0V(min)

# 3) Wifi Module

(a) Part Number: WIFI0001N

Overview	USB	Electrical Interface
• Max wireless data rate	• Supports up to USB2.0,	• V(OH):
100Mbps	480Mbps.	5V(max), 4.2V(min)
• USB interface		• V(OL):
		1.3V(max), 0V(min)
		• V(IH):
		5V(max), 3.6V(min)
		• V(IL):
		1.8V(max), 0V(min)

(b) Part Number: WIFI0010AC

Overview	SPI	Electrical Interface
• Max wireless data rate	• Max Clock rate: 50Mhz	• V(OH):
100Mbps	• Full duplex operation	3V(max), 2.4V(min)
• SPI interface		• V(OL):
		0.8V(max), 0V(min)
		• V(IH):
		3V(max), 1.8V(min)
		• V(IL):
		1.2V(max), 0V(min)

(c) Part Number: WIFI0015AC

Overview	SPI	Electrical Interface
• Max wireless data rate	Max Clock rate: 50Mhz	• V(OH): 5V(max),
100Mbps	• Full duplex operation	4.2V(min)
• SPI interface		• V(OL): 1.3V(max),
		0V(min)
		• V(IH): 5V(max),
		3.6V(min)
		• V(IL): 1.8V(max),
		0V(min)

## 4) Memory

(a) Part Number: DRAM0001-4M8

Overview	Electrical Interface
• 4 Mbyte Dynamic RAM	• V(OH): 5V(max), 4.2V(min)
• 8-bit Parallel interface	• V(OL): 1.3V(max), 0V(min)
• Maximum data strobe rate = 100Mhz	• V(IH): 5V(max), 3.6V(min)
	• V(IL): 1.8V(max), 0V(min)

#### (b) Part Number: DRAM0002-16M16

Overview	Electrical Interface
• 16 Mbyte Dynamic RAM	• V(OH): 3V(max), 2.4V(min)
• 16-bit Parallel interface	• V(OL): 0.8V(max), 0V(min)
• Maximum data strobe rate = 100Mhz	• V(IH): 3V(max), 1.8V(min)
	• V(IL): 1.2V(max), 0V(min)

# (c) Part Number: SRAM0001-1M8

Overview	Electrical Interface
• 1 Mbyte Static RAM	• V(OH): 5V(max), 4.2V(min)
8-bit Parallel interface	• V(OL): 1.3V(max), 0V(min)
• Maximum data strobe rate = 200Mhz	• V(IH): 5V(max), 3.6V(min)
	• V(IL): 1.8V(max), 0V(min)

## (d) Part Number: SRAM0002-2M16

Overview	Electrical Interface
• 2 Mbyte Static RAM	• V(OH): 3V(max), 2.4V(min)
• 16-bit Parallel interface	• V(OL): 0.8V(max), 0V(min)
• Maximum data strobe rate = 200Mhz	• V(IH): 3V(max), 1.8V(min)
	• V(IL): 1.2V(max), 0V(min)

## (e) Part Number: EEPROM0001-256K

Overview	Electrical Interface
• 256 Kbyte EEPROM	• V(OH): 3V(max), 2.4V(min)
• SPI interface	• V(OL): 0.8V(max), 0V(min)
• Maximum SPI clock rate = 50Mhz	• V(IH): 3V(max), 1.8V(min)
• Page Size = 64bytes	• V(IL): 1.2V(max), 0V(min)

# (f) Part Number: NAND0001-64M

Overview	Electrical Interface
• 64 Mbyte NAND Flash	• V(OH): 3V(max), 2.4V(min)
8-bit parallel interface	• V(OL): 0.8V(max), 0V(min)
• Maximum data strobe rate = 50Mhz	• V(IH): 3V(max), 1.8V(min)
• Page Size = 64Kbytes	• V(IL): 1.2V(max), 0V(min)

# (g) Part Number: NOR0001-1M

Overview	Electrical Interface
• 1 Mbyte NOR Flash	• V(OH): 3V(max), 2.4V(min)
• 16-bit parallel interface	• V(OL): 0.8V(max), 0V(min)
• Maximum data strobe rate = 10Mhz	• V(IH): 3V(max), 1.8V(min)
• Page Size = 4Kbytes	• V(IL): 1.2V(max), 0V(min)

# (h) Part Number: HDD001

Overview	Electrical Interface
<ul> <li>4 surfaces</li> <li>1024 tracks per surface</li> <li>128 sectors per track</li> <li>512 bytes/sector</li> <li>Track-to-track seek time = 5 msec</li> <li>Rotational speed = 5000 RPM</li> </ul>	<ul> <li>V(OH): 5V(max), 4.2V(min)</li> <li>V(OL): 1.3V(max), 0V(min)</li> <li>V(IH): 5V(max), 3.6V(min)</li> <li>V(IL): 1.8V(max), 0V(min)</li> </ul>
• MTTF = 500,000 hours	

# (i) Part Number: HDD002

Overview	Electrical Interface
<ul> <li>8 heads</li> <li>1024 cylinders</li> <li>256 sectors per track</li> <li>512 bytes/sector</li> <li>Track-to-track seek time = 4 msec</li> <li>Rotational speed =10000 RPM</li> </ul>	<ul> <li>V(OH): 5V(max), 4.2V(min)</li> <li>V(OL): 1.3V(max), 0V(min)</li> <li>V(IH): 5V(max), 3.6V(min)</li> <li>V(IL): 1.8V(max), 0V(min)</li> </ul>
<ul> <li>MTTF = 1,000,000 hours</li> </ul>	

# 5) Display Controller

(a) Part Number: LCD0001-HD

Overview	Electrical Interface
• Supports up to 1920x1080 pixels display	• V(OH): 3V(max), 2.4V(min)
• 24-bit Parallel interface	• V(OL): 0.8V(max), 0V(min)
• Maximum clock rate = 200Mhz	• V(IH): 3V(max), 1.8V(min)
• 16Mbyte video buffer	• V(IL): 1.2V(max), 0V(min)

## (b) Part Number: LCD0002-XVGA

Overview	Electrical Interface
• Supports up to 960x640 pixels display	• V(OH): 3V(max), 2.4V(min)
• SPI interface	• V(OL): 0.8V(max), 0V(min)
• Maximum clock rate = 20Mhz	• V(IH): 3V(max), 1.8V(min)
• 1Mbyte video buffer	• V(IL): 1.2V(max), 0V(min)