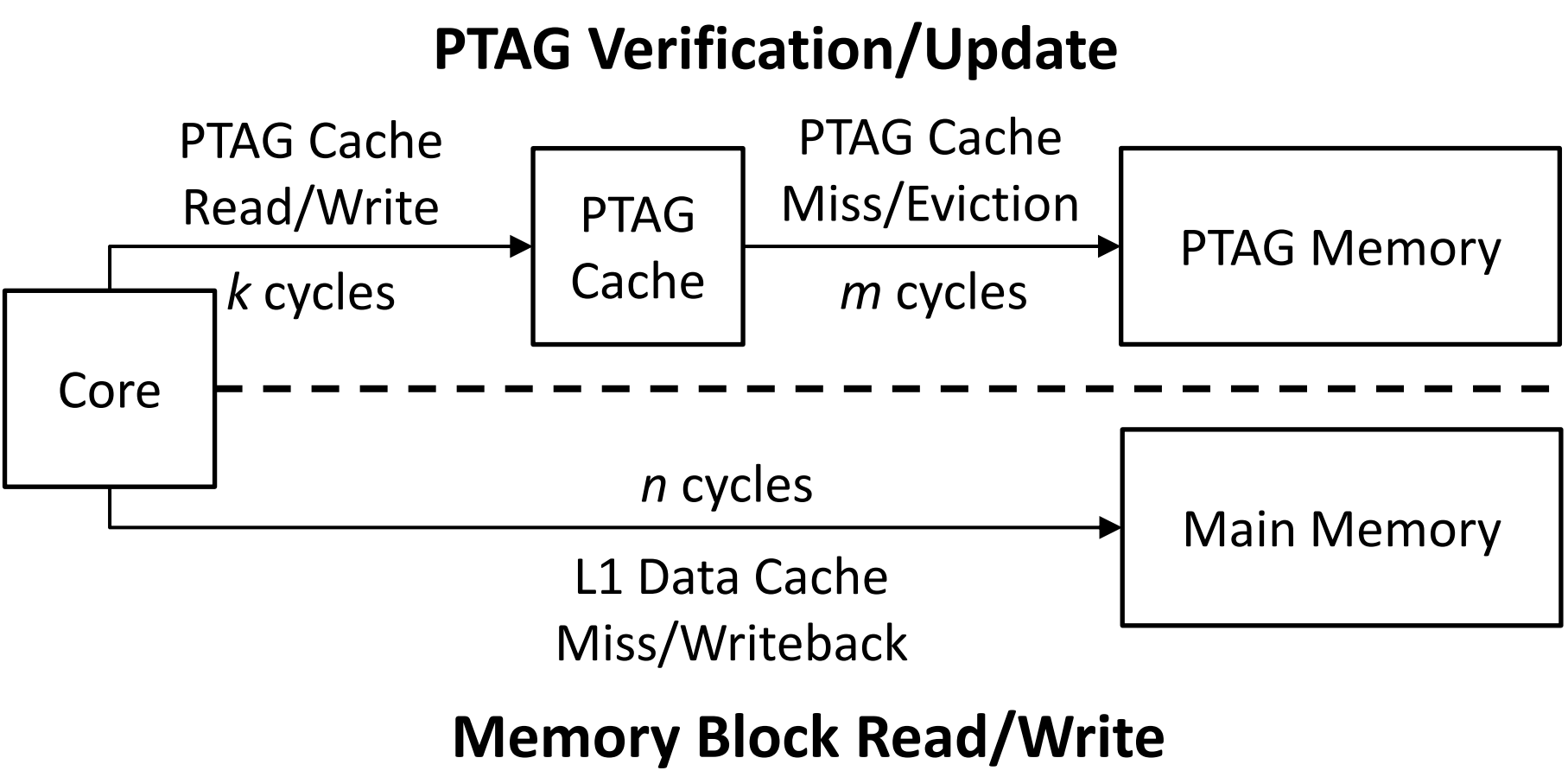
PTAG Verification/Update
PTAG Cache
Read/Write
k cycles
PTAG
Cache
PTAG Cache
Miss/Eviction
m cycles
PTAG Memory
Core
n cycles
L1 Data Cache
Miss/Writeback
Main Memory
Memory Block Read/Write