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|  | УТВЕРЖДАЮ |
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ОТЧЕТ-АННОТАЦИЯ

этапу 1 НИР «Разработка программного   
средства синтеза топологии микросхем»  
к Договору №\_\_\_\_\_\_ от \_\_\_\_\_\_\_

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| Ответственный исполнитель | \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_Ф.И.О. |
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# Введение

В рамках данного документа сформированы предложения по контрольному примеру для системы синтеза топологии микросхем.

# Описание контрольного примера для системы синтеза топологии микросхем

Для отработки работоспособности алгоритмов и отработки взаимодействия№ 199-974-216/06.06.2016. Контрольные примеры включают в себя малый пример для отладки (small\_test) с количеством компонент 179 и количеством цепей 75, а также большой пример (big\_test) cс количеством компонент 148024 и количеством цепей 148949.

# Перечень и описание файлов контрольного примера

Ба контрольных примера включают в себя:

1. библиотечные файлы LEF (Layout exchange format) с описанием конструкции кристалла и библиотечных элементов:   
   BMK\_040\_v1.7.lef  
   PADS\_GA\_Tol\_CSB\_13\_03\_29\_v4.lef  
   Tech.lef
2. Файлы обмена проектами (Design Exchange Format):

small\_test\_v3\_1.def для малого примера

big\_test2\_v3\_0.def для большого примера

# Фрагмент файла BMK\_040\_v1.7.lef

#######################################################################

# NiiiS #

# COMPANY CONFIDENTIAL #

########################################################################

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# NiiiS #

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########################################################################

# FileName: BMK\_040.lef #

# Description: Cadence LEF File #

# Preview export LEF #

# Preview sub-version 5.10.41.500.4.83 #

########################################################################

# Ref libs: BMK\_040 #

# Tech lib name: TECH035\_SOI #

# Tech file name: techfile.cds #

########################################################################

# v1.7 11 Apr 13 Created by Mezentcev Alexander #

# #

# Version History: #

# v1.1-3 +m +l +t +s #

# v1.4 na8x pin e #

# v1.5 change sdfrrsx1-4 #

# v1.6 missing pin in sdfrrsqx1-4; size and pin in mu2ix2 no5x2-4 #

# v1.7 add cap #

########################################################################

VERSION 5.4 ;

NAMESCASESENSITIVE ON ;

DIVIDERCHAR "|" ;

BUSBITCHARS "[]" ;

UNITS

DATABASE MICRONS 100 ;

END UNITS

MANUFACTURINGGRID 0.050000 ;

SITE CoreSite54

SYMMETRY Y ;

CLASS CORE ;

SIZE 3.00 BY 50.40 ;

END CoreSite54

SITE CoreSite5

SYMMETRY Y ;

CLASS CORE ;

SIZE 3.00 BY 12.60 ;

END CoreSite5

SITE CoreSite52

SYMMETRY Y ;

CLASS CORE ;

SIZE 3.00 BY 25.20 ;

END CoreSite52

MACRO na8x1

CLASS CORE ;

FOREIGN na8x1 0 0 ;

ORIGIN 0.00 0.00 ;

SIZE 36.00 BY 12.60 ;

SYMMETRY X ;

SITE CoreSite5 ;

PIN q

DIRECTION OUTPUT ;

PORT

LAYER MET1 ;

RECT 34.80 3.05 35.70 9.55 ;

END

END q

PIN h

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 24.30 7.25 25.20 8.15 ;

END

END h

PIN d

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 12.30 4.45 13.20 5.35 ;

END

END d

PIN f

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 30.30 6.55 31.20 7.45 ;

END

END f

PIN c

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 0.30 4.45 1.20 5.35 ;

END

END c

PIN b

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 3.30 5.85 4.20 6.75 ;

END

END b

PIN a

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 6.30 6.55 7.20 7.45 ;

END

END a

PIN g

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 27.30 5.85 28.20 6.75 ;

END

END g

PIN e

DIRECTION INPUT ;

ANTENNAGATEAREA 1.68 LAYER MET1 ;

PORT

LAYER MET1 ;

RECT 9.30 7.25 10.20 8.15 ;

END

END e

PIN gnd!

DIRECTION INOUT ;

USE GROUND ;

SHAPE ABUTMENT ;

PORT

LAYER MET1 ;

RECT 0.00 0.00 36.00 2.20 ;

END

END gnd!

PIN vdd!

DIRECTION INOUT ;

USE POWER ;

SHAPE ABUTMENT ;

PORT

LAYER MET1 ;

RECT 0.00 10.40 36.00 12.60 ;

END

END vdd!

…

END LIBRARY

# Фрагмент файла PADS\_GA\_Tol\_CSB\_13\_03\_29\_v4.lef

########################################################################

# NiiiS #

# COMPANY CONFIDENTIAL #

########################################################################

# Copyright (c) 2013 #

# NiiiS #

# All Rights Reserved. #

########################################################################

# FileName: PADS\_GA\_Tol\_CSB\_13\_03\_29\_v4\_ver1.lef #

# Description: Cadence LEF File #

# Preview export LEF #

# Preview sub-version 5.10.41.500.4.83 #

########################################################################

# Ref libs: PADS\_GA\_Tol\_CSB\_13\_03\_29\_v4\_ver1 #

# Tech lib name: TECH035\_SOI #

# Tech file name: techfile.cds #

########################################################################

# v2.0 18 Apr 13 Created by Mezentcev Alexander #

# v2.0.1 15 May 2013 Fixed by Fil Cattish: overlap added #

########################################################################

SITE IOSite

SYMMETRY Y ;

CLASS PAD ;

SIZE 140.00 BY 449.00 ;

END IOSite

SITE CornerSite

SYMMETRY R90 ;

CLASS PAD ;

SIZE 449.00 BY 449.00 ;

END CornerSite

#########################

# #

# I O #

# #

#########################

MACRO PADIOPASS\_035

CLASS PAD ;

FOREIGN PADIOPASS\_035 0 0 ;

ORIGIN 0.00 0.00 ;

SIZE 140.00 BY 449.00 ;

SYMMETRY R90 ;

SITE IOSite ;

PIN PAD

DIRECTION INOUT ;

PORT

LAYER MET3 ;

RECT 111.45 123.00 116.30 127.80 ;

END

END PAD

PIN OE

DIRECTION INPUT ;

ANTENNAGATEAREA 8.79 LAYER MET2 ;

PORT

LAYER MET2 ;

RECT 79.10 446.10 79.90 448.30 ;

END

END OE

PIN FROM\_CORE

DIRECTION INPUT ;

ANTENNAGATEAREA 17.59 LAYER MET2 ;

PORT

LAYER MET2 ;

RECT 86.45 446.10 87.25 448.30 ;

END

END FROM\_CORE

PIN TO\_CORE

DIRECTION OUTPUT ;

PORT

LAYER MET2 ;

RECT 54.65 446.10 55.45 448.30 ;

END

END TO\_CORE

OBS

LAYER MET1 ;

RECT 0.25 0.25 139.80 448.80 ;

LAYER MET2 ;

POLYGON 139.75 448.75 88.20 448.75 88.20 445.15 85.50 445.15

85.50 448.75 80.85 448.75 80.85 445.15 78.15 445.15

78.15 448.75 56.40 448.75 56.40 445.15 53.70 445.15

53.70 448.75 0.25 448.75 0.25 0.25 139.75 0.25 ;

LAYER MET3 ;

POLYGON 140.00 281.20 139.75 281.20 139.75 282.80 140.00 282.80

140.00 372.80 139.75 372.80 139.75 375.40 140.00 375.40

140.00 410.35 139.75 410.35 139.75 413.35 140.00 413.35

140.00 448.30 139.75 448.30 139.75 448.75 88.15 448.75

88.15 448.30 85.55 448.30 85.55 448.75 80.80 448.75

80.80 448.30 78.20 448.30 78.20 448.75 56.35 448.75

56.35 448.30 53.75 448.30 53.75 448.75 0.25 448.75 0.25 448.30

0.00 448.30 0.00 413.35 0.25 413.35 0.25 410.35 0.00 410.35

0.00 375.40 0.25 375.40 0.25 372.80 0.00 372.80 0.00 282.80

0.25 282.80 0.25 281.20 0.00 281.20 0.00 142.80 0.25 142.80

0.25 0.25 139.75 0.25 139.75 122.05 110.50 122.05

110.50 128.75 139.75 128.75 139.75 142.80 140.00 142.80 ;

END

END PADIOPASS\_035

MACRO PADOUTODX2\_035

CLASS PAD ;

FOREIGN PADOUTODX2\_035 0 0 ;

ORIGIN 0.00 0.00 ;

SIZE 140.00 BY 449.00 ;

SYMMETRY R90 ;

SITE IOSite ;

PIN FROM\_CORE

DIRECTION INPUT ;

ANTENNAGATEAREA 8.79 LAYER MET2 ;

PORT

LAYER MET2 ;

RECT 86.45 446.10 87.25 448.30 ;

END

END FROM\_CORE

PIN PAD

DIRECTION OUTPUT ;

PORT

LAYER MET3 ;

RECT 111.50 123.05 116.30 127.80 ;

END

END PAD

OBS

LAYER MET1 ;

RECT 0.25 0.25 139.80 448.80 ;

LAYER MET2 ;

POLYGON 139.75 448.75 88.20 448.75 88.20 445.15 85.50 445.15

85.50 448.75 0.25 448.75 0.25 0.25 139.75 0.25 ;

LAYER MET3 ;

POLYGON 140.00 281.20 139.75 281.20 139.75 282.80 140.00 282.80

140.00 372.80 139.75 372.80 139.75 375.40 140.00 375.40

140.00 410.35 139.75 410.35 139.75 413.35 140.00 413.35

140.00 448.30 139.75 448.30 139.75 448.75 88.15 448.75

88.15 448.30 85.55 448.30 85.55 448.75 0.25 448.75 0.25 448.30

0.00 448.30 0.00 413.35 0.25 413.35 0.25 410.35 0.00 410.35

0.00 375.40 0.25 375.40 0.25 372.80 0.00 372.80 0.00 282.80

0.25 282.80 0.25 281.20 0.00 281.20 0.00 142.80 0.25 142.80

0.25 0.25 139.75 0.25 139.75 122.10 110.55 122.10

110.55 128.75 139.75 128.75 139.75 142.80 140.00 142.80 ;

END

END PADOUTODX2\_035

MACRO PADIOZBHODX4\_035

CLASS PAD ;

FOREIGN PADIOZBHODX4\_035 0 0 ;

ORIGIN 0.00 0.00 ;

SIZE 140.00 BY 449.00 ;

SYMMETRY R90 ;

SITE IOSite ;

PIN OE

DIRECTION INPUT ;

ANTENNAGATEAREA 8.79 LAYER MET2 ;

PORT

LAYER MET2 ;

RECT 79.10 446.10 79.90 448.30 ;

END

END OE

PIN FROM\_CORE

DIRECTION INPUT ;

ANTENNAGATEAREA 8.79 LAYER MET2 ;

PORT

LAYER MET2 ;

RECT 86.45 446.10 87.25 448.30 ;

END

END FROM\_CORE

PIN TO\_CORE

DIRECTION OUTPUT ;

PORT

LAYER MET2 ;

RECT 54.65 446.10 55.45 448.30 ;

END

END TO\_CORE

OBS

LAYER MET1 ;

RECT 0.25 0.25 139.80 448.80 ;

LAYER MET2 ;

POLYGON 139.75 448.75 88.20 448.75 88.20 445.15 85.50 445.15

85.50 448.75 80.85 448.75 80.85 445.15 78.15 445.15

78.15 448.75 56.40 448.75 56.40 445.15 53.70 445.15

53.70 448.75 0.25 448.75 0.25 0.25 139.75 0.25 ;

LAYER MET3 ;

POLYGON 140.00 281.20 139.75 281.20 139.75 282.80 140.00 282.80

140.00 372.80 139.75 372.80 139.75 375.40 140.00 375.40

140.00 410.35 139.75 410.35 139.75 413.35 140.00 413.35

140.00 448.30 139.75 448.30 139.75 448.75 88.15 448.75

88.15 448.30 85.55 448.30 85.55 448.75 80.80 448.75

80.80 448.30 78.20 448.30 78.20 448.75 56.35 448.75

56.35 448.30 53.75 448.30 53.75 448.75 0.25 448.75 0.25 448.30

0.00 448.30 0.00 413.35 0.25 413.35 0.25 410.35 0.00 410.35

0.00 375.40 0.25 375.40 0.25 372.80 0.00 372.80 0.00 282.80

0.25 282.80 0.25 281.20 0.00 281.20 0.00 142.80 0.25 142.80

0.25 0.25 139.75 0.25 139.75 142.80 140.00 142.80 ;

END

END PADIOZBHODX4\_035

…

END LIBRARY

# Фрагмент файла Tech.lef

#======================================

# Tech.lef for VEER MO2

# OFFSET 0.75 0.70 ;

# fix 2013.05.15: added layer OVERLAP

#======================================

VERSION 5.4 ;

NAMESCASESENSITIVE ON ;

DIVIDERCHAR "|" ;

BUSBITCHARS "[]" ;

UNITS

DATABASE MICRONS 100 ;

END UNITS

MANUFACTURINGGRID 0.050000 ;

LAYER ACTIVE

TYPE MASTERSLICE ;

END ACTIVE

LAYER POLY

TYPE MASTERSLICE ;

END POLY

LAYER MET1

TYPE ROUTING ;

WIDTH 0.50 ;

AREA 0.48 ;

SPACING 0.45 ;

SPACING 0.80 RANGE 10 31 ;

OFFSET 0.75 0.70 ;

PITCH 1.50 1.40 ;

DIRECTION HORIZONTAL ;

CAPACITANCE CPERSQDIST 0.0000620000 ;

RESISTANCE RPERSQ 0.08300000 ;

THICKNESS 0.60000000 ;

ANTENNAAREARATIO 150.00 ;

ANTENNAAREAFACTOR 1.00 ;

END MET1

LAYER VIA1

TYPE CUT ;

WIDTH 0.5 ;

SPACING 0.45 ;

END VIA1

LAYER MET2

TYPE ROUTING ;

WIDTH 0.60 ;

AREA 0.64 ;

SPACING 0.50 ;

SPACING 0.80 RANGE 10 31 ;

OFFSET 0.75 0.70 ;

PITCH 1.50 1.40 ;

DIRECTION VERTICAL ;

CAPACITANCE CPERSQDIST 0.0000380000 ;

RESISTANCE RPERSQ 0.07700000 ;

THICKNESS 0.60000000 ;

ANTENNAAREARATIO 150.00 ;

ANTENNAAREAFACTOR 1.00 ;

END MET2

LAYER VIA2

TYPE CUT ;

WIDTH 0.5 ;

SPACING 0.45 ;

END VIA2

LAYER MET3

TYPE ROUTING ;

WIDTH 0.60 ;

AREA 0.64 ;

SPACING 0.50 ;

SPACING 0.80 RANGE 10 31 ;

OFFSET 0.75 0.70 ;

PITCH 1.50 1.40 ;

DIRECTION HORIZONTAL ;

CAPACITANCE CPERSQDIST 0.0000350000 ;

RESISTANCE RPERSQ 0.07700000 ;

THICKNESS 0.60000000 ;

ANTENNAAREARATIO 150.00 ;

ANTENNAAREAFACTOR 1.00 ;

END MET3

LAYER VIA3

TYPE CUT ;

WIDTH 0.5 ;

SPACING 0.45 ;

END VIA3

LAYER MET4

TYPE ROUTING ;

WIDTH 0.60 ;

AREA 0.64 ;

SPACING 0.60 ;

SPACING 0.80 RANGE 10 31 ;

OFFSET 0.75 0.70 ;

PITCH 1.50 1.40 ;

DIRECTION VERTICAL ;

CAPACITANCE CPERSQDIST 0.0000350000 ;

RESISTANCE RPERSQ 0.05500000 ;

THICKNESS 1.00000000 ;

ANTENNAAREARATIO 150.00 ;

ANTENNAAREAFACTOR 1.00 ;

END MET4

VIA ruleV1

LAYER MET1 ;

RECT -0.95 -0.45 0.95 0.45 ;

LAYER VIA1 ;

RECT -0.75 -0.25 -0.25 0.25 ;

RECT 0.25 -0.25 0.75 0.25 ;

LAYER MET2 ;

RECT -0.90 -0.40 0.90 0.40 ;

END ruleV1

VIA ruleV2

LAYER MET2 ;

RECT -0.95 -0.45 0.95 0.45 ;

LAYER VIA2 ;

RECT -0.75 -0.25 -0.25 0.25 ;

RECT 0.25 -0.25 0.75 0.25 ;

LAYER MET3 ;

RECT -0.90 -0.40 0.90 0.40 ;

END ruleV2

VIA ruleV3

LAYER MET3 ;

RECT -0.95 -0.45 0.95 0.45 ;

LAYER VIA3 ;

RECT -0.75 -0.25 -0.25 0.25 ;

RECT 0.25 -0.25 0.75 0.25 ;

LAYER MET4 ;

RECT -0.90 -0.40 0.90 0.40 ;

END ruleV3

VIA M2\_M1 DEFAULT

RESISTANCE 2.0000000000 ;

LAYER MET1 ;

RECT -0.45 -0.45 0.45 0.45 ;

LAYER VIA1 ;

RECT -0.25 -0.25 0.25 0.25 ;

LAYER MET2 ;

RECT -0.40 -0.40 0.40 0.40 ;

END M2\_M1

…

END LIBRARY

# Фрагмент файла small\_test\_v3\_1.def

###############################################################

# Generated by: Cadence Encounter 10.12-s181\_1

# OS: Linux x86\_64(Host ID machaon)

# Generated on: Tue Jun 7 09:52:04 2016

# Design: small\_test

# Command: defOut -floorplan -unplaced -netlist small\_test\_v3\_1.d...

###############################################################

VERSION 5.6 ;

DIVIDERCHAR "/" ;

BUSBITCHARS "[]" ;

DESIGN small\_test ;

UNITS DISTANCE MICRONS 100 ;

PROPERTYDEFINITIONS

COMPONENTPIN designRuleWidth REAL ;

DESIGN FE\_CORE\_BOX\_LL\_X REAL 675.000 ;

DESIGN FE\_CORE\_BOX\_UR\_X REAL 6324.650 ;

DESIGN FE\_CORE\_BOX\_LL\_Y REAL 674.800 ;

DESIGN FE\_CORE\_BOX\_UR\_Y REAL 6319.600 ;

END PROPERTYDEFINITIONS

DIEAREA ( 0 0 ) ( 699865 699800 ) ;

ROW ROW\_781 CoreSite5 67500 630700 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_780 CoreSite5 67500 629440 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_779 CoreSite5 67500 628180 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_778 CoreSite5 67500 626920 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_777 CoreSite5 67500 625660 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_776 CoreSite5 67500 624400 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_775 CoreSite5 67500 623140 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_774 CoreSite5 67500 621880 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_773 CoreSite5 67500 620620 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_772 CoreSite5 67500 619360 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_771 CoreSite5 67500 618100 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_770 CoreSite5 67500 616840 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_769 CoreSite5 67500 615580 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_768 CoreSite5 67500 614320 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_767 CoreSite5 67500 613060 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_766 CoreSite5 67500 611800 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_765 CoreSite5 67500 610540 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_764 CoreSite5 67500 609280 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_763 CoreSite5 67500 608020 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_762 CoreSite5 67500 606760 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_761 CoreSite5 67500 605500 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_760 CoreSite5 67500 604240 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_759 CoreSite5 67500 602980 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_758 CoreSite5 67500 601720 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_757 CoreSite5 67500 600460 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_756 CoreSite5 67500 599200 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_755 CoreSite5 67500 597940 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_754 CoreSite5 67500 596680 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_753 CoreSite5 67500 595420 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_752 CoreSite5 67500 594160 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_751 CoreSite5 67500 592900 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_750 CoreSite5 67500 591640 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_749 CoreSite5 67500 590380 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_748 CoreSite5 67500 589120 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_747 CoreSite5 67500 587860 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_746 CoreSite5 67500 586600 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_745 CoreSite5 67500 585340 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_744 CoreSite5 67500 584080 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_743 CoreSite5 67500 582820 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_742 CoreSite5 67500 581560 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_741 CoreSite5 67500 580300 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_740 CoreSite5 67500 579040 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_739 CoreSite5 67500 577780 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_738 CoreSite5 67500 576520 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_737 CoreSite5 67500 575260 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_736 CoreSite5 67500 574000 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_735 CoreSite5 67500 572740 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_734 CoreSite5 67500 571480 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_733 CoreSite5 67500 570220 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_732 CoreSite5 67500 568960 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_731 CoreSite5 67500 567700 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_730 CoreSite5 67500 566440 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_729 CoreSite5 67500 565180 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_728 CoreSite5 67500 563920 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_727 CoreSite5 67500 562660 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_726 CoreSite5 67500 561400 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_725 CoreSite5 67500 560140 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_724 CoreSite5 67500 558880 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_723 CoreSite5 67500 557620 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_722 CoreSite5 67500 556360 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_721 CoreSite5 67500 555100 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_720 CoreSite5 67500 553840 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_719 CoreSite5 67500 552580 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_718 CoreSite5 67500 551320 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_717 CoreSite5 67500 550060 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_716 CoreSite5 67500 548800 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_715 CoreSite5 67500 547540 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_714 CoreSite5 67500 546280 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_713 CoreSite5 67500 545020 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_712 CoreSite5 67500 543760 FS DO 1883 BY 1 STEP 300 0

;

ROW ROW\_711 CoreSite5 67500 542500 N DO 1883 BY 1 STEP 300 0

;

…

END DESIGN

# Фрагмент файла big\_test2\_v3\_0.def

###############################################################

# Generated by: Cadence Encounter 10.12-s181\_1

# OS: Linux x86\_64(Host ID machaon)

# Generated on: Tue Jun 7 08:57:28 2016

# Design: big\_test2

# Command: defOut -floorplan -unplaced -netlist big\_test2\_v3\_0.de...

###############################################################

VERSION 5.6 ;

DIVIDERCHAR "/" ;

BUSBITCHARS "[]" ;

DESIGN big\_test2 ;

UNITS DISTANCE MICRONS 100 ;

PROPERTYDEFINITIONS

COMPONENTPIN designRuleWidth REAL ;

DESIGN FE\_CORE\_BOX\_LL\_X REAL 675.000 ;

DESIGN FE\_CORE\_BOX\_UR\_X REAL 6324.650 ;

DESIGN FE\_CORE\_BOX\_LL\_Y REAL 674.800 ;

DESIGN FE\_CORE\_BOX\_UR\_Y REAL 6319.600 ;

END PROPERTYDEFINITIONS

DIEAREA ( 0 0 ) ( 699865 699800 ) ;

ROW ROW\_110 CoreSite54 67500 623140 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_109 CoreSite54 67500 618100 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_108 CoreSite54 67500 613060 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_107 CoreSite54 67500 608020 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_106 CoreSite54 67500 602980 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_105 CoreSite54 67500 597940 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_104 CoreSite54 67500 592900 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_103 CoreSite54 67500 587860 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_102 CoreSite54 67500 582820 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_101 CoreSite54 67500 577780 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_100 CoreSite54 67500 572740 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_99 CoreSite54 67500 567700 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_98 CoreSite54 67500 562660 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_97 CoreSite54 67500 557620 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_96 CoreSite54 67500 552580 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_95 CoreSite54 67500 547540 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_94 CoreSite54 67500 542500 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_93 CoreSite54 67500 537460 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_92 CoreSite54 67500 532420 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_91 CoreSite54 67500 527380 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_90 CoreSite54 67500 522340 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_89 CoreSite54 67500 517300 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_88 CoreSite54 67500 512260 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_87 CoreSite54 67500 507220 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_86 CoreSite54 67500 502180 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_85 CoreSite54 67500 497140 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_84 CoreSite54 67500 492100 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_83 CoreSite54 67500 487060 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_82 CoreSite54 67500 482020 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_81 CoreSite54 67500 476980 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_80 CoreSite54 67500 471940 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_79 CoreSite54 67500 466900 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_78 CoreSite54 67500 461860 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_77 CoreSite54 67500 456820 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_76 CoreSite54 67500 451780 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_75 CoreSite54 67500 446740 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_74 CoreSite54 67500 441700 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_73 CoreSite54 67500 436660 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_72 CoreSite54 67500 431620 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_71 CoreSite54 67500 426580 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_70 CoreSite54 67500 421540 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_69 CoreSite54 67500 416500 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_68 CoreSite54 67500 411460 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_67 CoreSite54 67500 406420 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_66 CoreSite54 67500 401380 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_65 CoreSite54 67500 396340 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_64 CoreSite54 67500 391300 N DO 1883 BY 1 STEP 300 0

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ROW ROW\_63 CoreSite54 67500 386260 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_62 CoreSite54 67500 381220 N DO 1883 BY 1 STEP 300 0

;

ROW ROW\_61 CoreSite54 67500 376180 N DO 1883 BY 1 STEP 300 0

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END DESIGN