



STMPS2242, STMPS2252 STMPS2262, STMPS2272

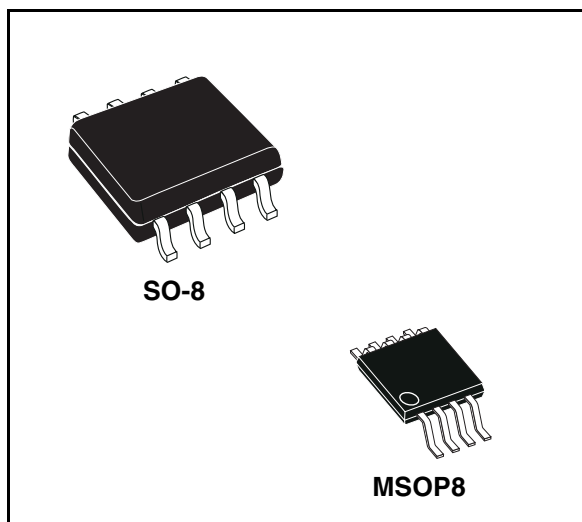
Enhanced dual channel power switches

Features

- 100 mΩ high-side MOSFET switch
- 500 mA/1000 mA continuous current per channel
- Thermal protection
- Independent short-circuit protection with overcurrent logic output
- Operating range from 2.7 V to 5.5 V
- CMOS and TTL compatible inputs
- 2.5 ms typical rise time
- Undervoltage lockout
- 13 μA maximum standby supply current
- Ambient temperature range: -40 °C to 85 °C
- 8 kV ESD protection
- Reverse current protection
- Fault blanking

Description

The STMPS2242/2252/2262/2272 power distribution switches are intended for applications where heavy capacitive loads and short circuits are likely to be encountered. These devices incorporate 100 mΩ MOSFET high-side power switches for power distribution systems that require multiple power switches in a single package.



Each switch is controlled by an independent logic enable input. When the output load exceeds the current limit threshold or a short is present, these devices limit the output current to a safe level by switching into a constant current mode, pulling the overcurrent (OCx) logic output low. When continuous heavy overloads and short circuits increase the power dissipation in the switch, causing the junction temperature to rise, a thermal protection circuit shuts off the switch to prevent damage. Recovery from a thermal shutdown is automatic once the device has cooled sufficiently. Internal circuitry ensures the switch remains off until valid input voltage is present.

Table 1. Device summary

Order code		Current limit (mA)	Enable	Packing
SO-8	MSOP8 ⁽¹⁾			
STMPS2242MTR	STMPS2242TTR	500	Active low	Tape and reel
STMPS2252MTR	STMPS2252TTR	500	Active high	Tape and reel
STMPS2262MTR	STMPS2262TTR	1000	Active low	Tape and reel
STMPS2272MTR	STMPS2272TTR	1000	Active high	Tape and reel

1. MSOP8 is also known as TSSOP8.

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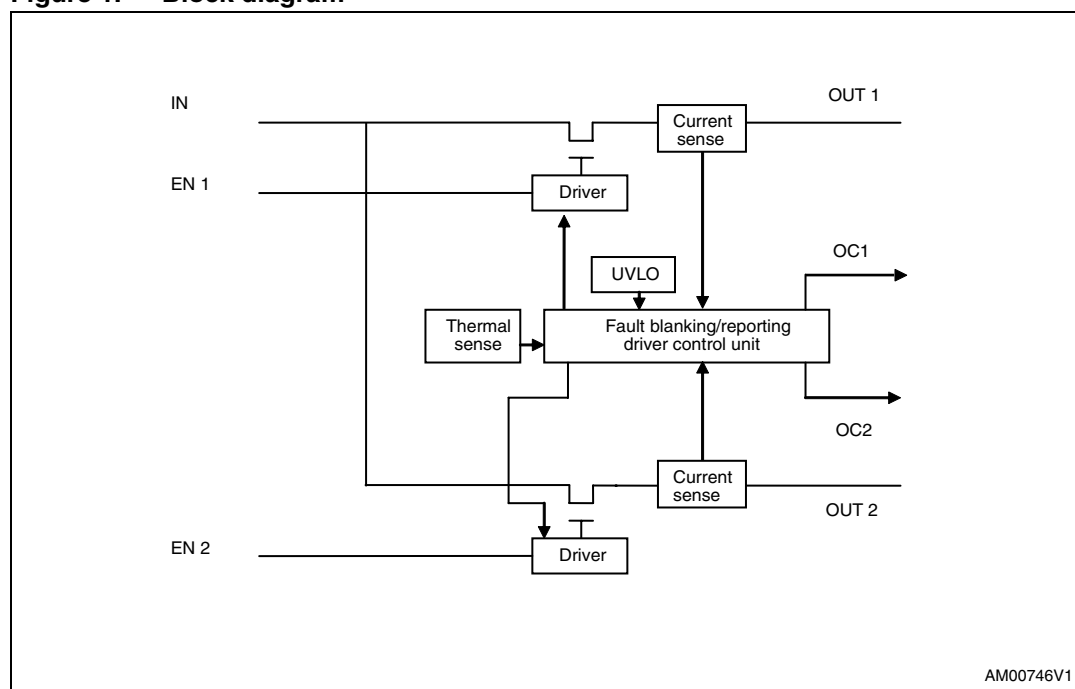
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1 Block diagram

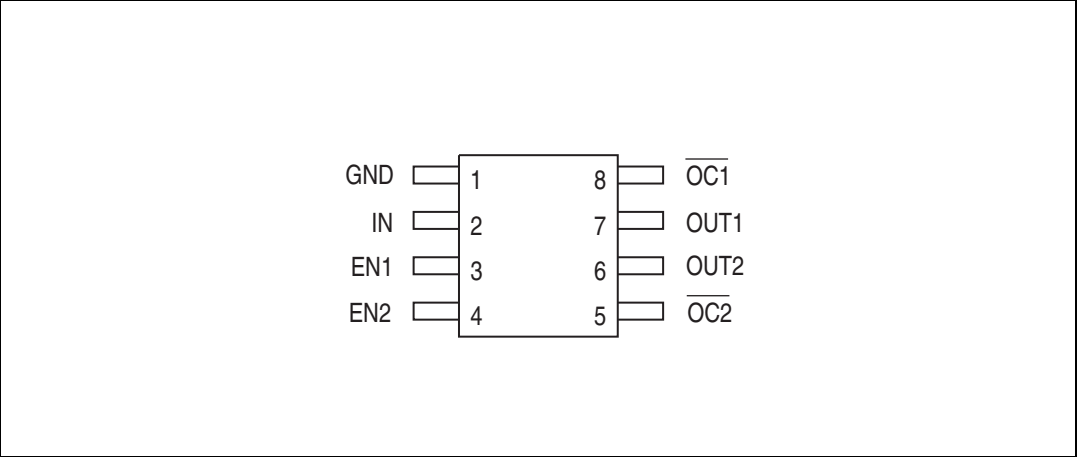
Figure 1. Block diagram



2 Pin settings

2.1 Pin description

Figure 2. Pin description



2.2 Pin description

Table 2. Pin description

Pin number	Name	Type	Function
1	GND	—	Ground
2	IN	—	V _{CC} input, 2.7 - 5.5 V
3	$\overline{EN1}/EN1$	I	Channel 1 active low (STMPS2242 and STMPS2262) or active high (STMPS2252 and STMPS2272) enable
4	$\overline{EN2}/EN2$	I	Channel 2 active low (STMPS2242 and STMPS2262) or active high (STMPS2252 and STMPS2272) enable
5	$\overline{OC2}$	O	Open drain output for fault indication of channel 2
6	OUT2	—	Output of channel 2
7	OUT1	—	Output of channel 1
8	$\overline{OC1}$	O	Open drain output for fault indication of channel 1

3 Functional description

3.1 Fault blanking

The STMP2 devices feature a 10 ms fault blanking. Fault blanking allows current-limit faults, including momentary short-circuit faults that occur when hot-swapping a capacitive load, and also ensures that no fault is issued during power-up. When a load transient causes the device to enter current limit, an internal counter starts. If the load fault persists beyond the 10 ms fault-blanking timeout, the FAULT output asserts “low”. Load-transient faults less than 10 ms (typ.) do not cause a FAULT output assertion. Only current-limit faults are blanked. Die over-temperature faults and input voltage drops below the UVLO threshold cause an immediate fault output.

3.2 Overcurrent/over-temperature protection

In overcurrent or short-circuit condition, the switch limits the current at 500 mA for STMP2242/STMP2252 and 1000 mA for STMP2262/STMP2272. If temperature of die goes above limit value, the switch turns OFF.

3.3 Reversed current blocking

When the switch is OFF, or when the STMP2 device is un-powered ($V_{CC}=0$ V), the switch behaves as an Hi-Z at the output pin, ensuring that no reverse current will flow into the device when $V_i < V_o$.

Note: In the case where the switch is ON, and a voltage higher than V_i is applied to the OUT pin, a reverse current will occur.

3.4 UVLO

When input voltage drops below critical value, the power switch turns OFF to prevent improper operation due to low voltage.

4 Maximum rating

Stressing the device above the rating listed in the “Absolute Maximum Ratings” table may cause permanent damage to the device. These are stress ratings only and operation of the device at these or any other conditions above those indicated in the Operating sections of this specification is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

4.1 Absolute maximum rating

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_I	Input voltage range	-0.3 – 6.0	V
V_O	Output voltage range	-0.3 – ($V_I+0.3$)	V
V_{IENX}	EN Input voltage range	-0.3 – 6.0	V
I_O	Continuous output current	Internally limited	
ESD	ESD protection level	8	kV
T_J	Junction operating temperature	-40 to 125	°C
T_{STG}	Storage temperature	-55 to 150	°C
T_R	Thermal resistance (MSOP8)	220	°C/W
T_R	Thermal resistance (SO-8)	160	°C/W

4.2 Recommended operating conditions

Table 4. Recommended operating conditions

Symbol	Parameter	Value			Unit
		Min	Typ	Max	
V_I	Input voltage	2.7	5.0	5.5	V
V_O	Output voltage	0	5.0	5.5	V
I_O (STMPS2242 STMPS2252)	Continuous output current	0	-	500	mA
I_O (STMPS2262 STMPS2272)	Continuous output current	0	-	1000	mA

5 Electrical specification

Table 5. Electrical characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min	Typ	Max	
R _{ds on}	Static drain source ON state resistance	V _I = 2.7 V T _J = 25°C		120	160	mΩ
		V _I = 5.0 V T _J = 25°C;		105	115	mΩ
R _{ds on}	Static drain source ON state resistance	V _I = 2.7 V -40 < T _J < 125°C			200	mΩ
		V _I = 5.0 V -40 < T _J < 125°C			140	
T _r	Output rise time (STMP52242,STMP52252)	V _I = 5.0 V R _L = 10 Ω C _L = 1 μF	0.05		2	ms
	Output rise time (STMP52262,STMP52272)	V _I = 5.0 V R _L = 10 Ω C _L = 1 μF	0.05		2	ms
T _f	Output fall time (STMP52242,STMP52252)	V _I = 5.0 V R _L = 10 Ω C _L = 1 μF	0.05		2	ms
	Output fall time (STMP52262,STMP52272)	V _I = 5.0 V R _L = 10 Ω C _L = 1 μF	0.05		2	ms

Table 6. Current limit characteristics
(V_I = 5.5 V, I_O = rated current, T_J = 25°C, unless otherwise specified)

Symbol	Parameter	Test condition	Value			Unit
			Min	Typ	Max	
I _{OS} (STMP52242 STMP52252)	Short circuit output current	V _I = 5 V OUT connected to GND through 10 mΩ load, device enabled into short circuit	0.6	0.8	1.0	A
I _{OS} (STMP52262 STMP52272)			1.1	1.6	2.0	A

Table 7. Supply current characteristics(V_I = 5.5 V, I_O = rated current, T_J = 25°C, unless otherwise specified)

Symbol	Parameter	Test condition	Value			Unit
			Min	Typ	Max	
I _{off}	Switch turned off	No load Switch is off		9	14	μA
		No load Switch is off -40 < T _J < 125°C			16	
I _{on}	Switch turned on	No load Switch is on		50	70	μA
		No load Switch is on -40 < T _J < 125°C			85	
I _{leakage}	Output leakage current ⁽¹⁾	I _{OFF} (grounded output) - I _{OFF} (floating output)		1	2	μA
		I _{OFF} (grounded output) - I _{OFF} (floating output) -40 < T _J < 125°C		1	6	
I _{reverse}	Reversed leakage current	Switch is off V _i < V _o , Output connected to 5.5 V, 25°C		1	2	μA
		Switch is off V _i < V _o , Output connected to 5.5 V, 125°C		1	10	

1. I_{leakage} = I_{off-ground} - I_{off}, where I_{off-ground} = current into Vin when switch is off and output is grounded**Table 8. Thermal characteristics**(V_I = 5.5 V, I_O = rated current, T_J = 25°C, unless otherwise specified)

Symbol	Parameter	Test condition	Value			Unit
			Min	Typ	Max	
T1	Thermal shutdown threshold		135			°C
T2	Recovery from thermal shutdown		125			°C
Hysteresis				10		°C

Table 9. UVLO characteristics(V_I = 5.5 V, I_O = rated current, T_J = 25°C, unless otherwise specified)

Symbol	Parameter	Test condition	Value			Unit
			Min	Typ	Max	
V _{UVLO}	Undervoltage lockout threshold		2.0		2.5	V
Hysteresis				75		mV

Table 10. OCx pin characteristics(V_I = 5.5 V, I_O = rated current, T_J = 25°C, unless otherwise specified)

Symbol	Parameter	Test condition	Value			Unit
			Min	Typ	Max	
OC blanking	OCx assertion and de-assertion		4	8	15	ms
V _O	Output low voltage	I _O = 5 mA	—	—	0.4	V
I _{OFF}	Off current	V _{OC} = 2.7 V, 5.5 V (No OC condition)	—	—	1.0	µA

Table 11. ENx pin characteristics(V_I = 5.5 V, I_O = rated current, T_J = 25°C, unless otherwise specified)

Symbol	Parameter	Test condition	Value			Unit
			Min	Typ	Max	
V _{IH}	High level input voltage	V _I = 2.7 V to 5.5 V	2.0	—	—	V
V _{IL}	Low level input voltage	V _I = 4.5 V to 5.5 V	—	—	0.8	V
		V _I = 2.7 V to 4.5 V	—	—	0.4	V
I _I	Input current	V _{IENX} = 0 V or V _I	-0.5	—	0.5	µA
t _{ON}	Turn ON time ⁽¹⁾	R _L = 10 Ω C _L = 100 µF	—	—	5	ms
t _{OFF}	Turn OFF time ⁽¹⁾	R _L = 10 Ω C _L = 100 µF	—	—	10	ms

1. Not tested in production, specified by design.

6 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Figure 3. SO-8 package outline

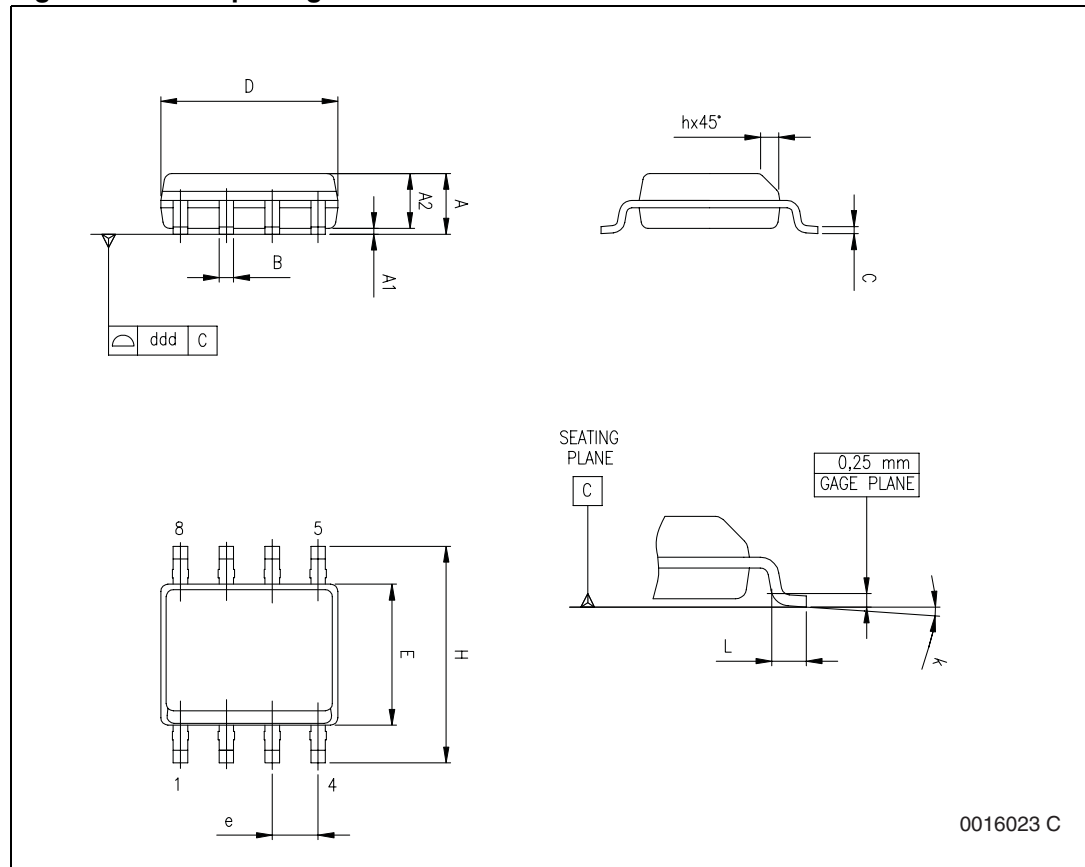


Table 12. SO-8 mechanical data

Symbol	Millimeters		
	Min	Typ	Max
A	1.35	—	1.75
A1	0.10	—	0.25
A2	1.10	—	1.65
B	0.33	—	0.51
C	0.19	—	0.25
D ⁽¹⁾	4.80	—	5.00
E	3.80	—	4.00
e	—	1.27	—
H	5.80	—	6.20
h	0.25	—	0.50
L	0.40	—	1.27
k	0° (min.), 8° (max.)		
ddd	—	—	0.10

1. Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15mm (.006inch) in total (both sides).

Figure 4. MSOP8 package outline

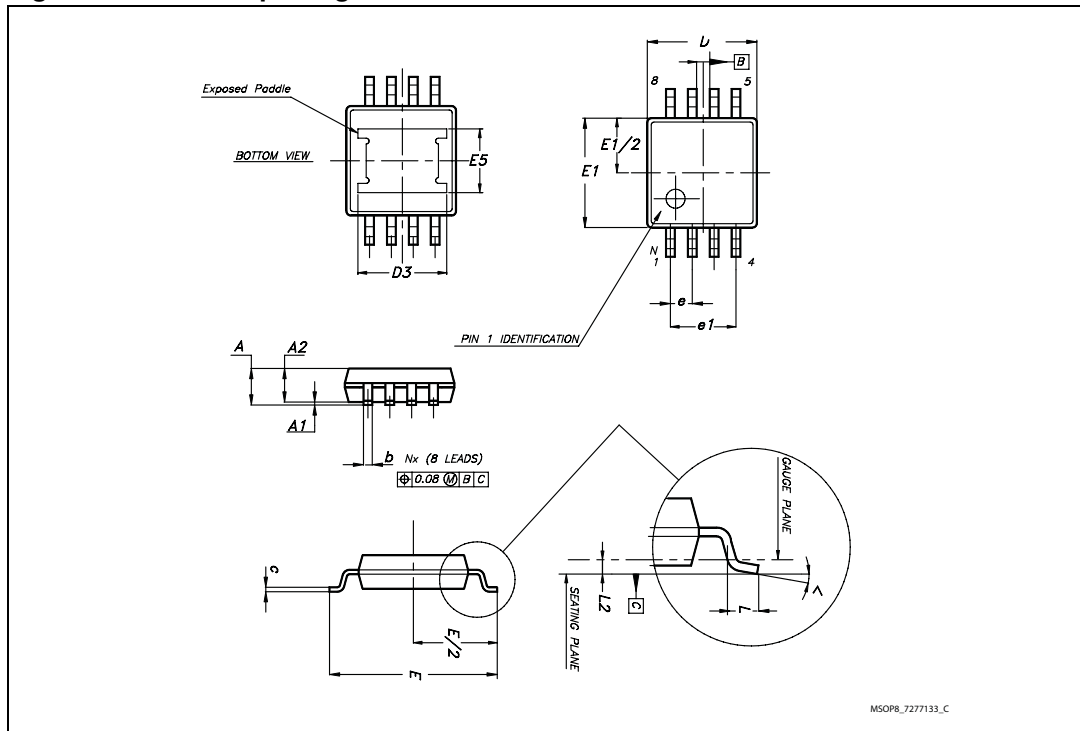


Table 13. MSOP8 mechanical data

Symbol	Millimeters		
	Min	Typ	Max
A	—	—	1.10
A1	0	—	0.15
A2	0.75	0.85	0.95
b	0.22	—	0.40
c	0.08	—	0.23
D	2.90	3.00	3.10
D3	—	2.16	—
E	4.67	4.90	5.07
E1	2.90	3.00	3.10
E5	—	1.73	—
e	—	0.65	—
e1	—	1.95	—
L	0.40	—	0.80
L2	—	0.25	—
<	0°	—	6°

Figure 5. SO-8 carrier tape

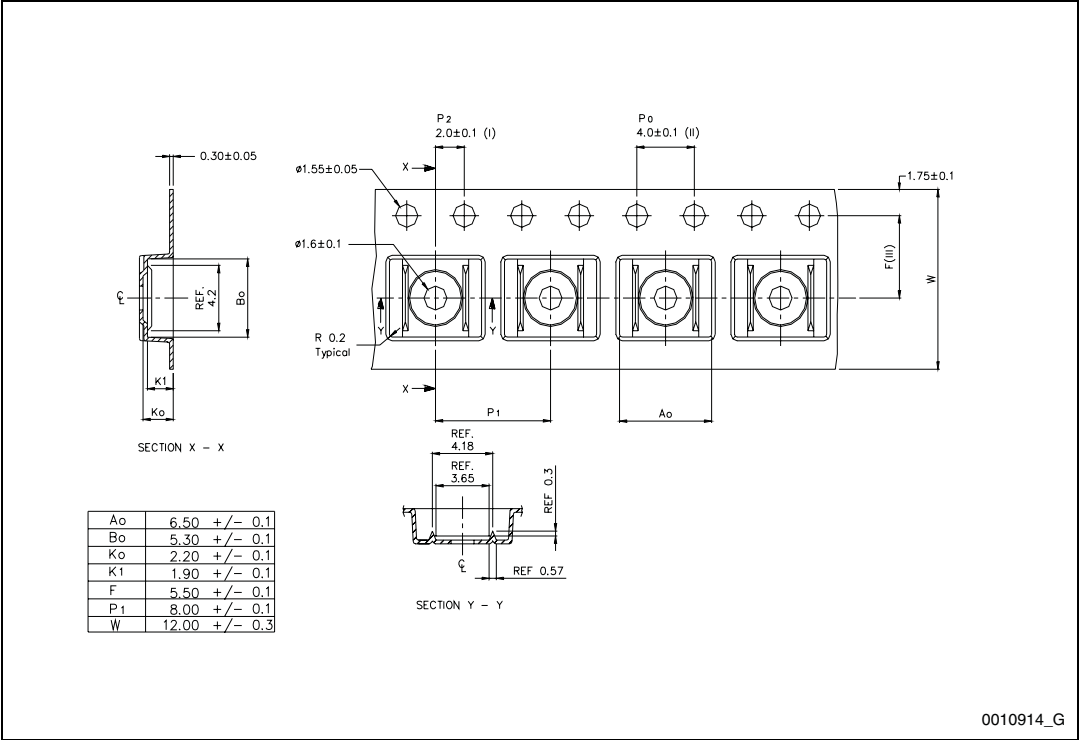


Figure 6. MSOP8 carrier tape

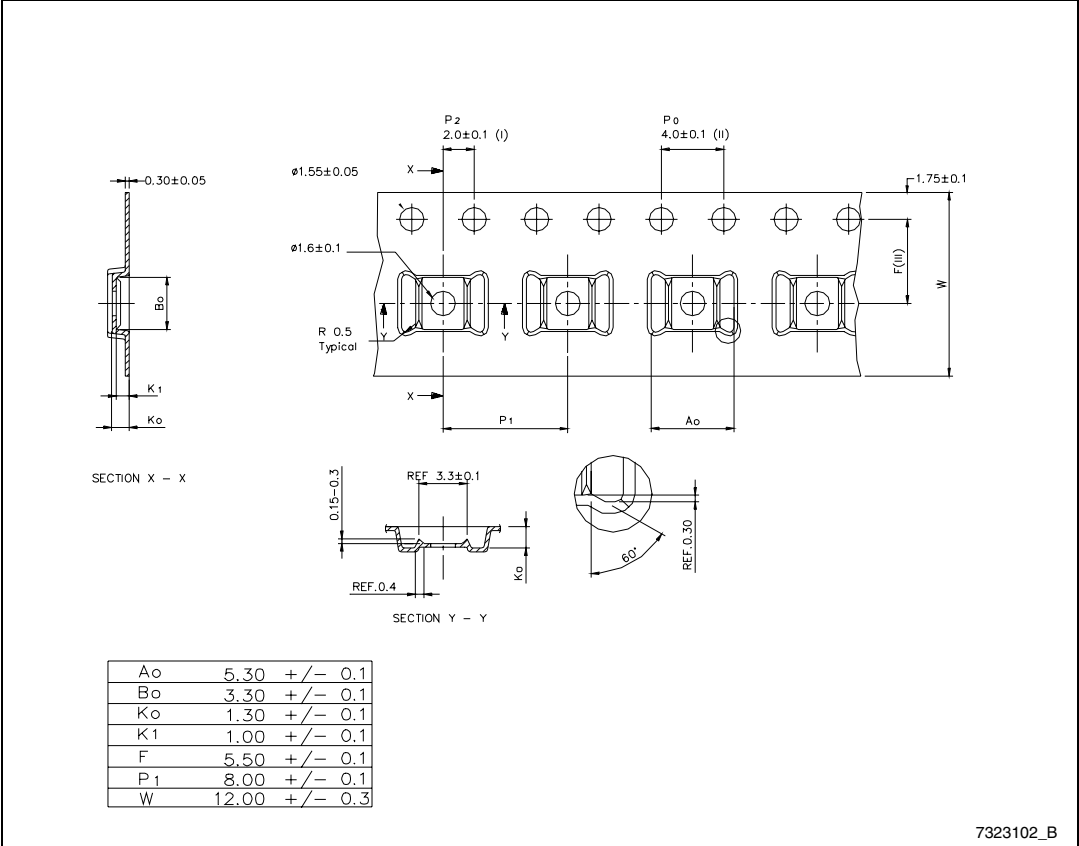


Figure 7. Reel information

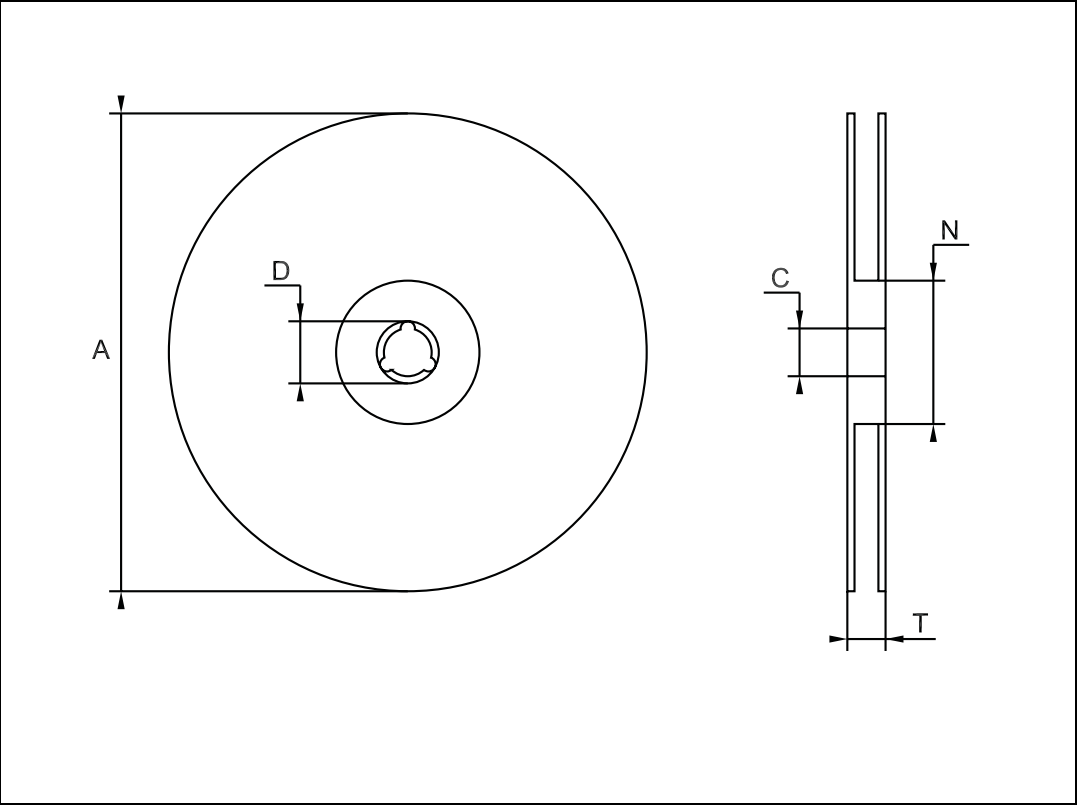


Table 14. Reel mechanical data

Symbol	Millimeters		
	Min	Typ	Max
A			330
C	12.8		13.2
D	20.2		
N	60		
T			22.4

7 Revision history

Table 15. Document revision history

Date	Revision	Changes
03-Dec-2008	1	Initial release.
18-Dec-2009	2	Modified: Table 6 , Table 7 and Table 9 .

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