

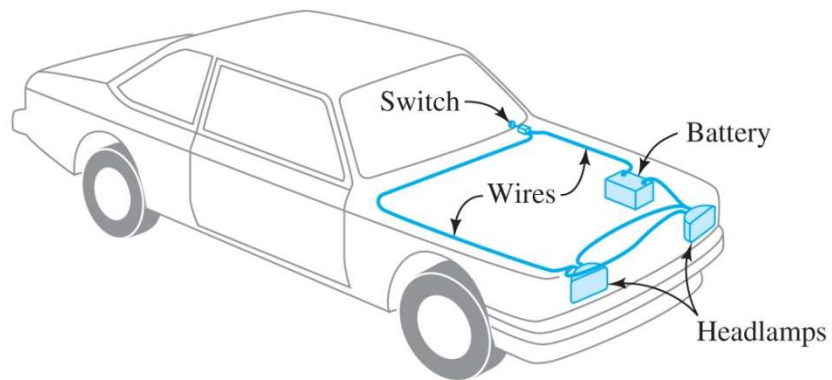
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# Basic Circuit Components and Theory\*

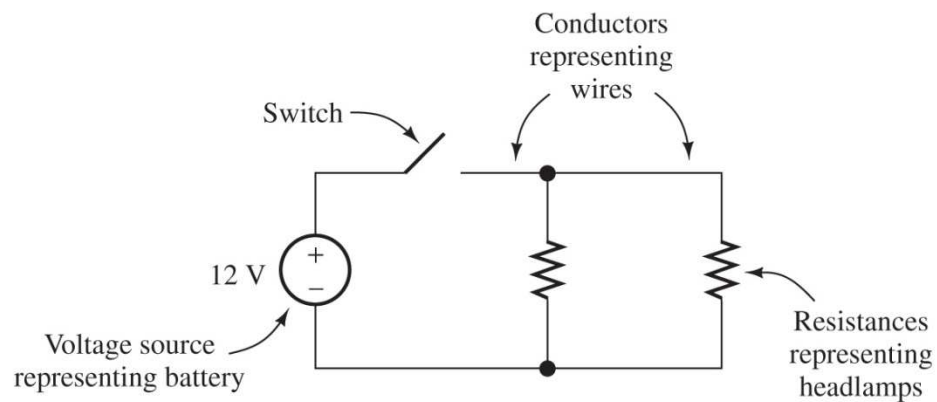
Yuttapong Jiraraksopakun

\*Image courtesy: Allan R. Hambley, Electrical Engineering Principles and Applications, 4<sup>th</sup> Ed., Upper Saddle River, NJ: Pearson Education, Inc., 2008.

# Circuits

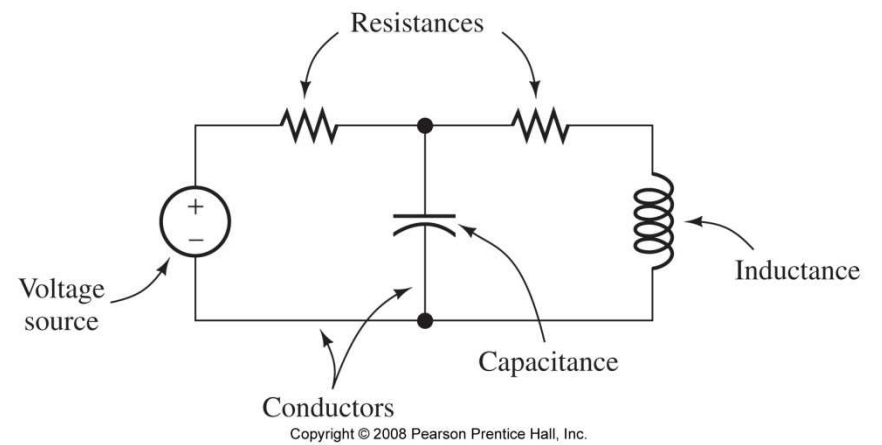


(a) Physical configuration

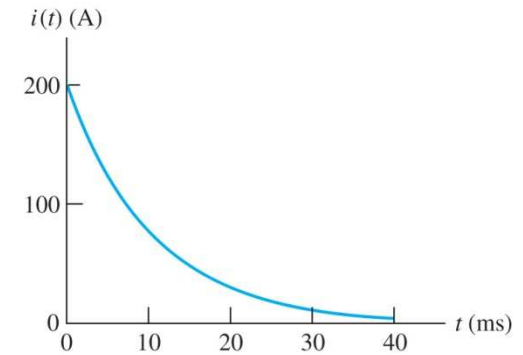
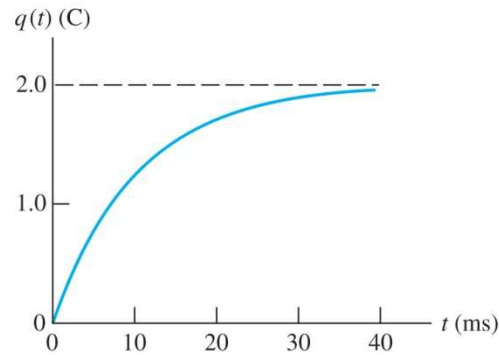
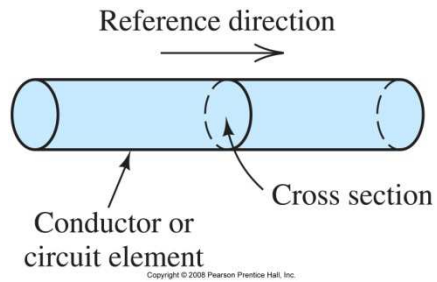


(b) Circuit diagram

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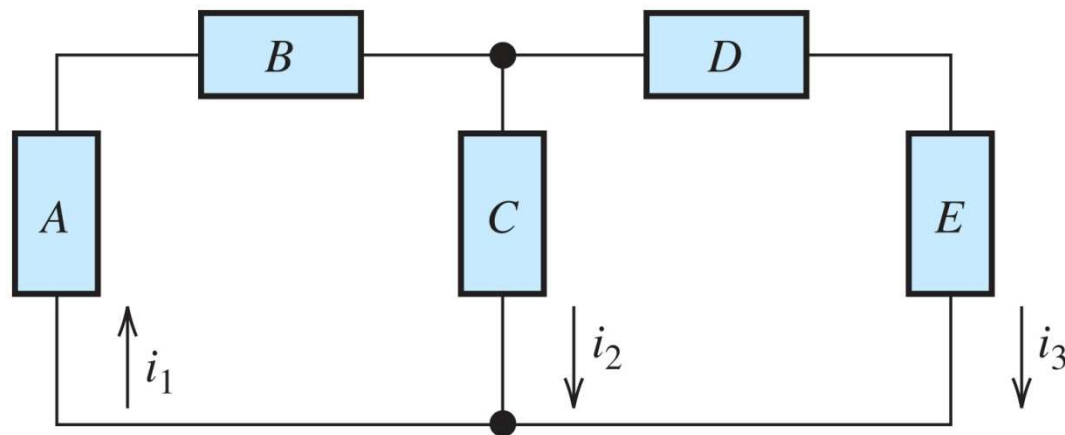
# Currents



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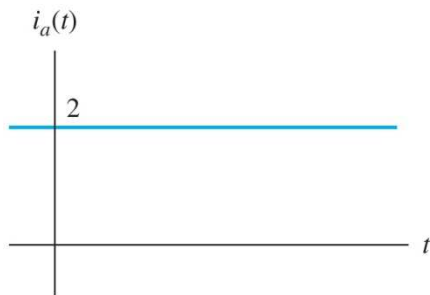
$$i(t) = \frac{dq(t)}{dt}$$

$$q(t) = \int_0^t i(t) dt + q(t_0)$$



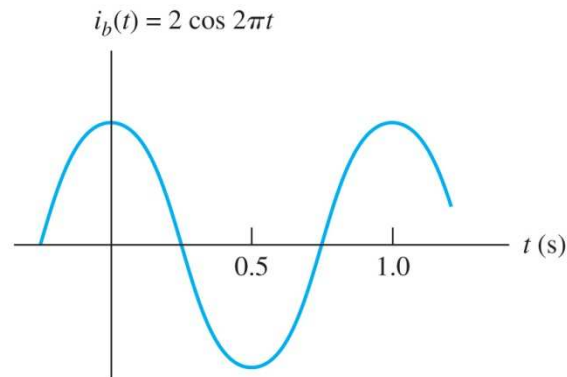
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# Direct and Alternating Current

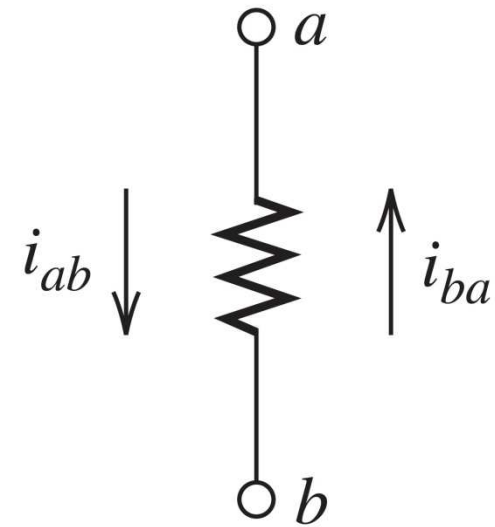


(a) Dc current

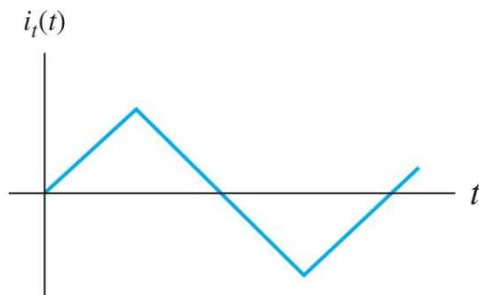
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(b) Ac current

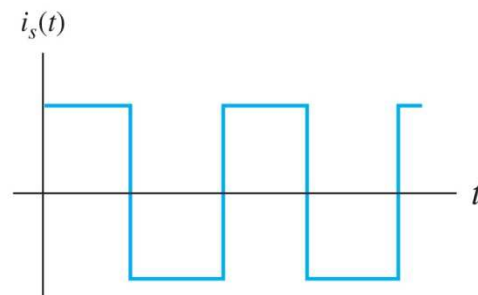


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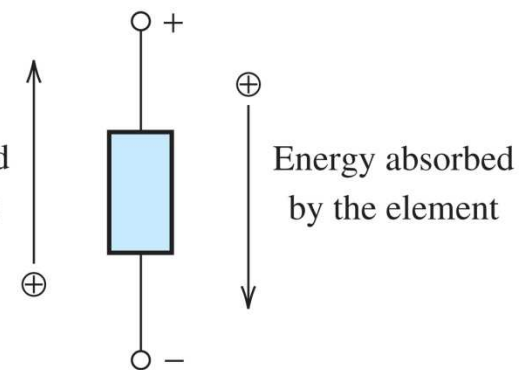
(a) Triangular waveform

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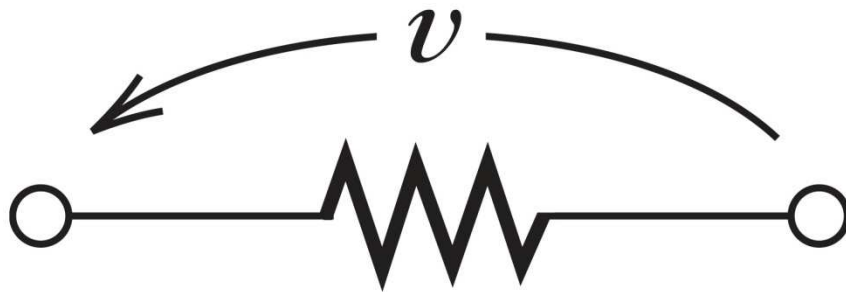
(b) Square waveform

Energy supplied  
by the element

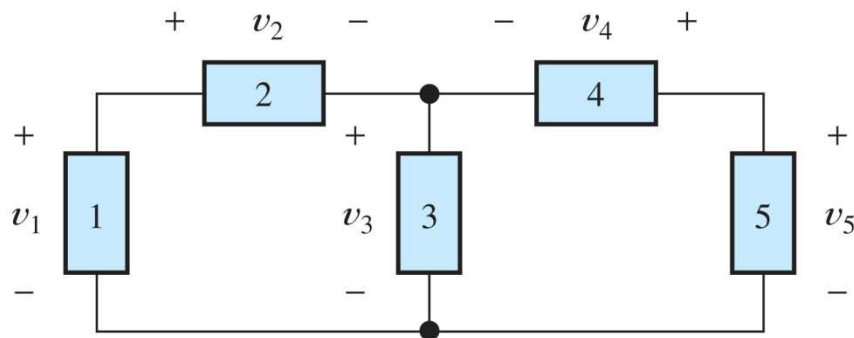


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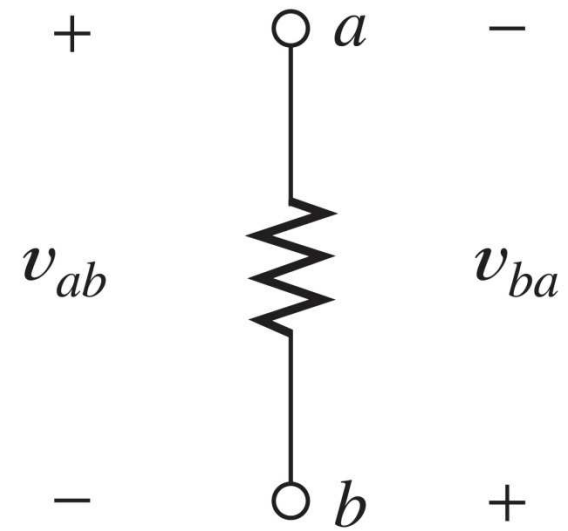
# Votages



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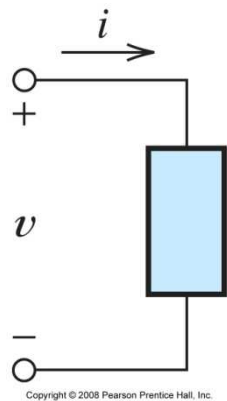
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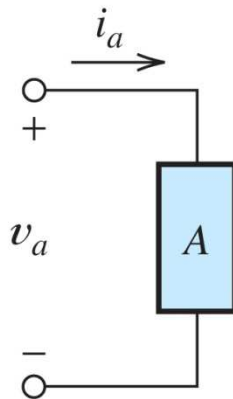


# Power and Energy



$$p(t) = v(t)i(t)$$

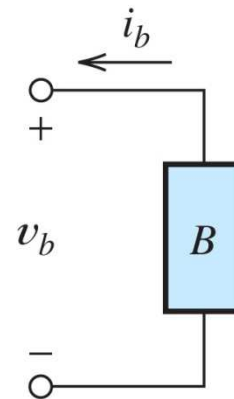
$$w(t) = \int_{t_1}^{t_2} p(t) dt$$



$$v_a = 12 \text{ V}$$
$$i_a = 2 \text{ A}$$

(a)

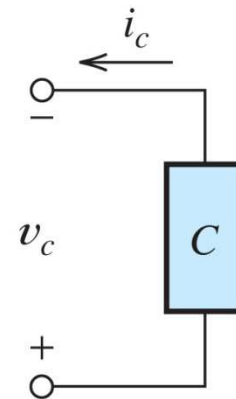
charged



$$v_b = 12 \text{ V}$$
$$i_b = 1 \text{ A}$$

(b)

discharged



$$v_c = 12 \text{ V}$$
$$i_c = -3 \text{ A}$$

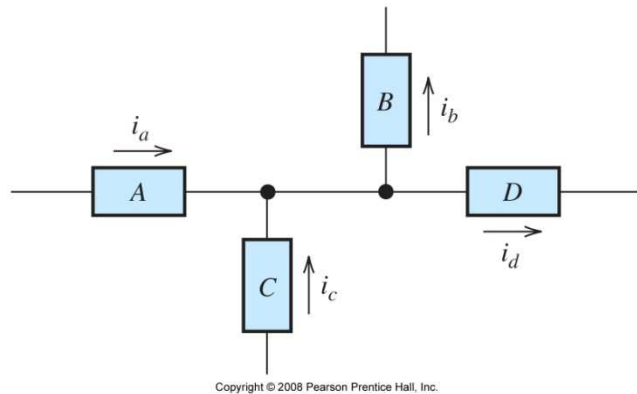
(c)

discharged

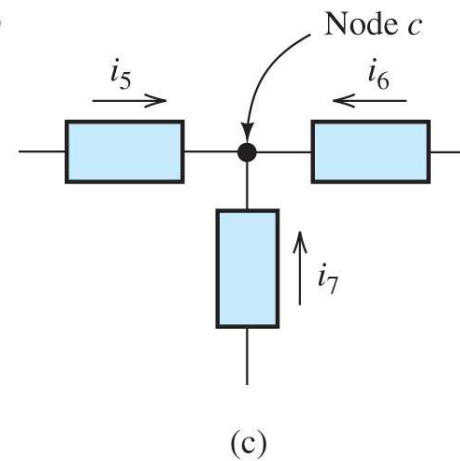
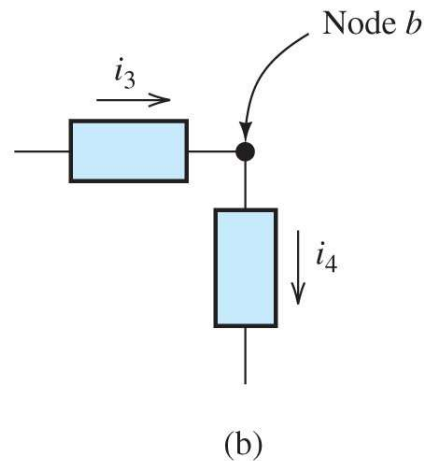
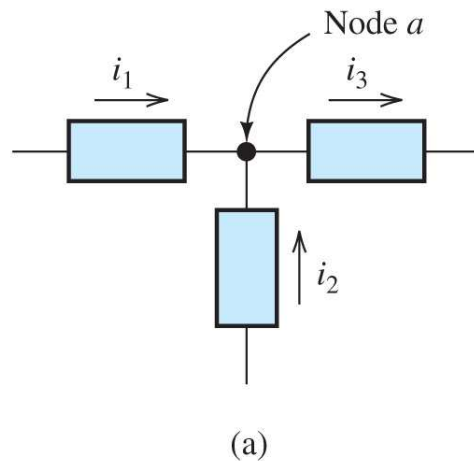
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# Kirchhoff's Current Law (KCL)

- ▶ The net current entering a node is zero

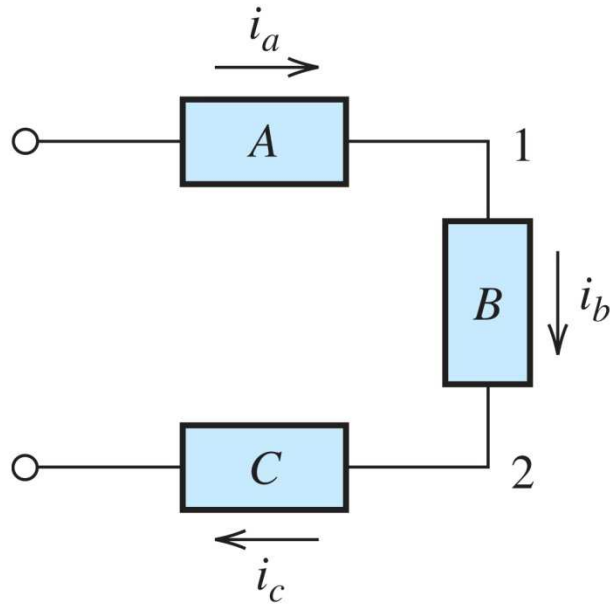


$$i_a + i_c = i_b + i_d$$



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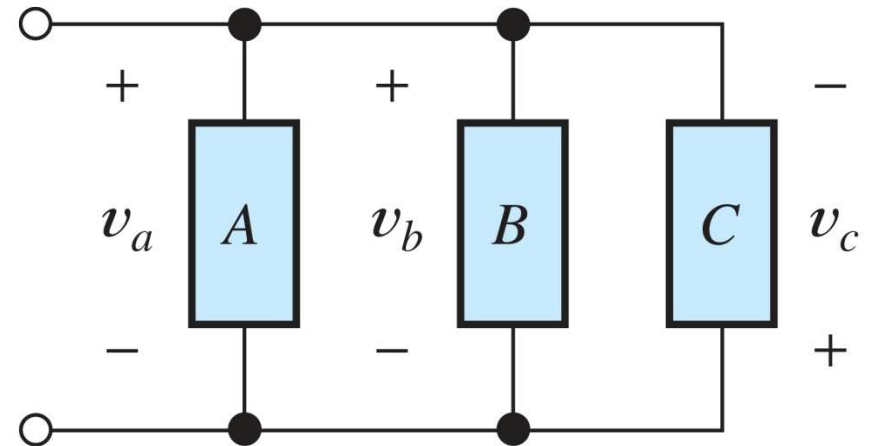
# Series/ Parallel Circuits



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$$v_a = v_b = -v_c$$

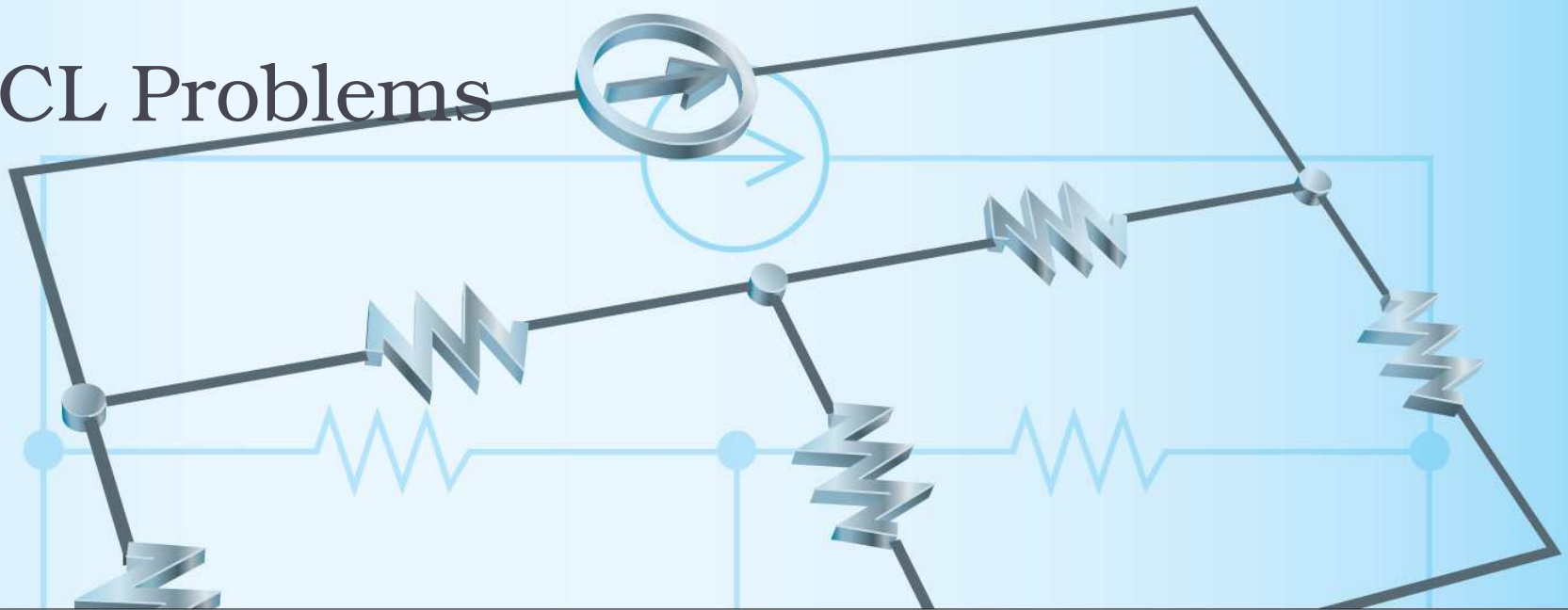
$$i_a = i_b = i_c$$



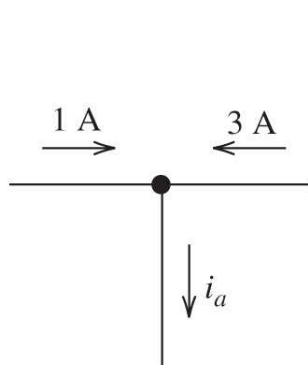
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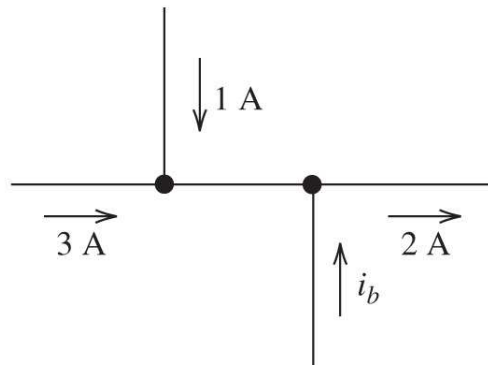
# KCL Problems



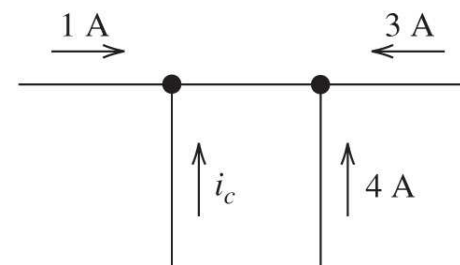
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(a)



(b)



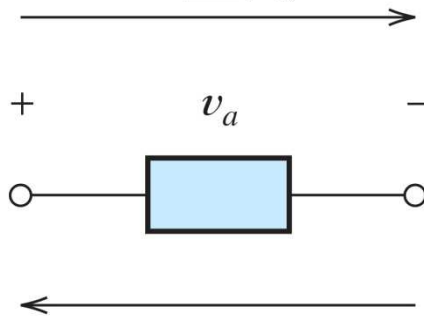
(c)

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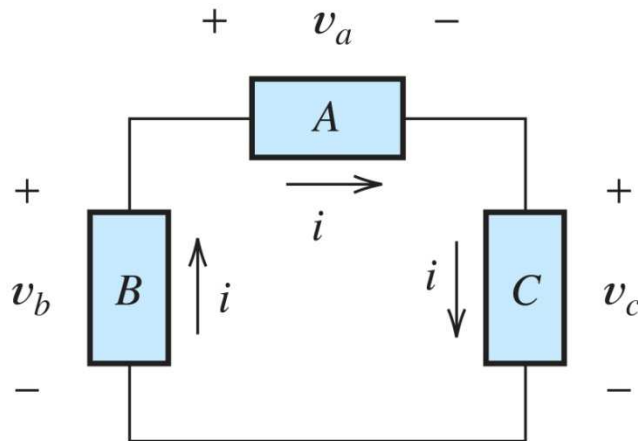
# Kirchhoff's Voltage Law (KVL)

Moving from + to -  
we add  $v_a$ .

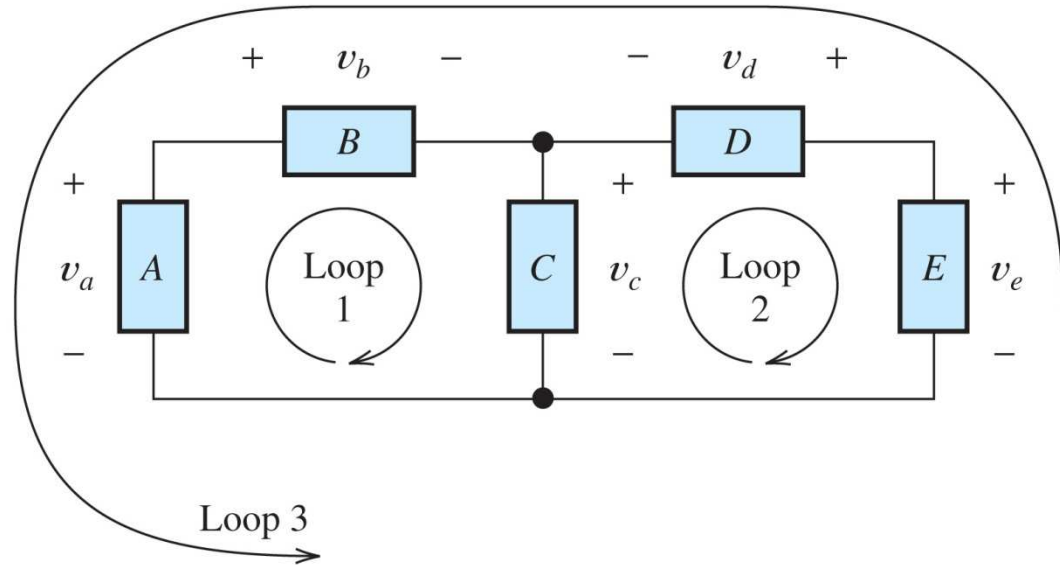


Moving from - to +  
we subtract  $v_a$ .

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KVL related to Conservation of Energy

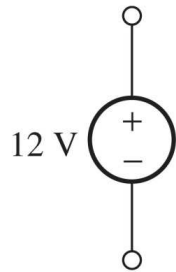
$$p_a + p_b + p_c = 0$$

$$v_a i + v_b i + v_c i = 0$$

$$v_a + v_b + v_c = 0$$

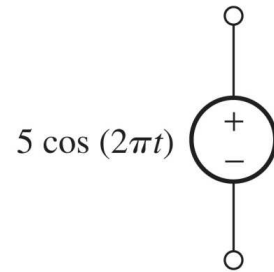
# Voltage Sources

## ► Independent sources

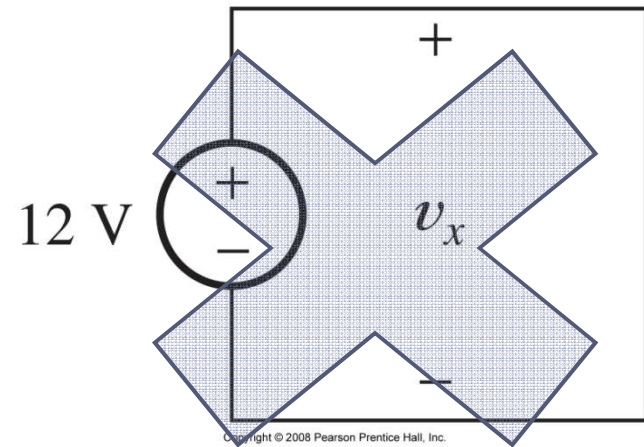


(a) Constant or dc voltage source

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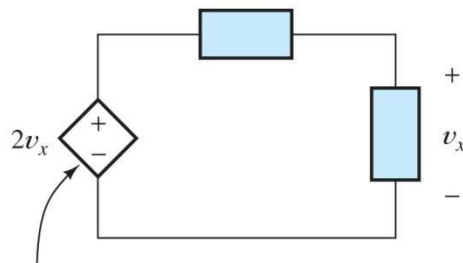
(b) Ac voltage source



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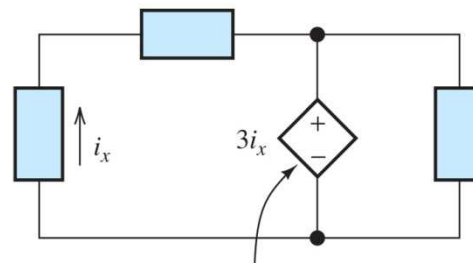
Avoid self-contradictory circuit

## ► Dependent sources



Voltage-controlled voltage source

(a)



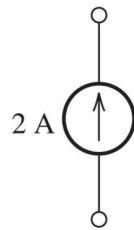
Current-controlled voltage source

(b)

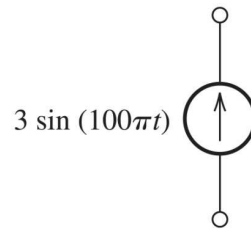
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# Current Sources

## ► Independent sources



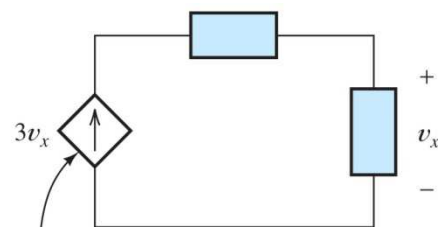
(a) Dc current source



(b) Ac current source

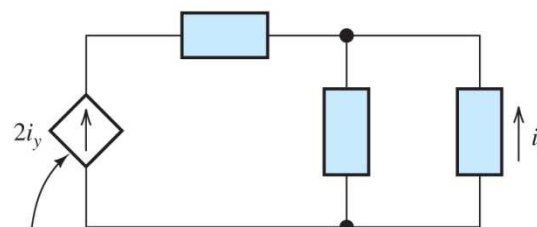
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## ► Dependent sources



Voltage-controlled current source

(a)

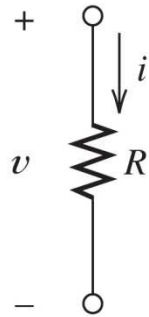


Current-controlled current source

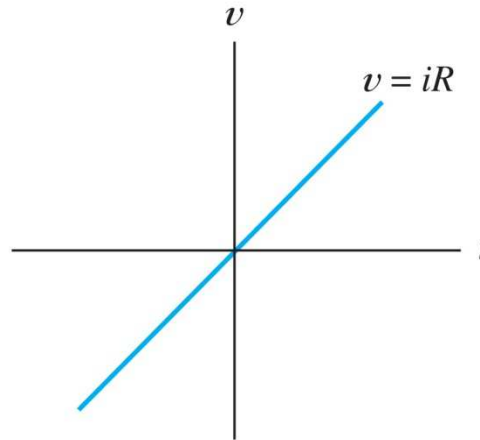
(b)

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# Resistance and Ohm's Law



(a) Resistance symbol



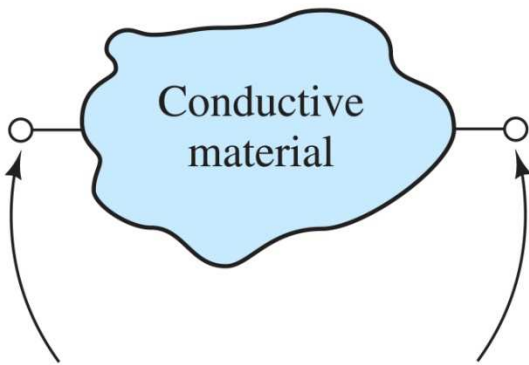
(b) Ohm's law

$$p = vi$$

$$p = Ri^2$$

$$p = \frac{v^2}{R}$$

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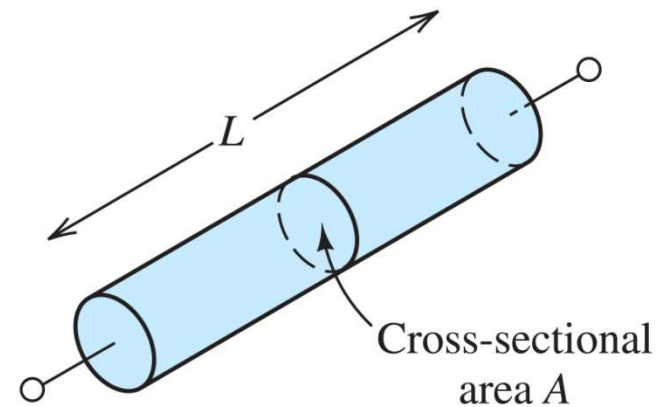
Terminals for current  
to enter or leave

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$$v = iR$$

$$i = \frac{1}{R}v$$

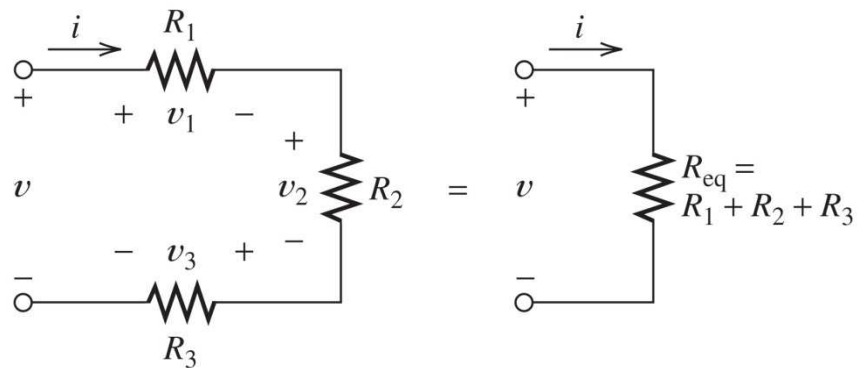
$$i = Gv$$



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$$R = \frac{\rho L}{A}$$

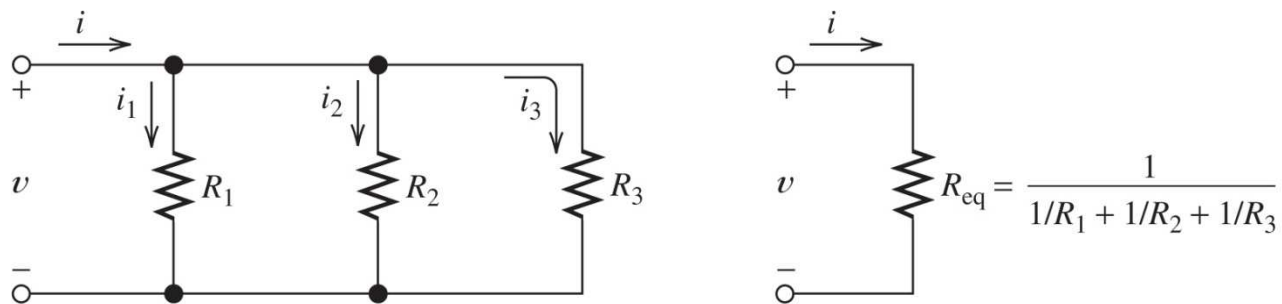
# Resistive Circuits



(a) Three resistances  
in series

(b) Equivalent  
resistance

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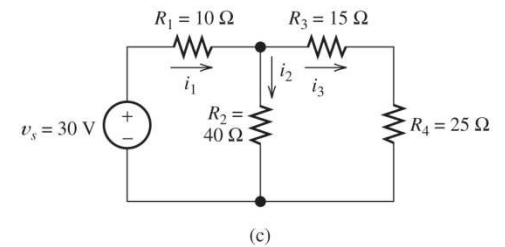
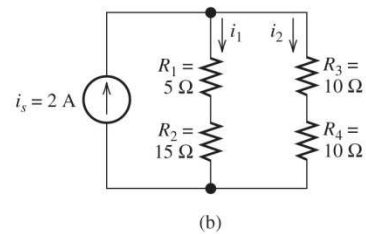
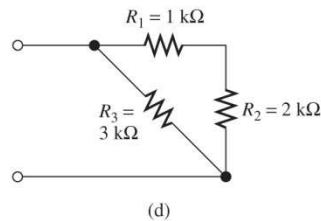
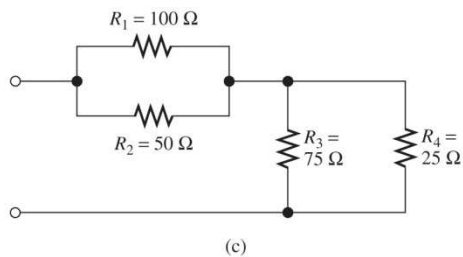
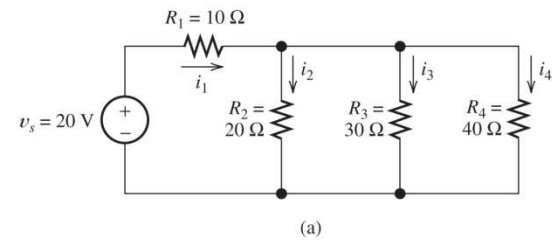
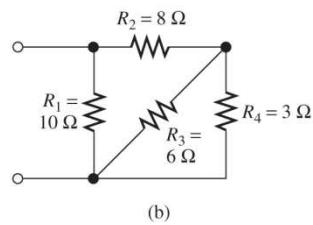
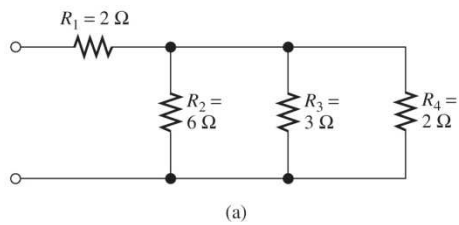
(a) Three resistances in parallel

(b) Equivalent resistance

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# Problems

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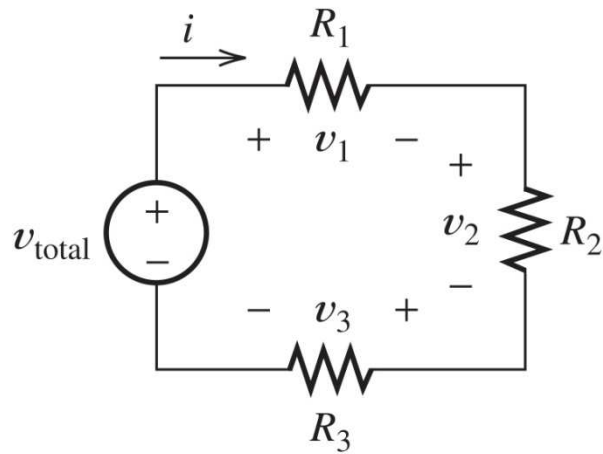


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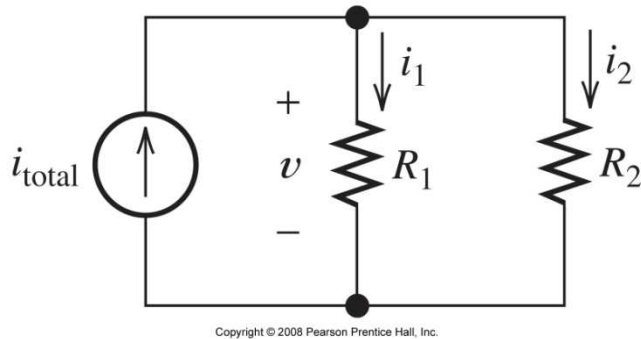
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# Voltage-Divider and Current-Divider Circuits

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$$v_i = \frac{R_i}{R_1 + R_2 + R_3} v_{total}$$

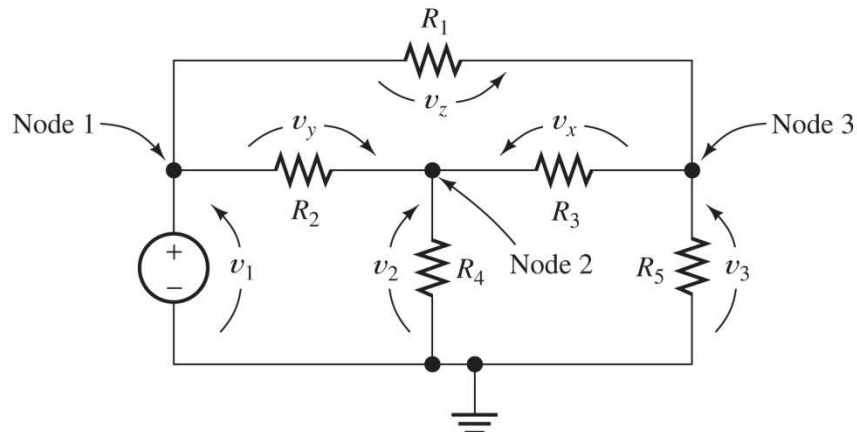


$$i_i = \frac{R_i}{R_1 + R_2} i_{total}$$

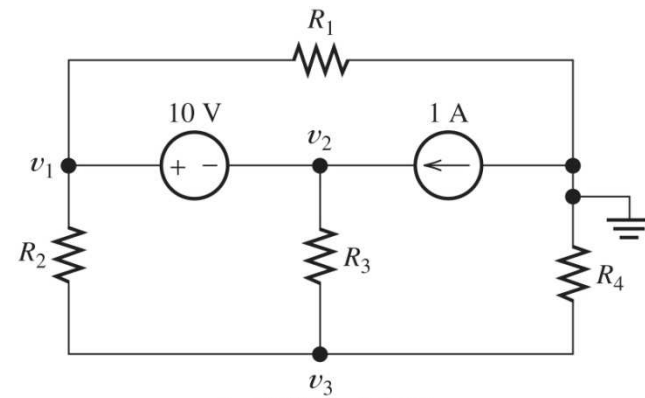




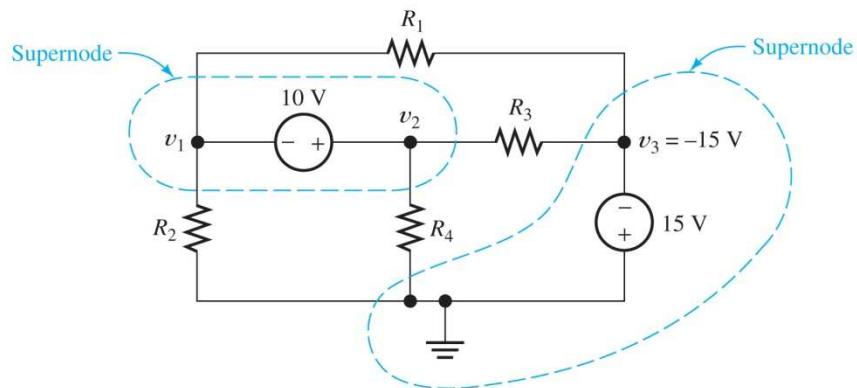
# Node-Voltage Analysis



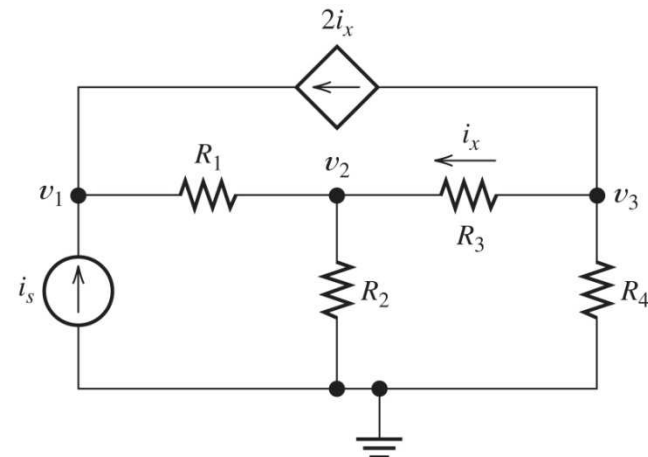
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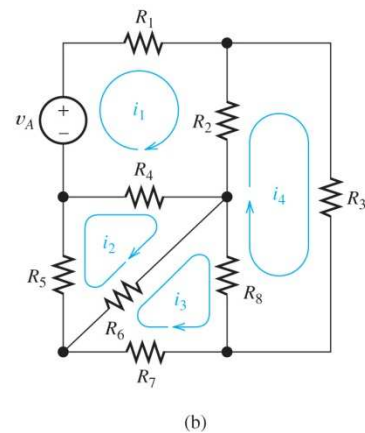
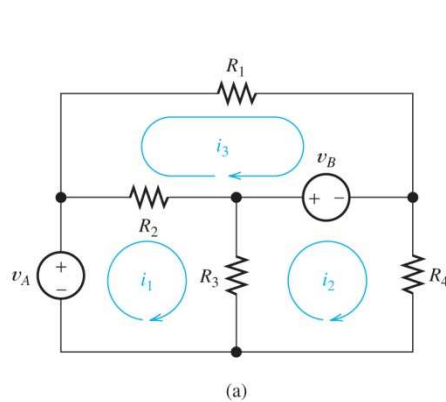


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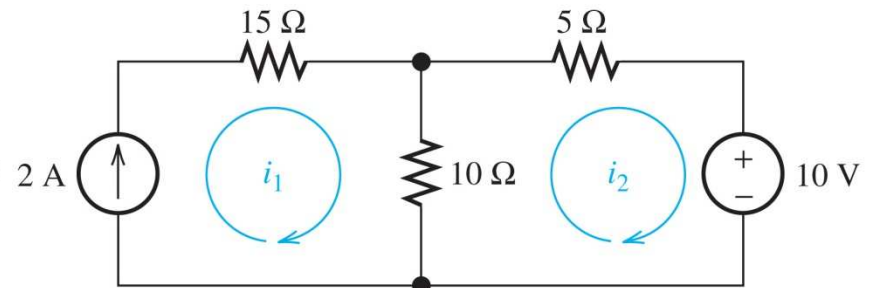


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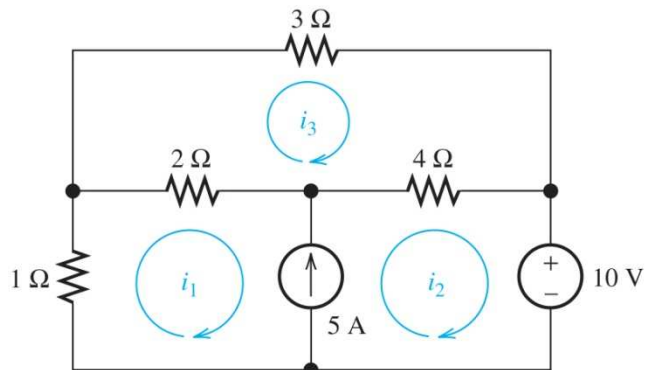
# Mesh-Current Analysis



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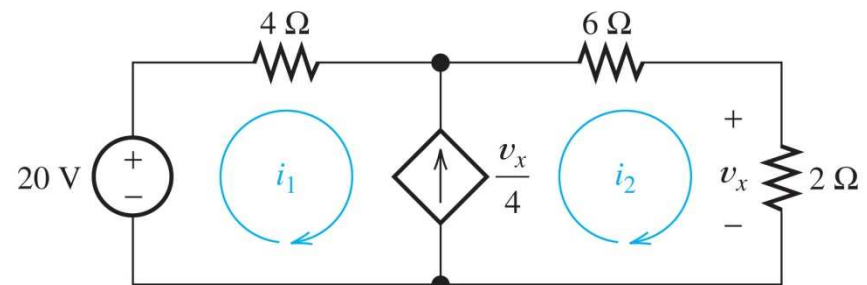


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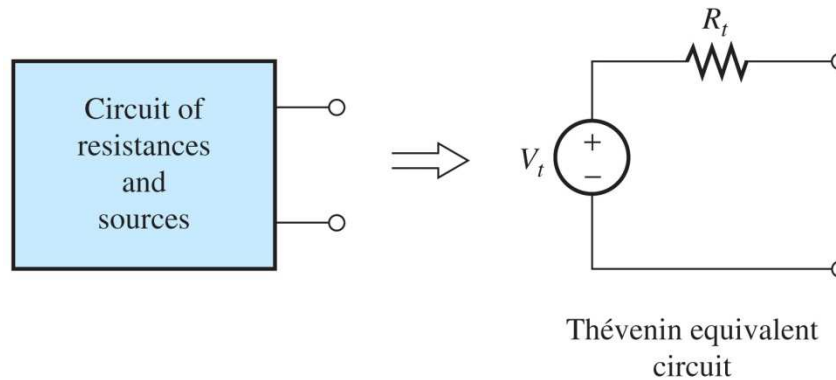
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$$i_1 - i_2 = 5$$

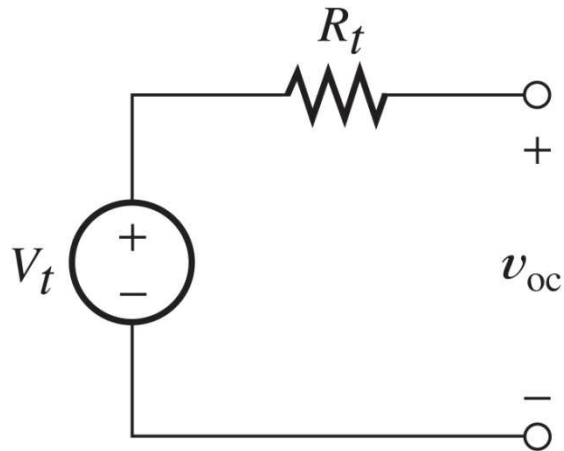


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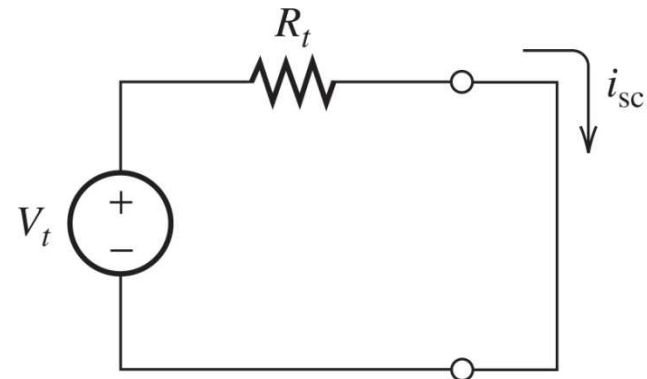
# Thévenin Equivalent Circuits



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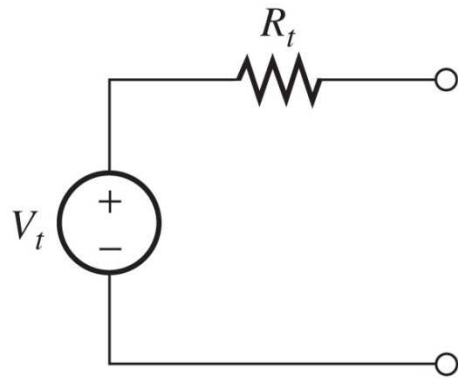
$$V_t = v_{oc} \quad \Rightarrow \quad R_t = \frac{v_{oc}}{i_{sc}} \quad \Leftarrow \quad i_{sc} = \frac{V_t}{R_t}$$



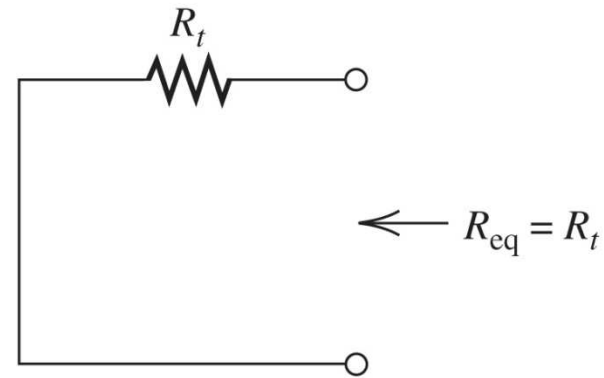
# Thévenin Resistance

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## ► Zeroing sources



(a) Thévenin equivalent

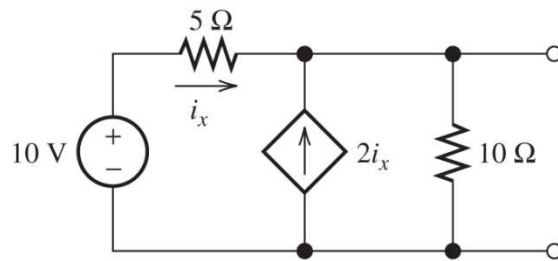


(b) Thévenin equivalent with its source zeroed

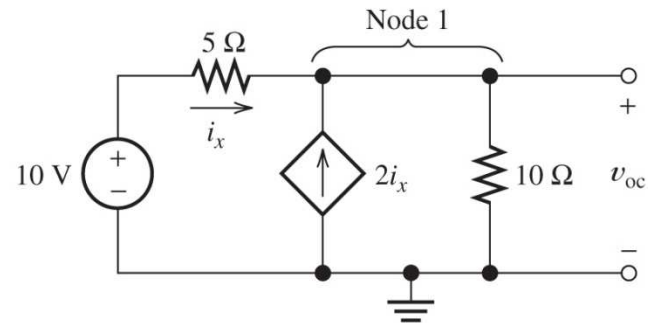
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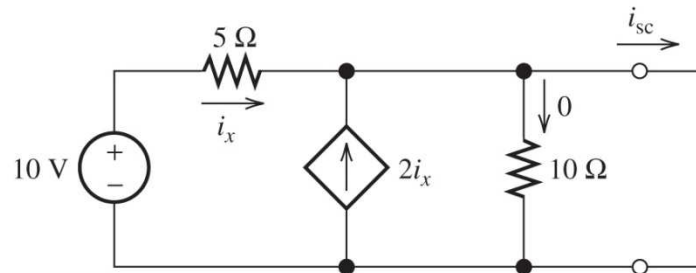
# Thévenin Circuit Example



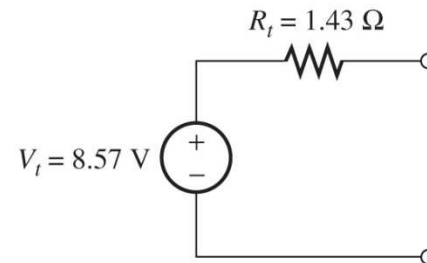
(a) Original circuit



(b) Circuit with an open circuit



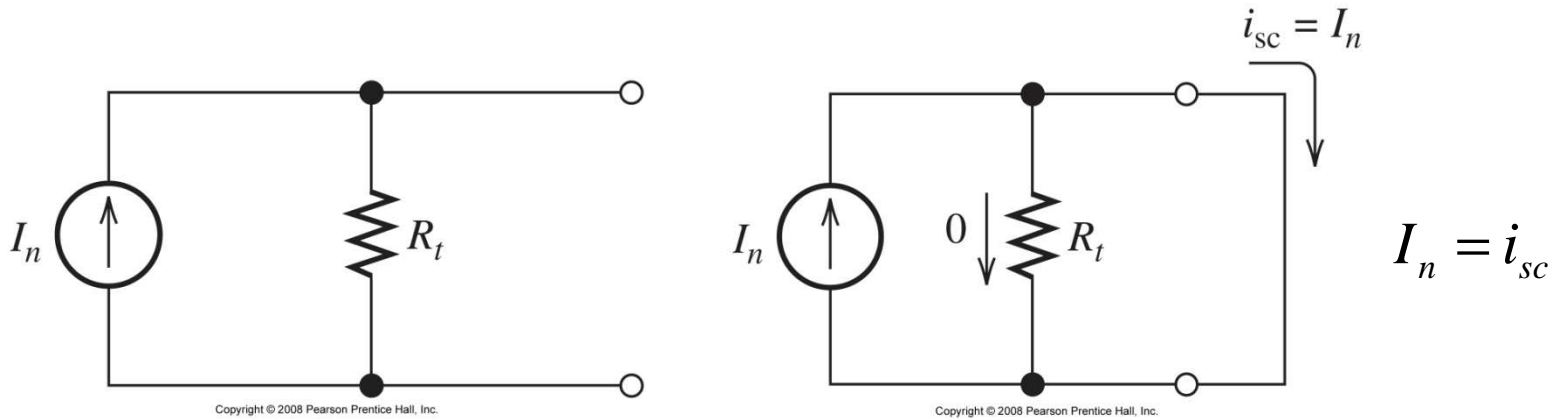
(c) Circuit with a short circuit



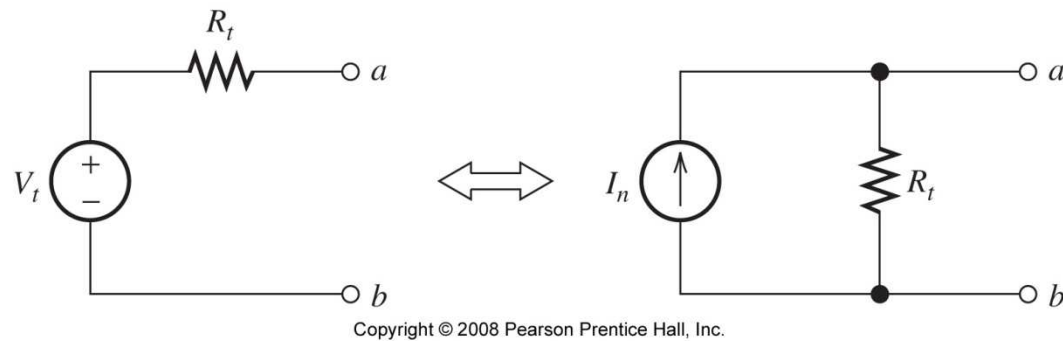
(d) Thévenin equivalent

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# Norton Equivalent Circuits

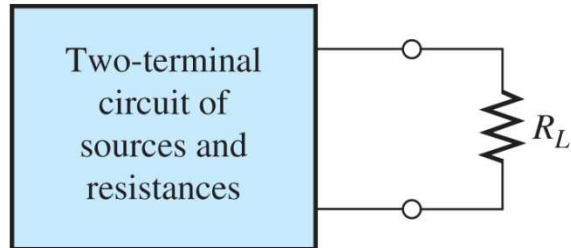


## ► Source Transformation

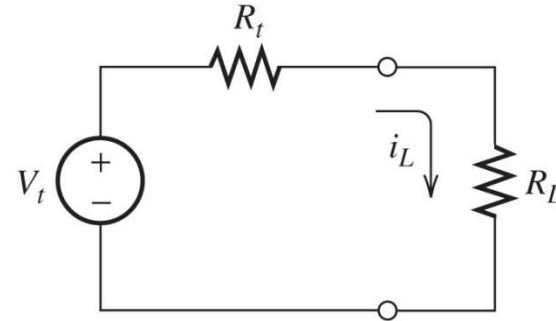


$$I_n = V_t / R_t$$

# Maximum Power Transfer



(a) Original circuit with load



(b) Thévenin equivalent circuit with load

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$$p_L = \frac{V_t^2 R_L}{(R_t + R_L)^2}$$

$$\frac{dp_L}{dR_L} = 0 \Rightarrow R_L = R_t$$

$$P_{L\max} = \frac{V_t^2}{4R_t}$$

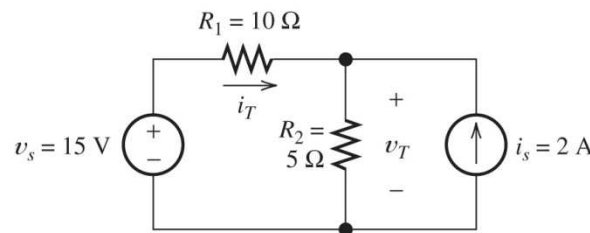
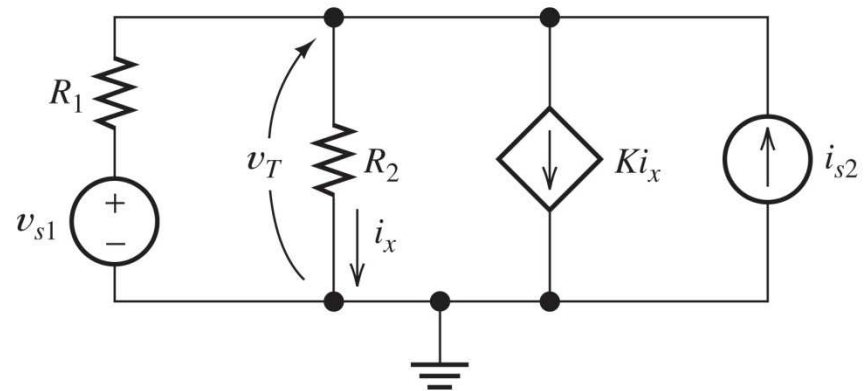
# Superposition Principle

- ▶ Total response is the sum of the responses to each of the independent sources acting individually
- ▶ Linearity

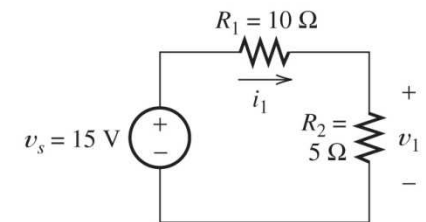
$$v = 10i^2$$

$$i_{cs} = K \cos(i_x)$$

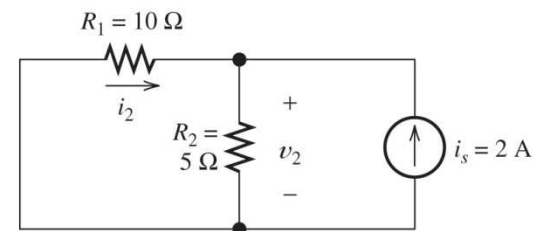
$$i = e^v$$



(a) Original circuit



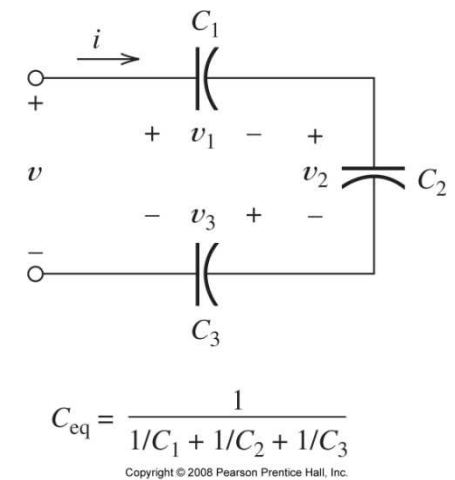
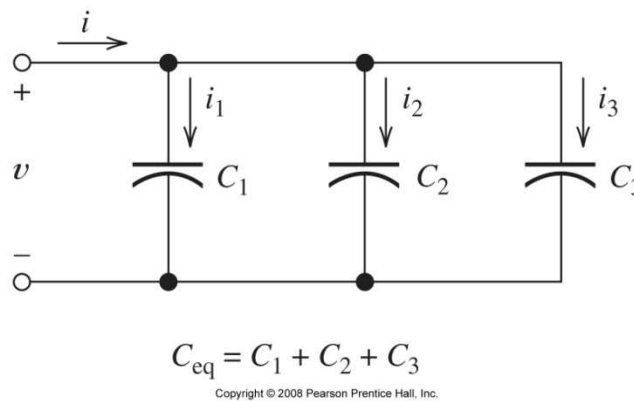
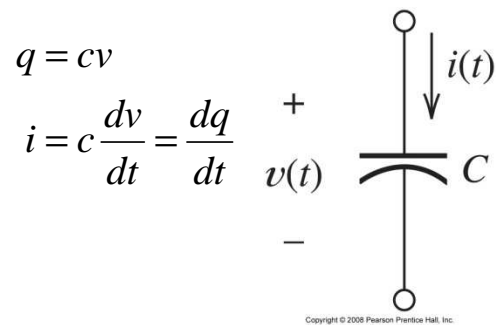
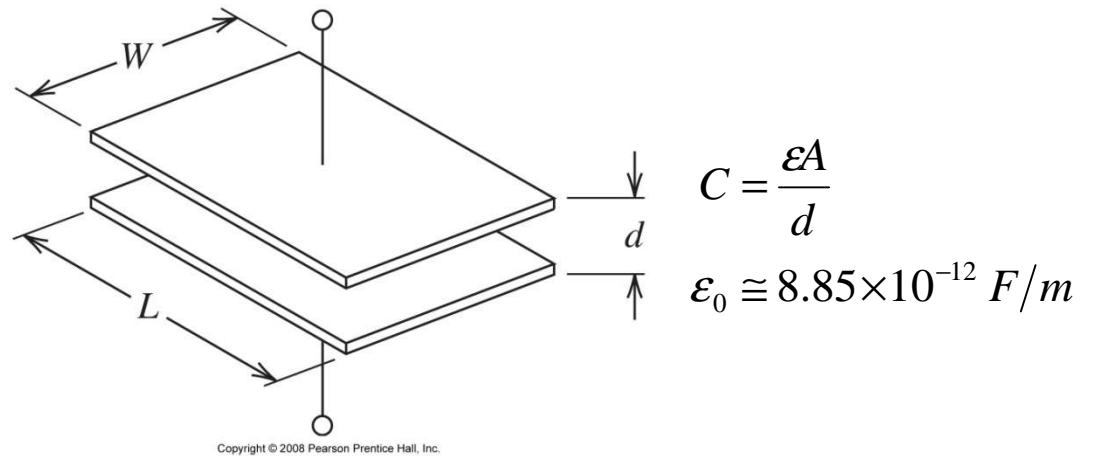
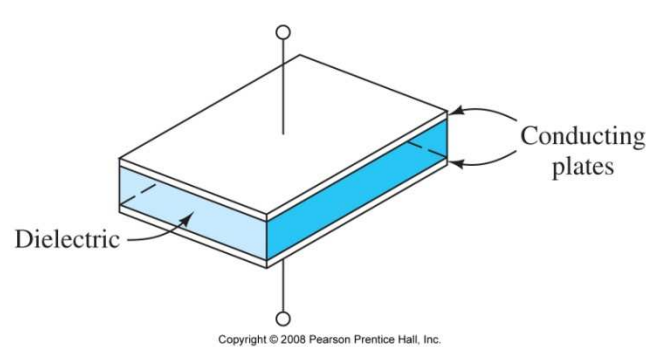
(b) Circuit with only the voltage source active



(c) Circuit with only the current source active



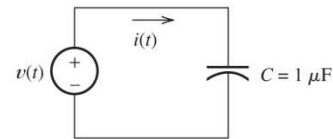
# Capacitance



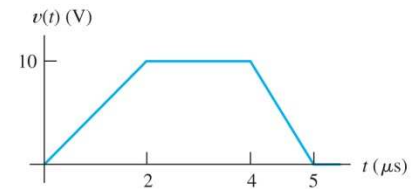
# Voltage and Stored Energy

$$q(t) = \int_{t_0}^t i(t) dt + q(t_0)$$

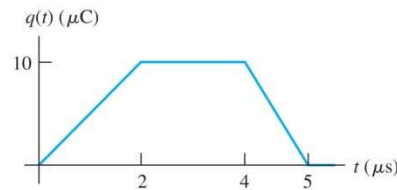
$$v(t) = \frac{1}{C} \int_{t_0}^t i(t) dt + v(t_0)$$



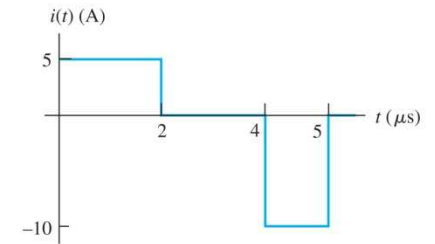
(a)



(b)



(c)



(d)

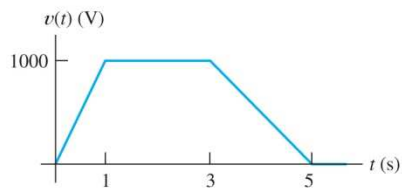
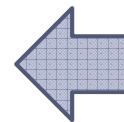
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$$p(t) = v(t)i(t) = Cv \frac{dv}{dt}$$

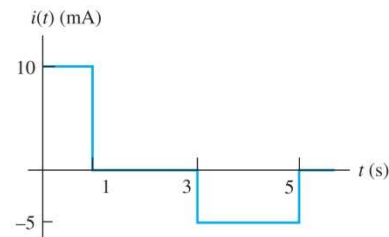
$$w(t) = \int_{t_0}^t p(t) dt = \frac{1}{2} Cv^2(t)$$

$$= \frac{1}{2} v(t)q(t)$$

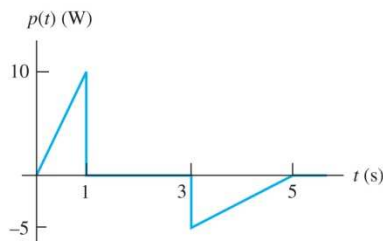
$$= \frac{q^2(t)}{2C}$$



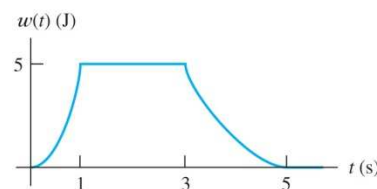
(a)



(b)



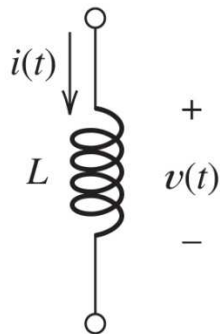
(c)



(d)

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# Inductance



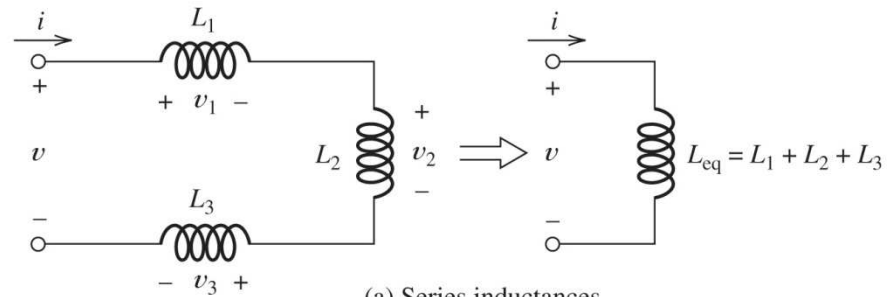
$$v(t) = L \frac{di}{dt}$$

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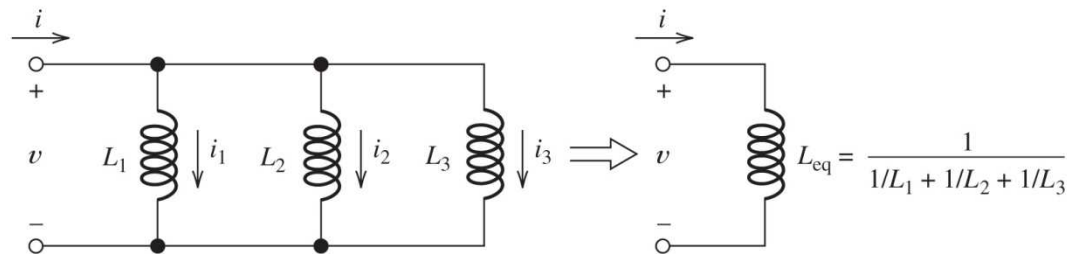
$$i(t) = \frac{1}{L} \int_{t_0}^t v(t) dt + i(t_0)$$

$$p(t) = Li(t) \frac{di}{dt}$$

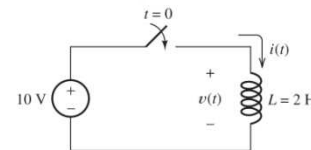
$$w(t) = \frac{1}{2} Li^2(t)$$



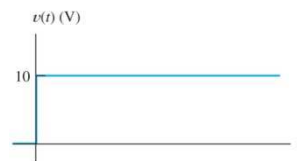
(a) Series inductances



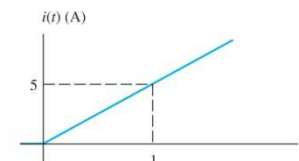
(b) Parallel inductances



(a)



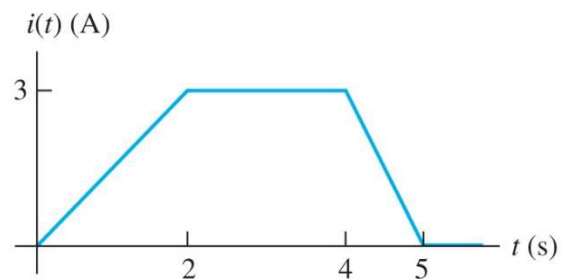
(b)



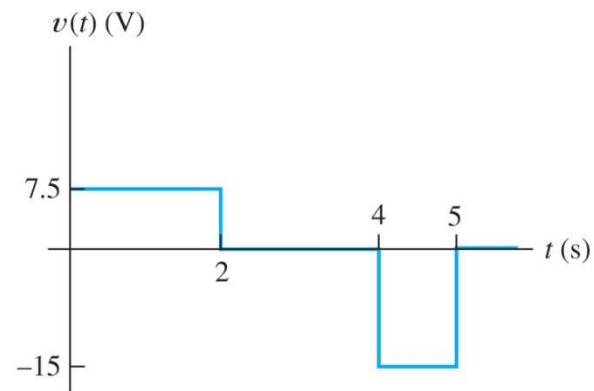
(c)

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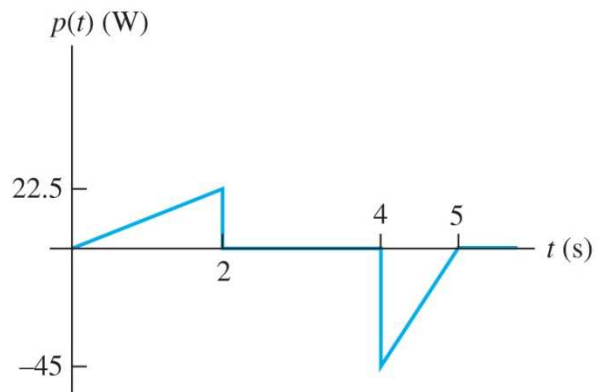
# Voltage, Power, and Energy for Inductance



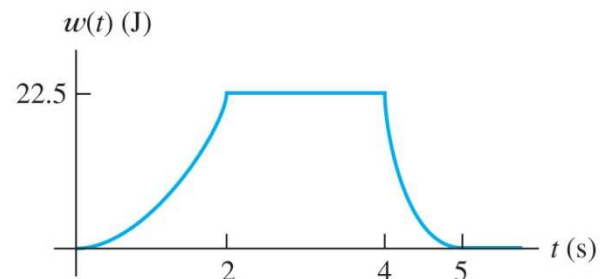
(a)



(b)



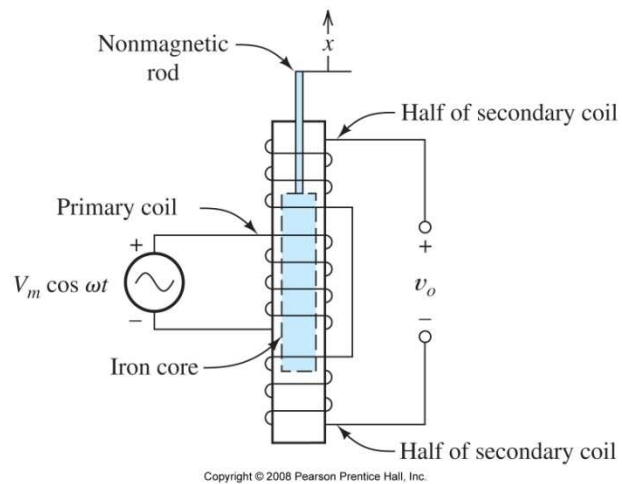
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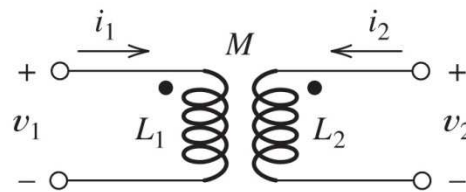
(d)

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# Mutual Inductance



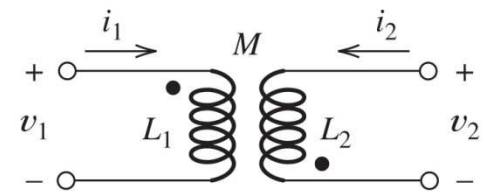
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$$v_1 = L_1 \frac{di_1}{dt} + M \frac{di_2}{dt}$$

$$v_2 = M \frac{di_1}{dt} + L_2 \frac{di_2}{dt}$$

(a)



$$v_1 = L_1 \frac{di_1}{dt} - M \frac{di_2}{dt}$$

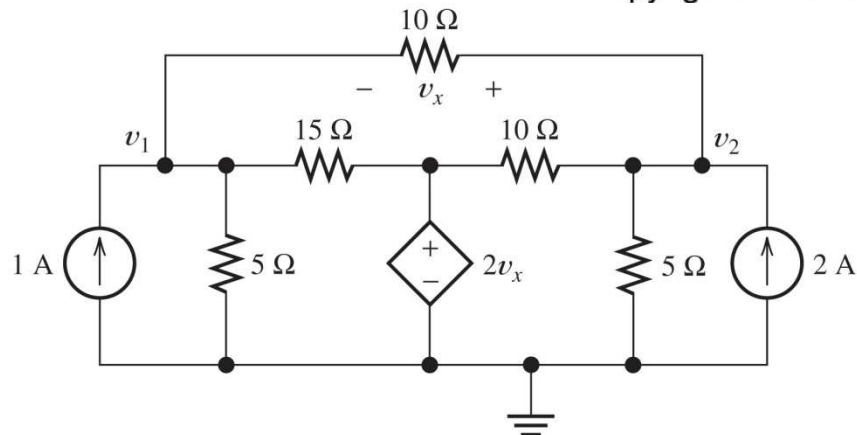
$$v_2 = -M \frac{di_1}{dt} + L_2 \frac{di_2}{dt}$$

(b)

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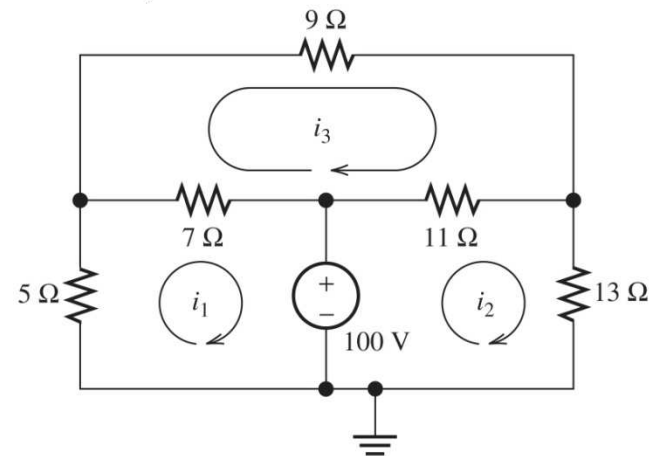
# Problems

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solve for node voltage



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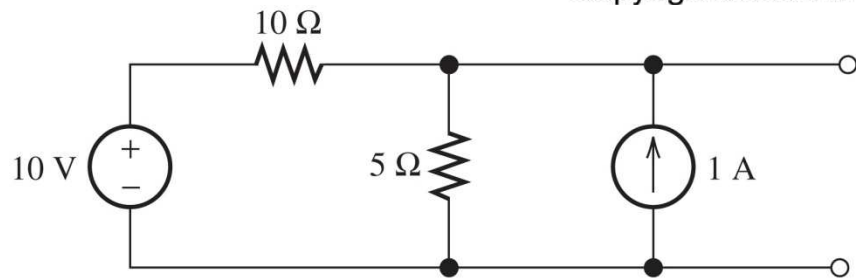
solve for power delivered by the voltage source



# Problems

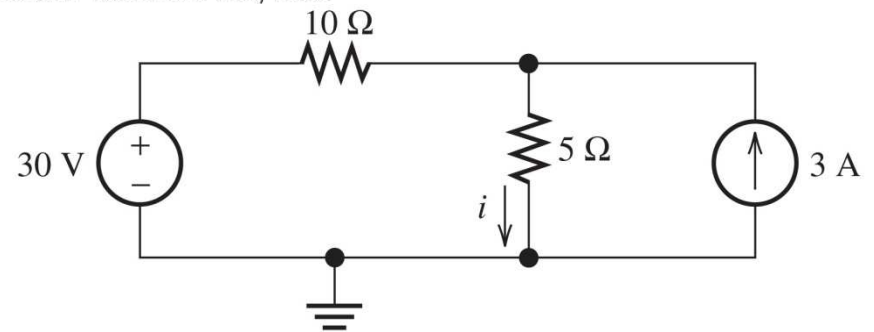


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find Thévenin and Norton equivalent circuits



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find  $i$  using superposition principle

