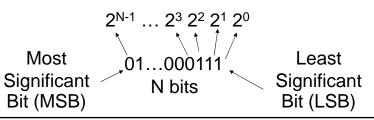
# Electrical and Electronic Measurements:

# Digital Electronic Meters

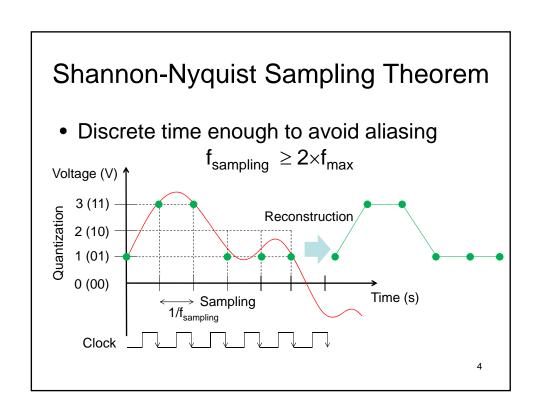
1

# **Digital Signal**

- Binary or two stages:
  - "0" (Low voltage → 0 3 V)
  - "1" (High voltage → 4 5 V)
- Binary digit is called "bit".
- Group of bits is called "word".
- 8-bit group is called "byte".
- For N-bit base-2 number = 2<sup>N</sup> levels

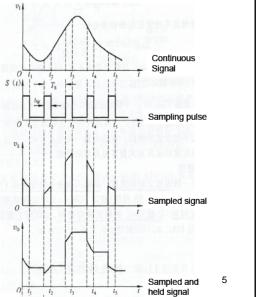


# Analog-to-Digital Process 2 Steps • Sampling and Holding (S/H) • Quantizing and Encoding (Q/E) Input analog signal $u_{\rm I}(t)$ Quantizing $u_{\rm I}(t)$ Quantizing $u_{\rm I}(t)$ Quantizing $u_{\rm I}(t)$ $u_{\rm I}$



# Sampling and Hold

- Holding signal benefits the accuracy of the A/D conversion.
- Minimum sampling rate should be at least twice the highest data frequency of the analog signal



## Resolution

Quantization error → rounding

Input Voltage	Binary Word	Digital Signal
(V)	(4 bits)	
0.0	0000	
k		
0.1	0001	
k		
0.2	0010	

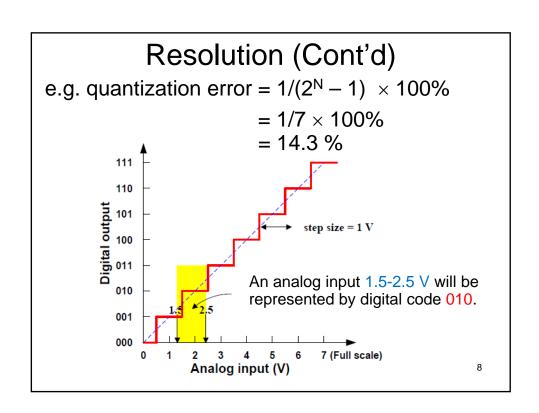
Step size for N-bit code,  $k = V_{reference} / 2^{N}$ 

# Resolution (Cont'd)

Ex Signal from 800-1500 mV may be converted to 8-bit binary codes starting from  $01010000_2$  ( $80_{10}$ ) to  $10010110_2$  ( $150_{10}$ ). In this case, the step size is equal to 10 mV.

$$V_{in} \approx k \times digital output$$

Quantization error = 
$$\frac{\text{step size}}{\text{full scale}} \times 100 \%$$



# Accuracy of A/D

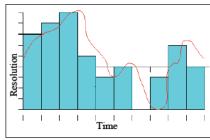
There are two ways to best improve the accuracy of A/D conversion:

- Increasing the sampling rate which increases the maximum frequency that can be measured.
- Increasing the resolution which improves the accuracy in measuring the amplitude of the analog signal.

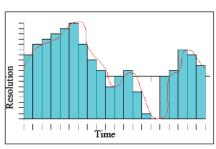
9

# Accuracy of A/D (Cont'd)

Low accuracy

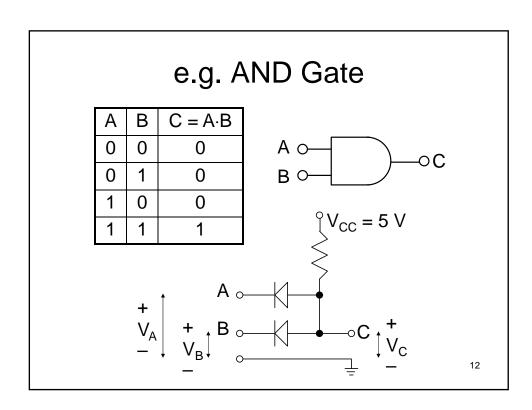


Improved



# **Digital Electronic Basics**

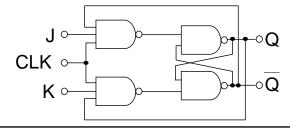
- Logic gate, e.g. AND, OR, NAND, NOR, NOT, XOR
- Adder and Subtractor
- Flip-Flop, e.g. RS-FF, JK-FF
- Shift Register
- Counter
- Digital display, e.g. LED, 7-Segment, LCD



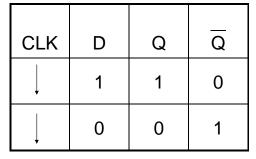
e.g. J-K Flip Flop

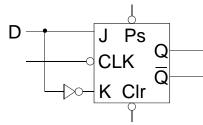
It is like a memory.

J	K	CLK	Q	
0	0	1	Q	Unchanged
0	1	1	0	Reset
1	0	1	1	Set
1	1	1	Q	Toggle

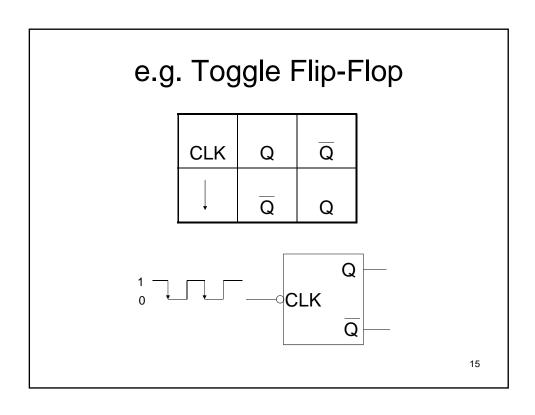


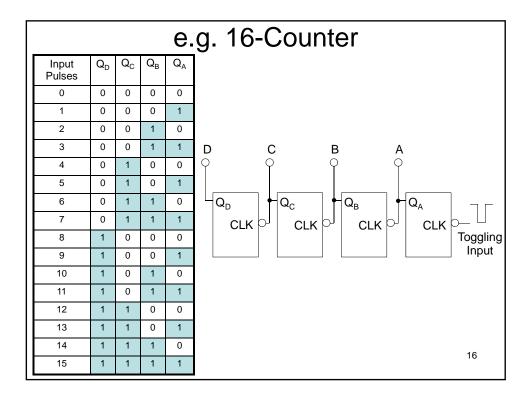
e.g. D Flip-Flop



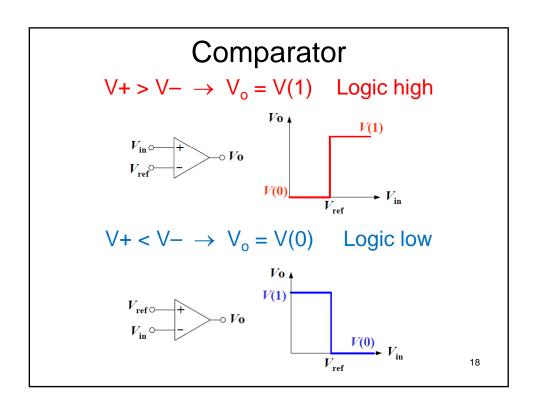


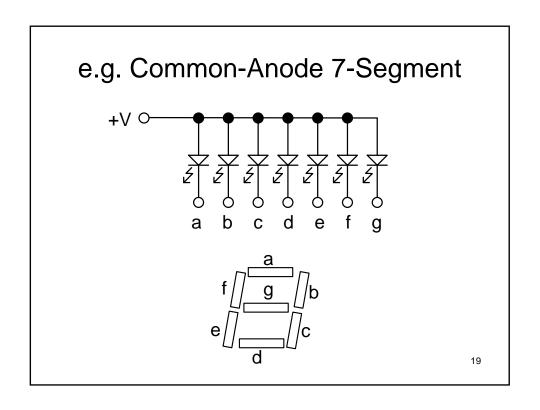
14

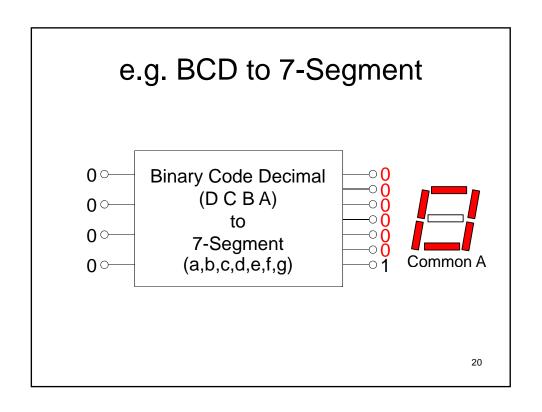




# e.g. Parallel-In Parallel-Out Shift Register







# Liquid Crystal Display

Glass Plates

- There is a set of two 90°-polarlized transparent panels with a liquid crystal solution between them.
- Horizontal filer Molecule • When electricity is applied to one of the segments, the crystals line up in such a way as to make the light twists through the panels and is visible on the other side.

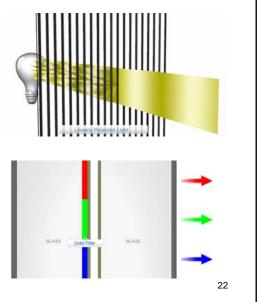
21

Vertical Filter

Crystal

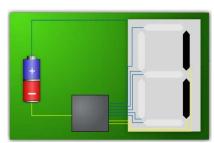
# Liquid Crystal Display (Cont'd)

- Light is shined from behind the panels.
- Color filters are used in color LCD. where each color sub-pixel is controlled individually



# **Direct Address Display**

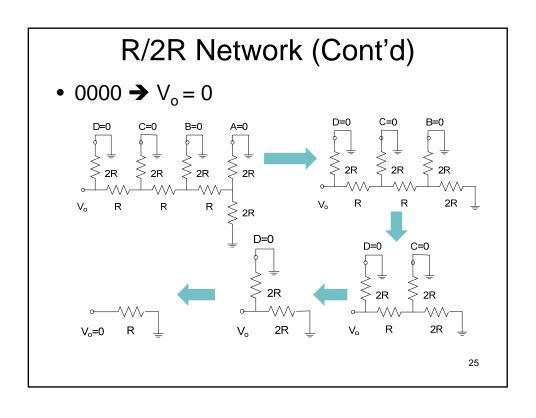
 When the display include limited variable components, e.g. watches or calculators

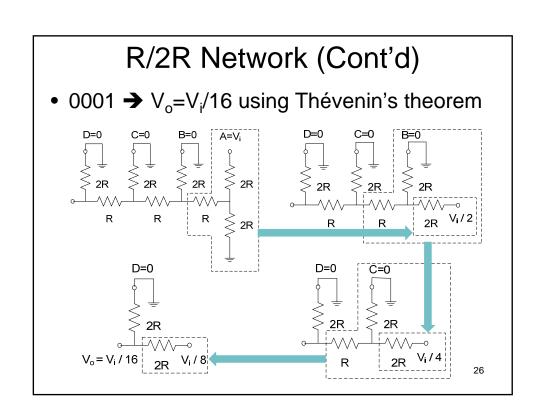


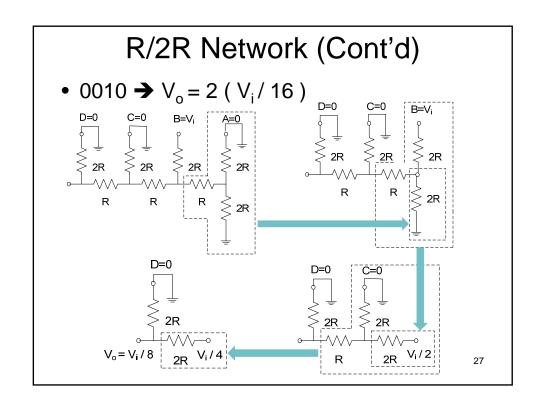
 Simple electronics is used to control the components

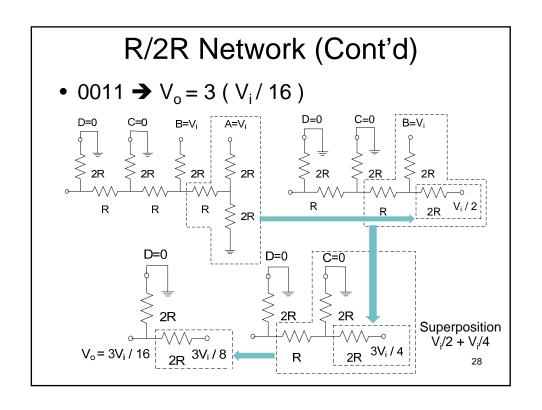
 Simple types of LCDs such as in pocket calculators are built without an internal light source, requiring external light sources to convey the display image to the user.

e.g. R/2R Network V<sub>i</sub> at each input **Analog Output** Digital-to-analog converter  $(V_o)$ С 0 0 0 0 V<sub>i</sub> / 16 1 0 2 (V<sub>i</sub> / 16)  $2R \lesssim 2R \lesssim 2R \lesssim 2R \lesssim$ 3 (V<sub>i</sub> / 16) 4 (V<sub>i</sub> / 16) 5 ( V<sub>i</sub> / 16 ) 0 1 R 6 ( V<sub>i</sub> / 16 ) R 0 1 0 1 0 1 1 1 7 (V<sub>i</sub> / 16) 8 (V<sub>i</sub> / 16) 1 9 (V<sub>i</sub> / 16) 0 0 10 (V<sub>i</sub> / 16) 11 ( $V_i/16$ ) 12 (V<sub>i</sub> / 16) 0 1 13 ( V<sub>i</sub> / 16 ) 0 14 ( V<sub>i</sub> / 16 ) 15 (V<sub>i</sub> / 16)



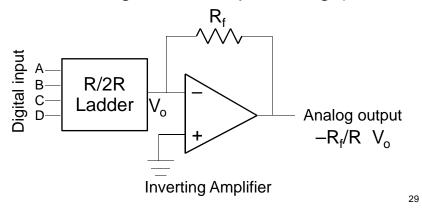






# R/2R Network (Cont'd)

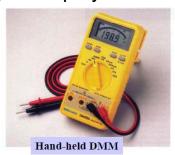
 The speed of the converter is limited by the output amplifier slew rate (the maximum rate of change of the output voltage).



# Digital Multimeter (DMM)

Digital multimeter is an electronic volt-ohm-milliammeter with digital display.

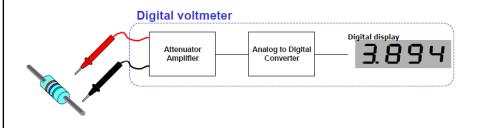






# **Digital Voltmeter**

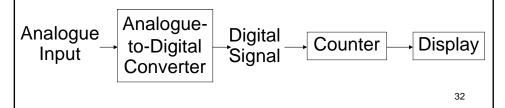
Digital voltmeter (DVM) is essentially an analog to digital converter (A/D) with a digital display.



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# Digital Voltmeter (Cont'd)

- Analogue voltage is sampled at some instant of time (sampled and hold) and converted to digital signals (series of pulses).
- Number of pulses related to the voltage is counted and displayed as digits.



# Advantages over Analog Meters

- The numerical readout reduces the human reading error, many readers read the same value, and makes no parallax error
- Faster reading
- The accuracy is much higher e.g. the best tolerance of analog meters is about ±0.5%, while it is about ±0.005% for digital meters
- Higher precision (repeatability) and also contain automatic ranging
- No moving part, life will be long
- Digital signal processing is possible e.g. hold, max, min, polarity or peak

# Disadvantages

- Battery needed for electronic circuits
- Cannot show trend and continuous changing number not easy to be interpreted (bar graph may be optional added)
- Cannot measure very high frequency signals (not more than Nyquist rate of sampling)

In spite of above mentioned disadvantages, the digital meters are gaining popularity and are most widely used.

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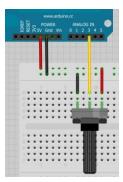
# Relating ADC Value to Voltage

 Not every pin on a microcontroller has the ability to do analog to digital conversions.
 On the Arduino board, these pins have an 'A' in front of their label (A0 through A5) to indicate these pins can read analog voltages.

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# Relating ADC Value to Voltage (Cont'd)

• The ADC on the Arduino is a 10-bit ADC meaning it has the ability to detect 2<sup>10</sup> or 1,024 discrete analog levels (0-1,023).



### Ratiometric Value

- Analog to digital conversions are dependant on the system voltage.
- For example, the 10-bit ADC of the Arduino on a 5V system, if the analog voltage is 2.12V what will the ADC report as a value?

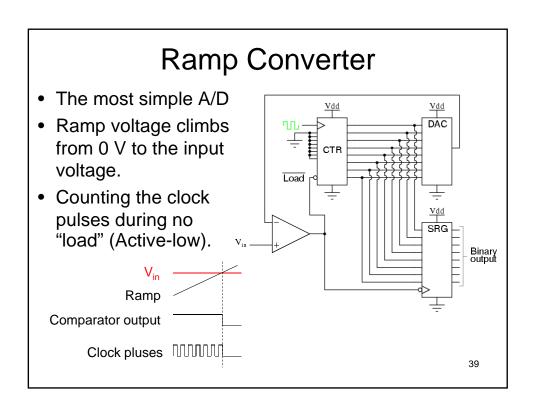
$$\frac{Resolution\ of\ the\ ADC}{System\ Voltage} = \frac{ADC\ Reading}{Analog\ Voltage\ Measured}$$
 
$$\frac{1023}{5.00V} = \frac{x}{2.12V}$$

The ADC should report 434.

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# Analogue-to-Digital Converter

- 1. Ramp converter
- 2. Successive approximation
- 3. Flash converter
- 4. Voltage-to-frequency converter
- 5. Dual-slope DVM
- 6. Delta-sigma converter



# Ramp Converter (Cont'd)

 Slow conversion and conversion time depends on the magnitude of input signal.

$$T_{\text{conversion, max}} = (2^{N} - 1) \times \text{clock period}$$

$$\frac{Analog}{\text{input}}$$

$$\frac{Analog}{\text{time}}$$

$$\frac{Digital}{\text{output}}$$

$$\frac{Digital}{\text{time}}$$

# **Successive Approximation**

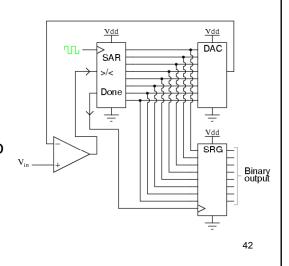
Ramp converter Vs. Successive approximation



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# Successive Approximation (Cont'd)

- Input voltage is compared to the voltage increased until it reaches the input voltage.
- Trying all values starting with MSB to LSB.



Successive Approximation (Cont'd)					
D (MSB)	С	В	A (LSB)	$\mathbf{V}_{ref}$	0.77
0	0	0	0	0	e.g. V <sub>in</sub> = 9 V
0	0	0	1	1	• V <sub>ref1</sub> = 8 V ( <u>1</u> 000)
0	0	1	0	2	if $V_{in} > V_{ref1} \rightarrow D = "1"$
0	0	1	1	3	
0	1	0	0	4	• V <sub>ref2</sub> = 12 V ( <b>1<u>1</u>00</b> )
0	1	0	1	5	if $V_{in} < V_{ref2} \rightarrow C = "0"$
0	1	1	0	6	
0	1	1	1	7	• V <sub>ref3</sub> = 10 V ( <b>10<u>1</u></b> 0)
1	0	0	0	8	$\int \text{if } V_{\text{in}} < V_{\text{ref3}} \rightarrow B = "0"$
1	0	0	1	9	
1	0	1	0	10	• $V_{ref4} = 9 \ V \ (1001)$
1	0	1	1	11	if $V_{in} = V_{ref4} \rightarrow A = "1"$
1	1	0	0	12	V <sub>in</sub> = <b>1001</b>
1	1	0	1	13	
1	1	1	0	14	
1	1	1	1	15	43

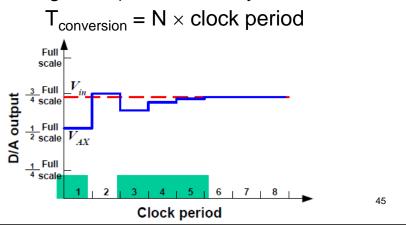
# Successive Approximation (Cont'd)

Ex To determine a number between 0-511 (9-bit binary), given the number to be determined is 301.

No.	Esti	Results	
1	256	1 0000 0000	$V_{in} > V_{AX}$
2	256+128 = 384	1 1000 0000	<
3	256+64 = 320	1 0100 0000	<
4	256+32 = 288	1 0010 0000	>
5	288+16 = 304	1 0011 0000	<
6	288+8 = 296	1 0010 1000	>
7	296+4 = 300	1 0010 1100	>
8	300+2 = 302	1 0010 1110	<
9	300+1 = 301	1 0010 1101	Finished

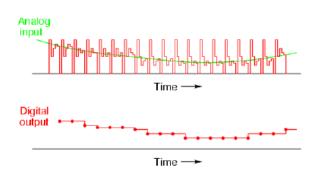
# Successive Approximation (Cont'd)

- The most common A/D for general applications
- Conversion time is fixed (not depend on the signal magnitude) and relatively fast.



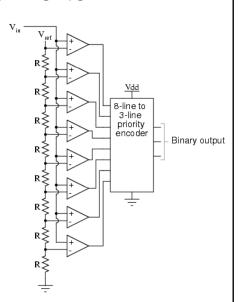
# Successive Approximation (Cont'd)

 When you require higher resolution and do not require high sampling rate



# Flash Converter

- Simultaneous comparison between the analogue input and the reference signals.
- N-bit conversion needs 2<sup>N-1</sup> comparators.
- Consumes power (many op-amps)
- Fast! up to 20 G Samples / second



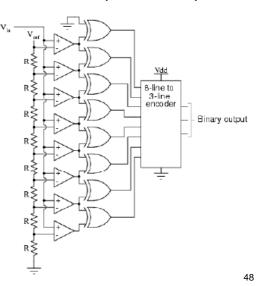
# Flash Converter (Cont'd)

- Implementation of a priority encoder can be simple circuitry.
- 8-line to 3-line priority encoder, e.g.

 $00000001 \Rightarrow 001$ 

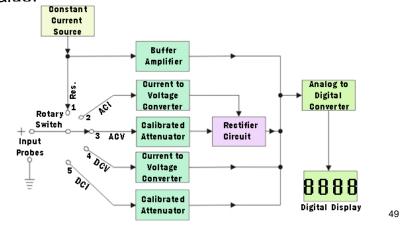
 $00000011 \Rightarrow 010$ 

 $00000111 \Rightarrow 011$ 



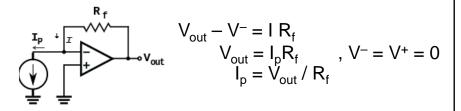
# Digital AC Voltmeter

- Using rectifier in a similar way to analogue meter.
- The average value from rectifier is scaled to RMS value.



# **Digital Ammeter**

- Current can be measured by using digital voltmeter to measure the potential difference across a standard resistor.
- Current-to-voltage converter, e.g.



# **Digital Ohmmeter**

 The DMM supplies a constant current source to the resistor and the meter measures the voltage across it, the voltage being proportional to the resistance.

 $V_{\text{lead}}$   $V = I_{\text{s}}(2R_{\text{lead}} + R_{\text{x}})$   $R_{\text{x}} = V/I_{\text{s}} - 2R_{\text{lead}}$ 51

# Reading Resolution

- For the fixed resolution of .001 V (step size)
   Range 0 1 V → 3 digits ( .999) 1,000 steps
   Range 0 10 V → 4 digits ( 9.999) 10,000 steps
   Range 0 100 V → 5 digits (99.999) 100,000 steps
   Number of digit = log(step)
- How many digit for the range 0 3 V ?
   It is 3½ digits (log(3000) ≈ 3.477 digits)

# Reading Resolution (Cont'd)

- 3½ digit display 0000 → 1999
   (e.g. full scale 2 V if enable the 1<sup>st</sup> decimal point, 0.000 → 1.999)
- MSB can only be "0" or "1" (usually not visible when the reading is less than 999), whereas all the other can be "0", "1", "2", "3", ..., "9"
- e.g. 1V range, resolution is 1V/1999 ≈ 0.001 V (0.000, 0.001, 0.002, ..., 0.999)
- For 3¾ digit for the range 5 V, MSB can be "0" to "4"

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# Range Changing

Auto ranging by using frequency divider circuit to change clock frequency.

 e.g. 3½ digit display (fixed digit), change step size

Range 1V (0.999) →

 $1/1999 \approx 0.001 \text{ V/step } (1,000 \text{ steps})$ 

Range 10V (09.99) →

 $10/1999 \approx 00.01 \text{ V/step } (1,000 \text{ steps})$ 

Range 100V (099.9) →

 $100/1999 \approx 000.1 \text{ V/step (1,000 steps)}$ 

## Accuracy

- $\pm$  ( 0.5% Reading + 1 Digit LSB )
- e.g. when you read a voltage 1.8 V

error =  $\pm$  (0.5% of 1.8V + 0.001V) =  $\pm$  0.01V

 $\approx \pm 0.56\%$  of reading

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### Reference

- https://learn.sparkfun.com/tutorials/analogto-digital-conversion
- http://en-us.fluke.com/training/traininglibrary/test-tools/digital-multimeters/digitalmultimeter-fundamentals.html
- https://www.electrical4u.com/digitalmultimeter/
- How to Control LCD Displays Arduino Tutorial: https://youtu.be/85LvW1QDLLw