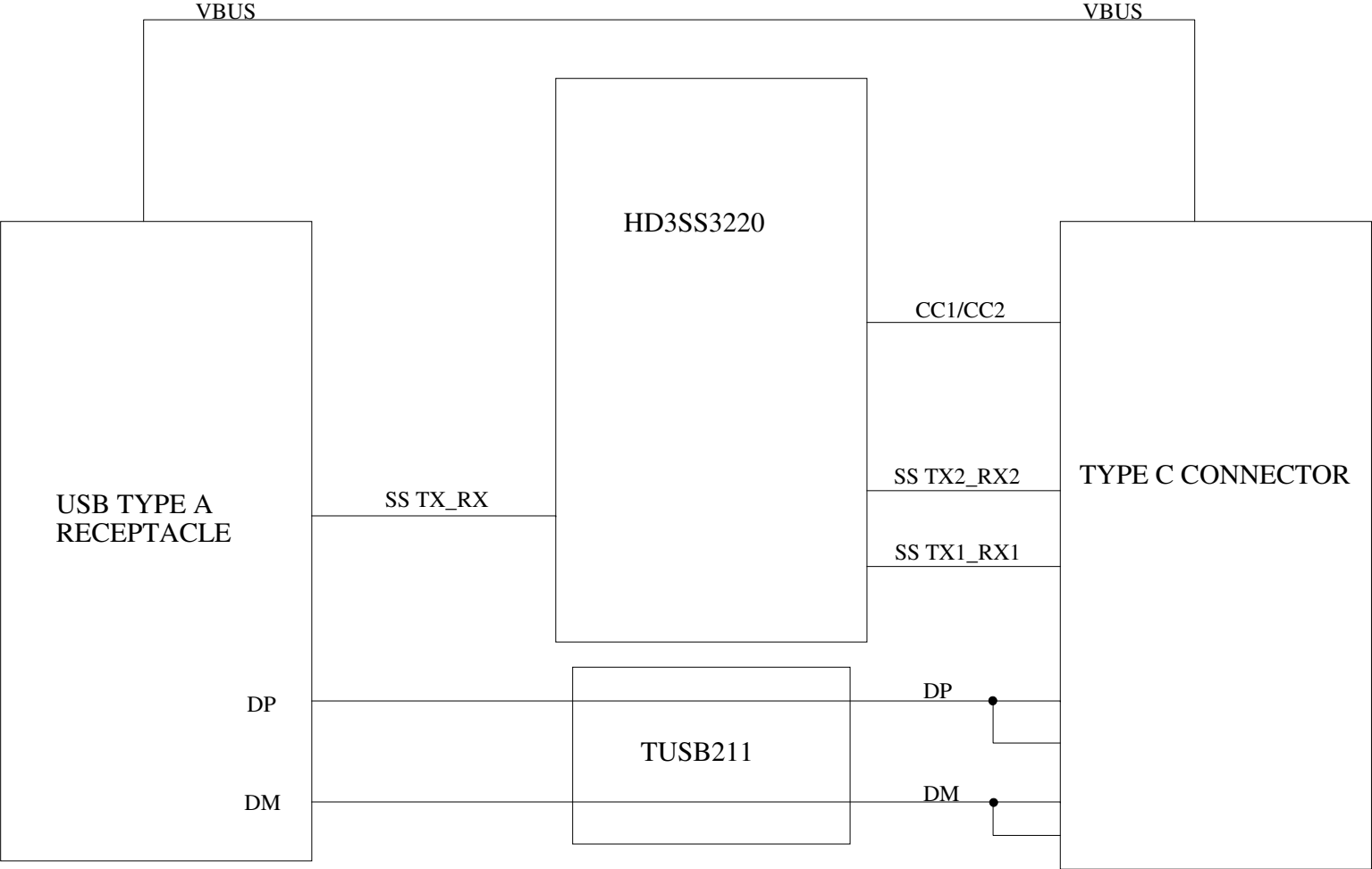
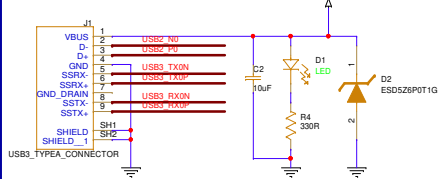


BLOCK DIAGRAM



# MAIN

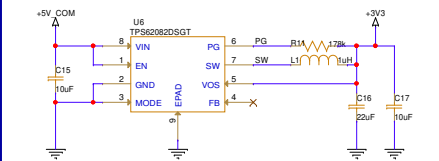
## USB Legacy Device Connection



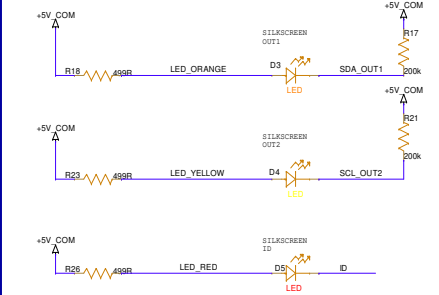
## DESIGN NOTES

1. Install only if active discharge circuit exists.
2. Current Mode valid only in DFP mode.
3. Bulk capacitor for VCONN between 10uF to 220uF.
4. C10, C12, R12, R13, R14 and R15 are placed for interoperability purposes with USB Vcm above 2V.
5. C9, C11, C13 and C14 are placed for interoperability purposes with USB Vcm above 2V.
6. ALL DIFF PAIRS ARE ROUTED 85 TO 90 OHMS DIFFERENTIAL AND 50 OHMS COMMON MODE. ALL OTHER TRACES ARE 50 OHMS.

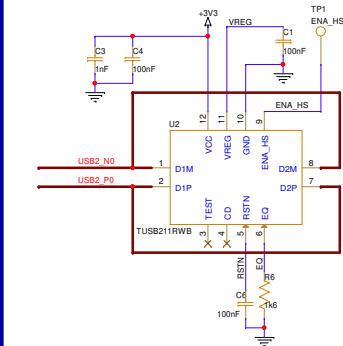
### Step Down Converter (5V to 3V3)



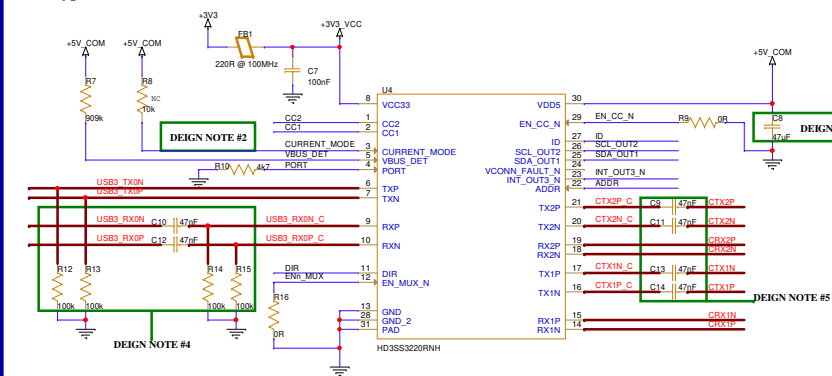
## DFP Indicators



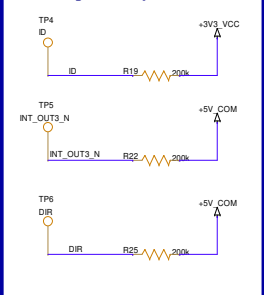
## High Speed Signal Conditioner



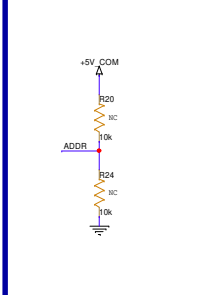
## USB TypeC DRP Port Controller



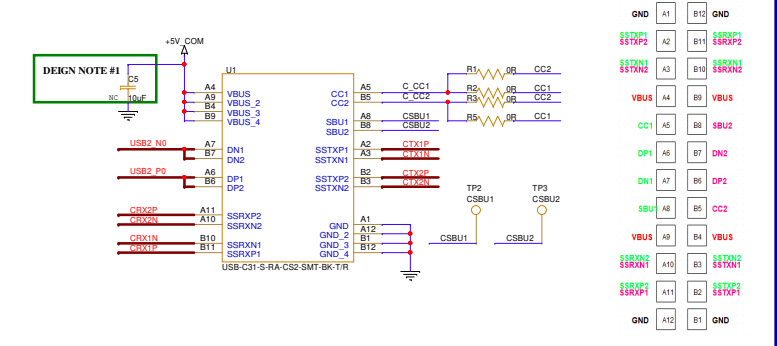
**Test Purpose Only**



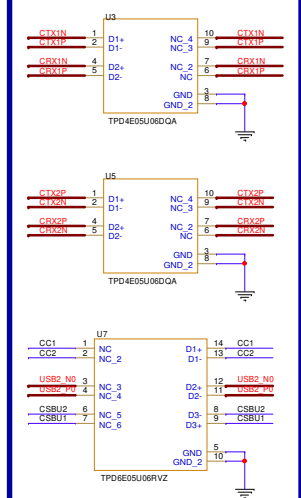
## I2C or GPIO Selector



### USB TypeC Connector



## ESD Protection



## DEIGN NOTE #6