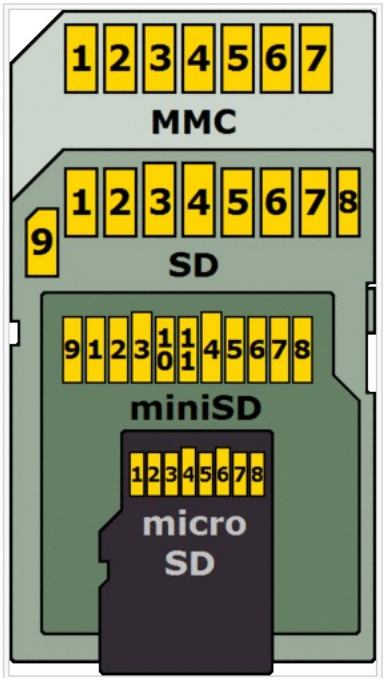
Carte SD et µSD



SPI Bus Mode

MMC Pin	SD Pin	miniSD Pin	microSD Pin	Name	I/O	Logic	Description
1	1	1	2	nCS	I	PP	SPI Card Select [CS] (Negative Logic)
2	2	2	3	DI	I	PP	SPI Serial Data In [MOSI]
3	3	3		VSS	S	S	Ground
4	4	4	4	VDD	S	S	Power
5	5	5	5	CLK	I	PP	SPI Serial Clock [SCLK]
6	6	6	6	VSS	S	S	Ground
7	7	7	7	DO	O	PP	SPI Serial Data Out [MISO]
	8	8	8	NC nIRQ	. O	. OD	Unused (memory cards) Interrupt (SDIO cards) (Negative Logic)
	9	9	1	NC	.	.	Unused
		10		NC	.	.	Reserved
		11		NC	.	.	Reserved

One-Bit SD Bus Mode

MMC Pin	SD Pin	miniSD Pin	microSD Pin	Name	I/O	Logic	Description
1	1	1	2	NC	.	.	Unused
2	2	2	3	CMD	I/O	PP, OD	Command, Response
3	3	3		VSS	S	S	Ground
4	4	4	4	VDD	S	S	Power
5	5	5	5	CLK	I	PP	Serial Clock
6	6	6	6	VSS	S	S	Ground
7	7	7	7	DAT0	I/O	PP	SD Serial Data 0
	8	8	8	NC nIRQ	. O	. OD	Unused (memory cards) Interrupt (SDIO cards) (Negative Logic)
	9	9	1	NC	.	.	Unused
		10		NC	.	.	Reserved
		11		NC	.	.	Reserved

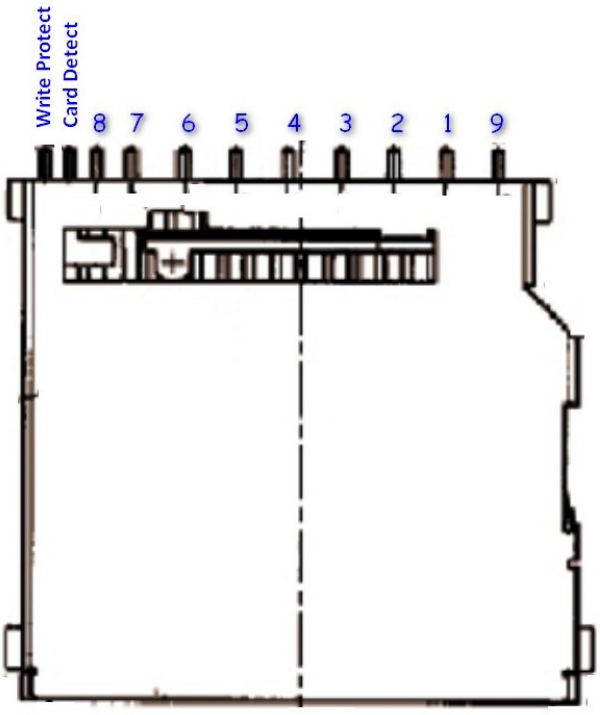
Four-Bit SD Bus Mode

MMC Pin	SD Pin	miniSD Pin	microSD Pin	Name	I/O	Logic	Description
.	1	1	2	DAT3	I/O	PP	SD Serial Data 3
.	2	2	3	CMD	I/O	PP, OD	Command, Response
.	3	3		VSS	S	S	Ground
.	4	4	4	VDD	S	S	Power
.	5	5	5	CLK	I	PP	Serial Clock
.	6	6	6	VSS	S	S	Ground
.	7	7	7	DAT0	I/O	PP	SD Serial Data 0
	8	8	8	DAT1 nIRQ	I/O O	PP OD	SD Serial Data 1 (memory cards) Interrupt Period (SDIO cards share pin via protocol)
	9	9	1	DAT2	I/O	PP	SD Serial Data 2
		10		NC	.	.	Reserved
		11		NC	.	.	Reserved

1. Direction is relative to card. I = Input, O = Output.
2. PP = Push-Pull logic, OD = Open-Drain logic.
3. S = Power Supply, NC = Not Connected (or logical high).

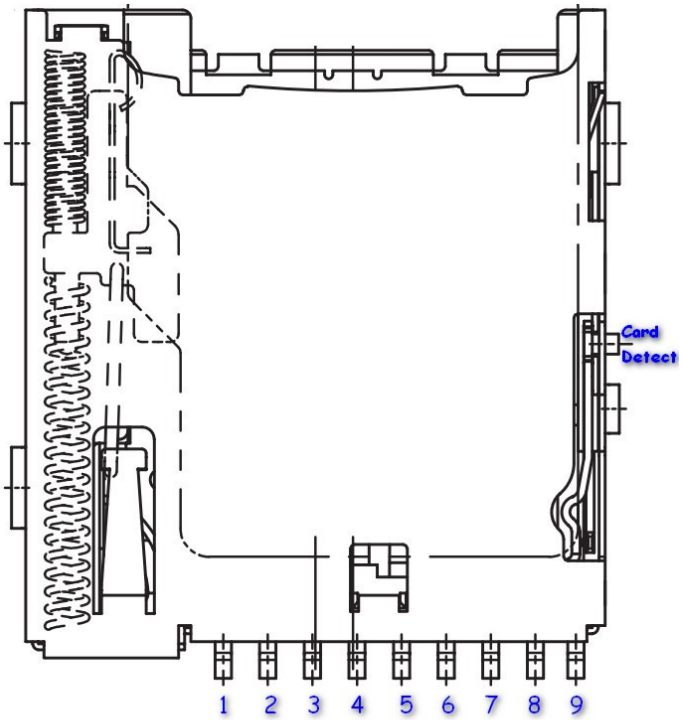
Connecteur Carte SD et µSD

Connecteur Carte SD :



Pin	Signal
1	CD / DATA 3
2	CMD / D1
3	VSS (GND)
4	VDD
5	CLK / SCLK
6	VSS (GND)
7	DATA 0
8	DATA 1
9	DATA 2

Connecteur Carte µSD :



Pin	Signal
1	DATA 2
2	DATA 3
3	CMD
4	VDD
5	CLK / SCLK
6	VSS (GND)
7	DATA 0
8	DATA 1
9	COM / GND