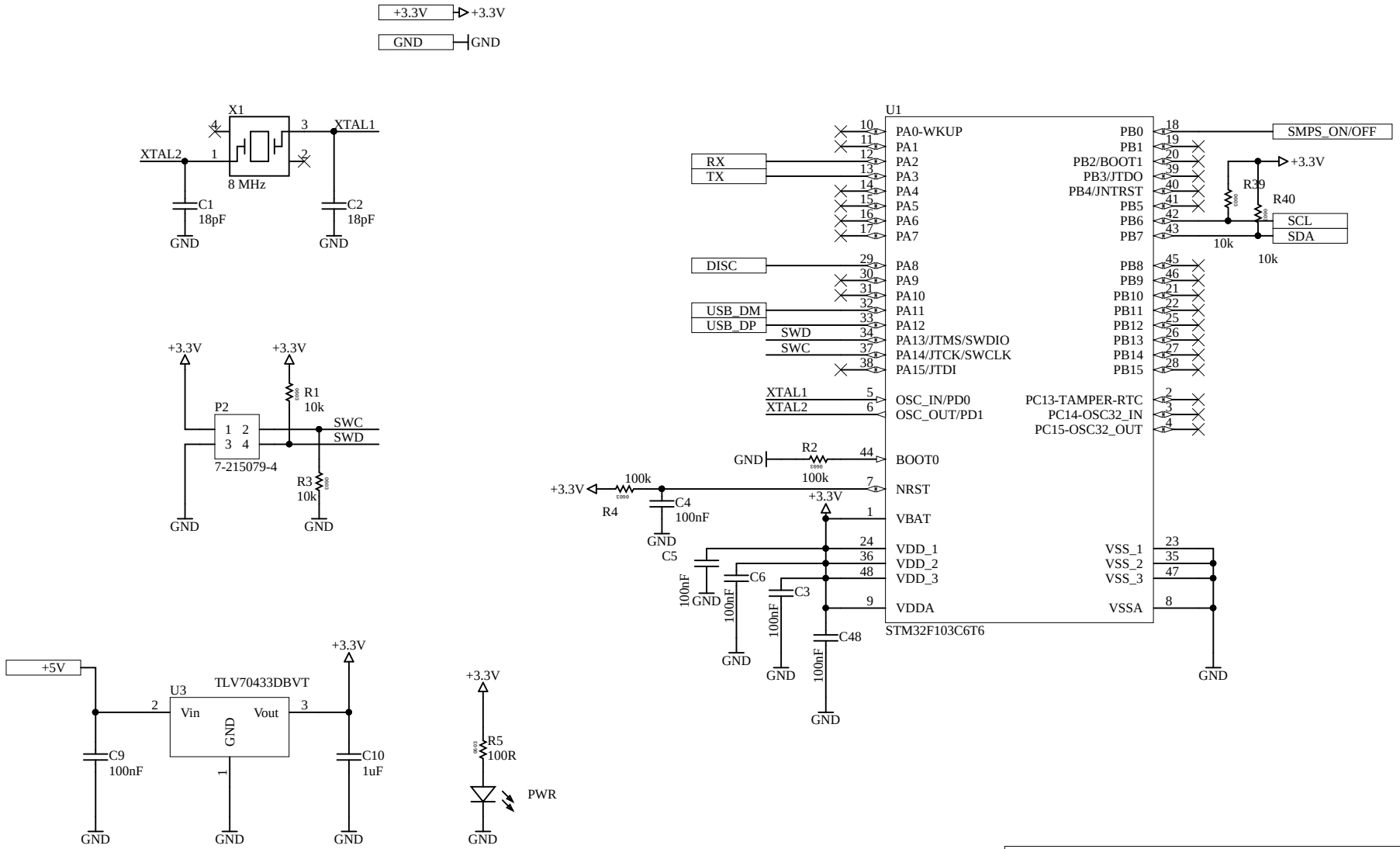
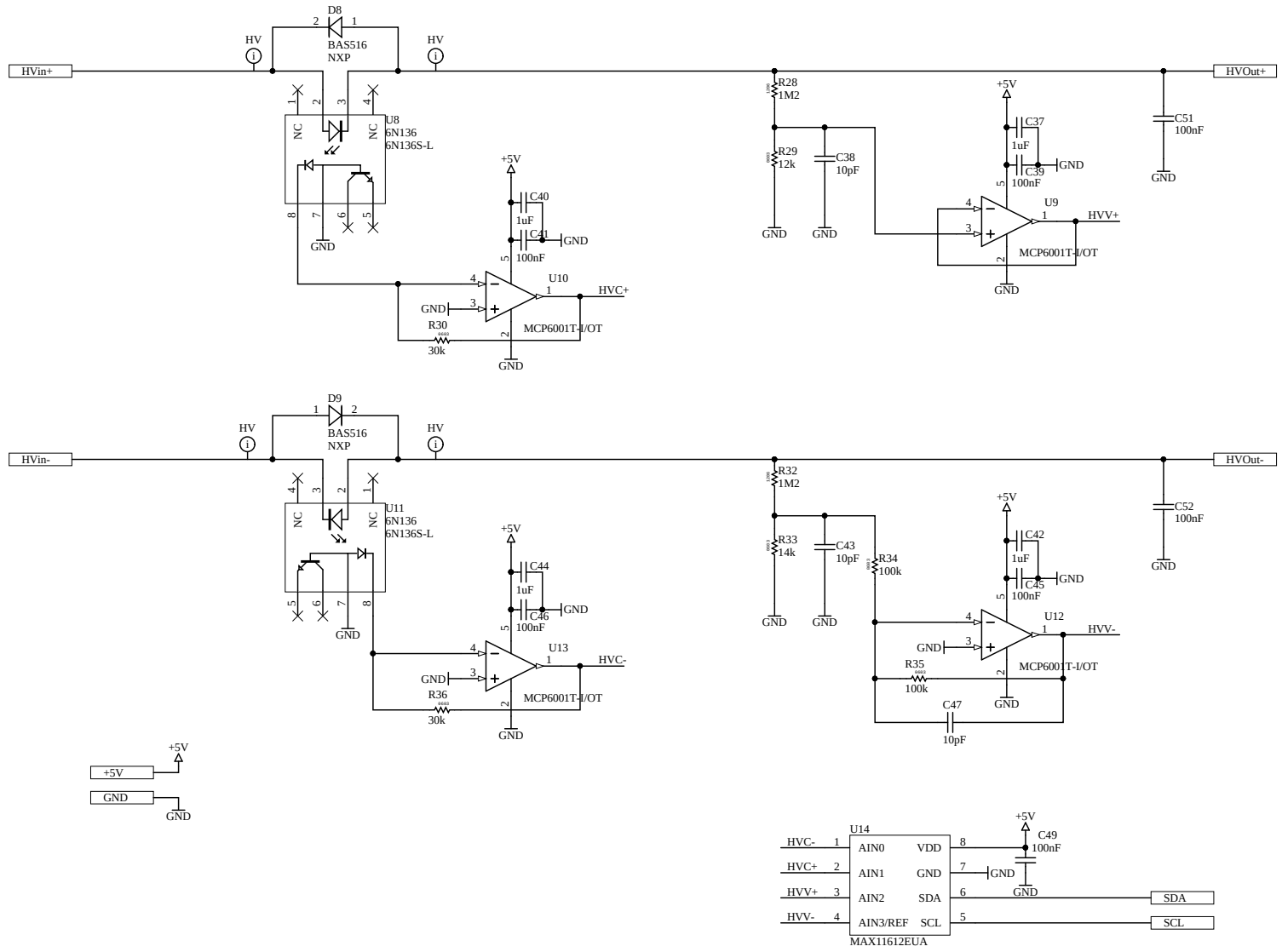
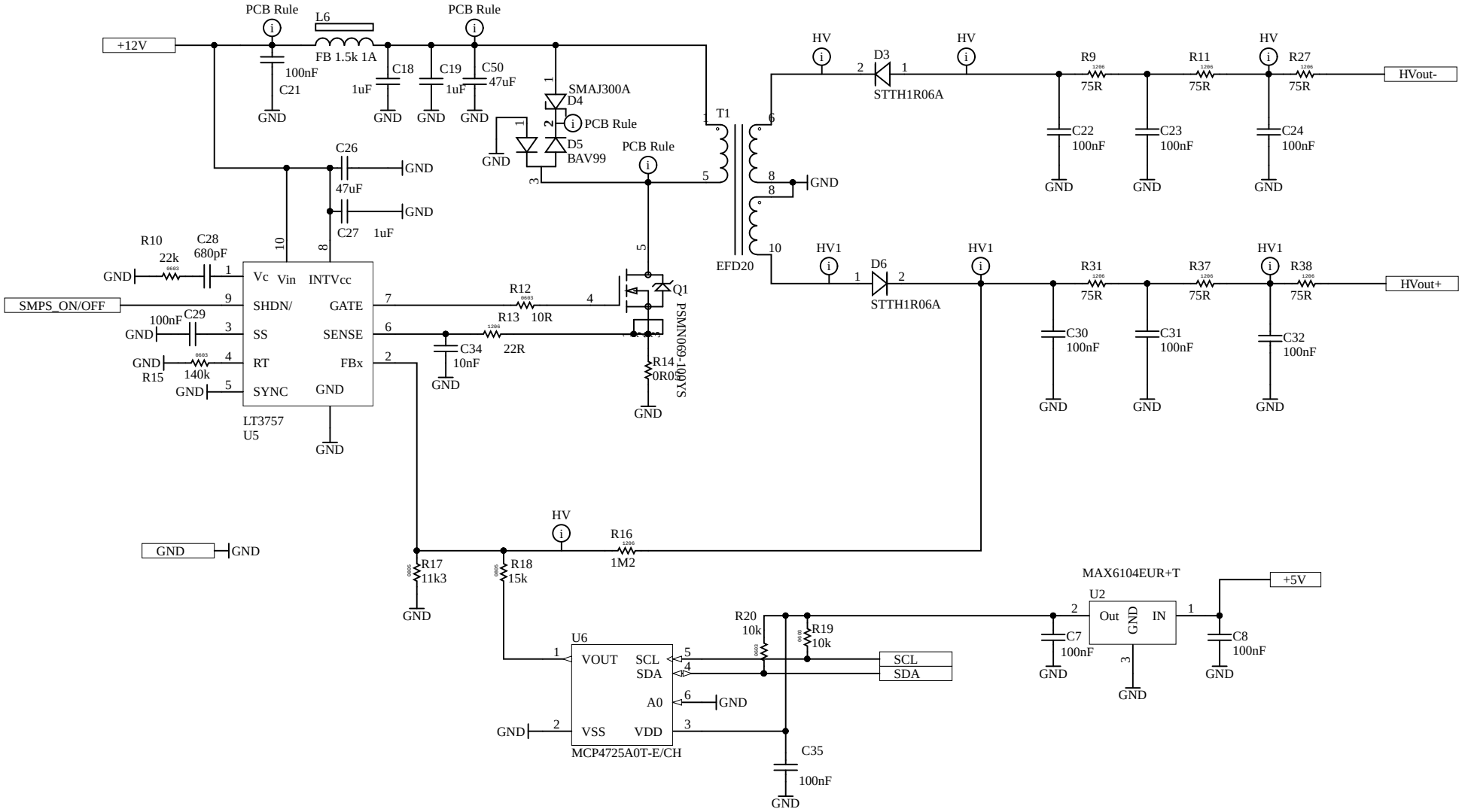


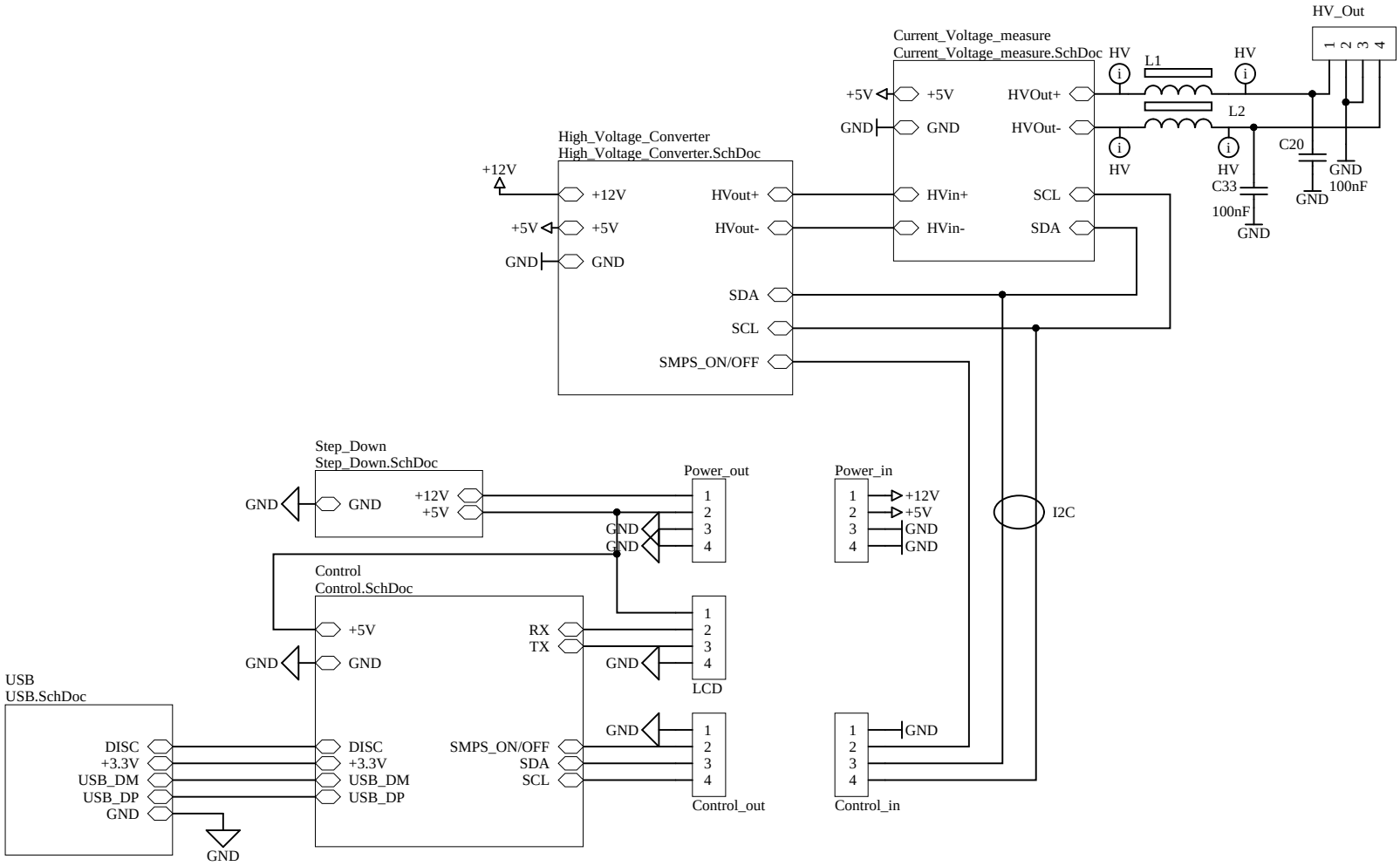
Title		
Size: A4	Number	Revision
Date: 2018-05-10		Sheet of
File: D:\Aurimo\..\Control.SchDoc		Drawn By:



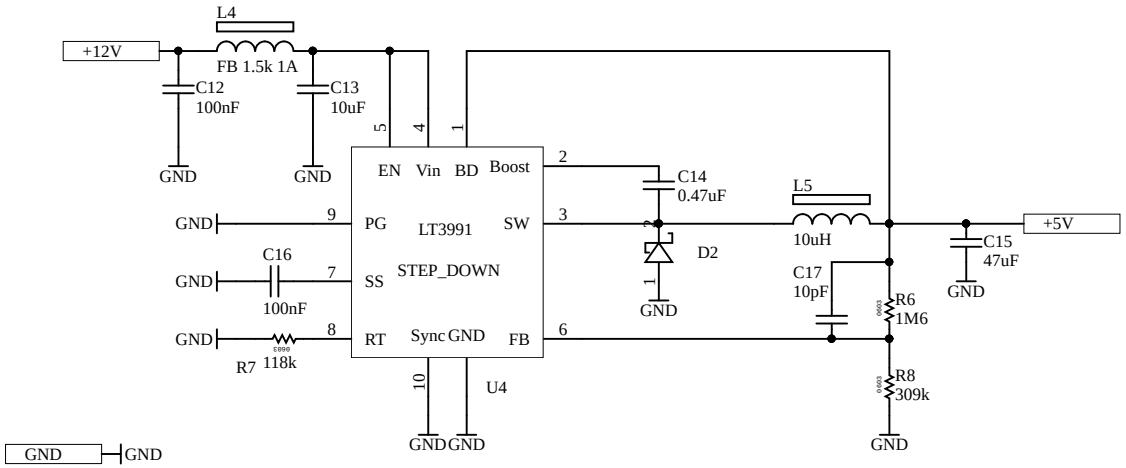
Title		
Size: A4	Number	Revision
Date: 2018-05-10	Sheet of	
File: D:\Aurimo\..\Current_Voltage_measure.SchDoc	Drawn By:	



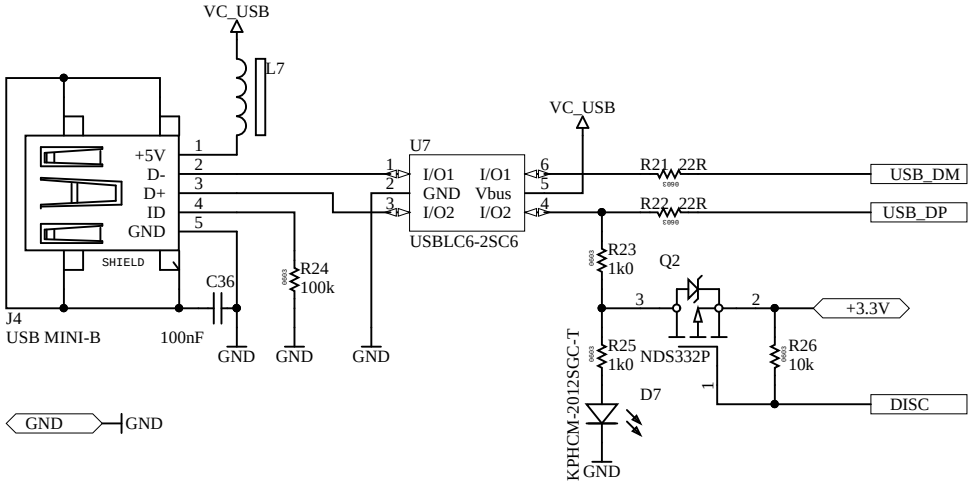
Title		
Size A4	Number	Revision
Date: 2018-05-10		Sheet of
File: D:\Aurimo\..\High_Voltage_Converter.SchDoc		Drawn By:



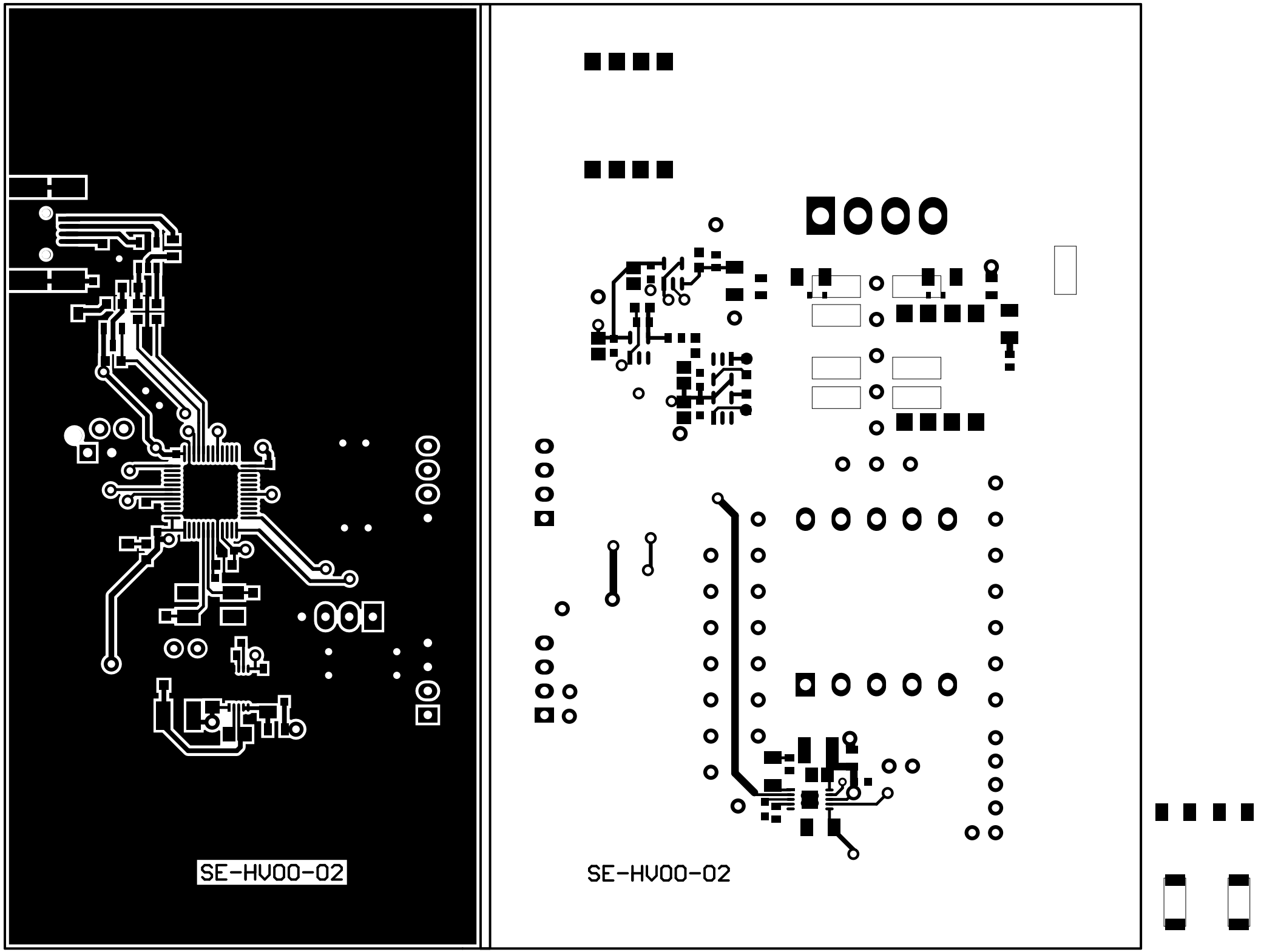
Title		
Size: A4	Number	Revision
Date: 2018-05-10		Sheet of
File: D:\Aurimo\..\SE-HV00-02.SchDoc		Drawn By:

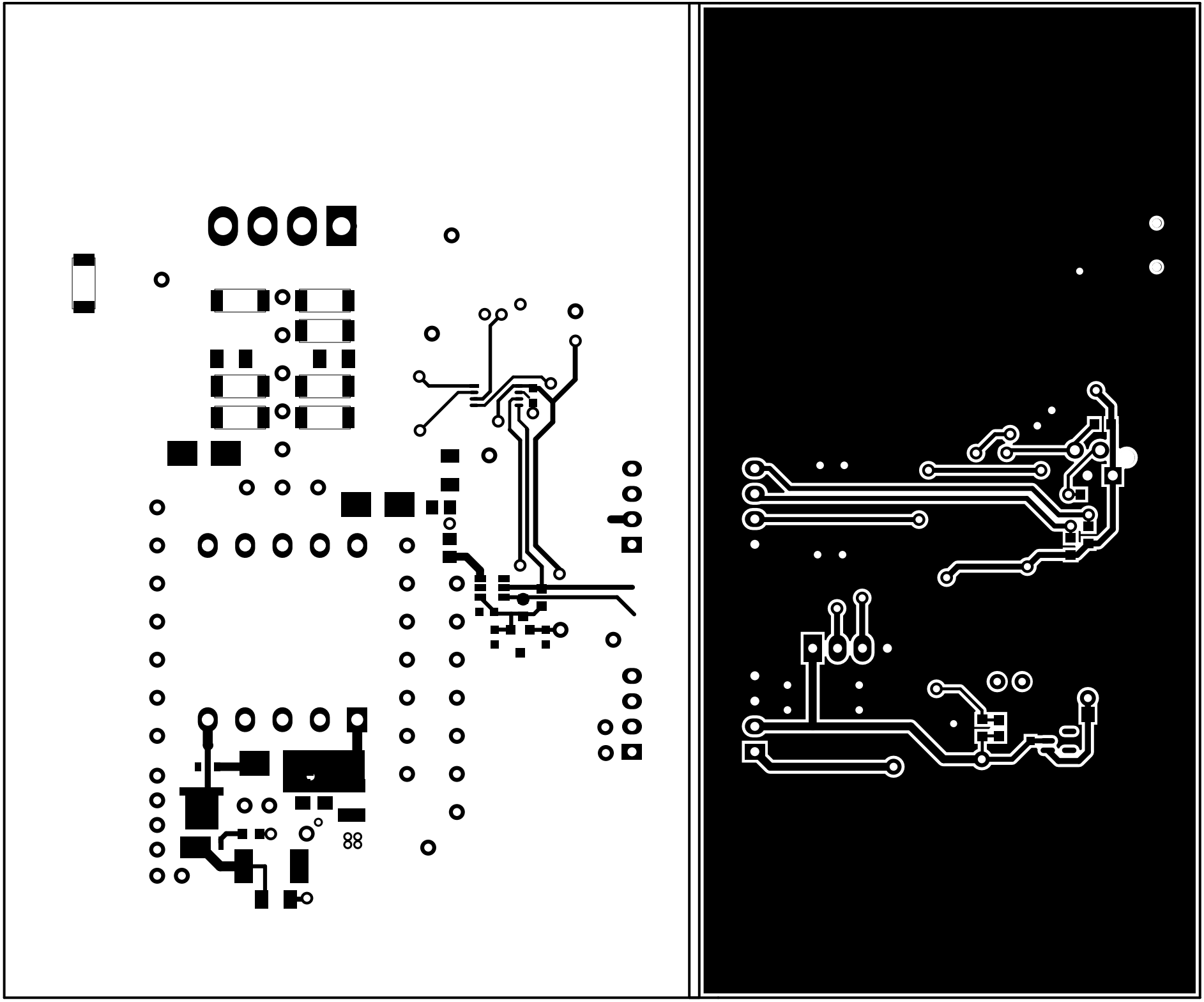


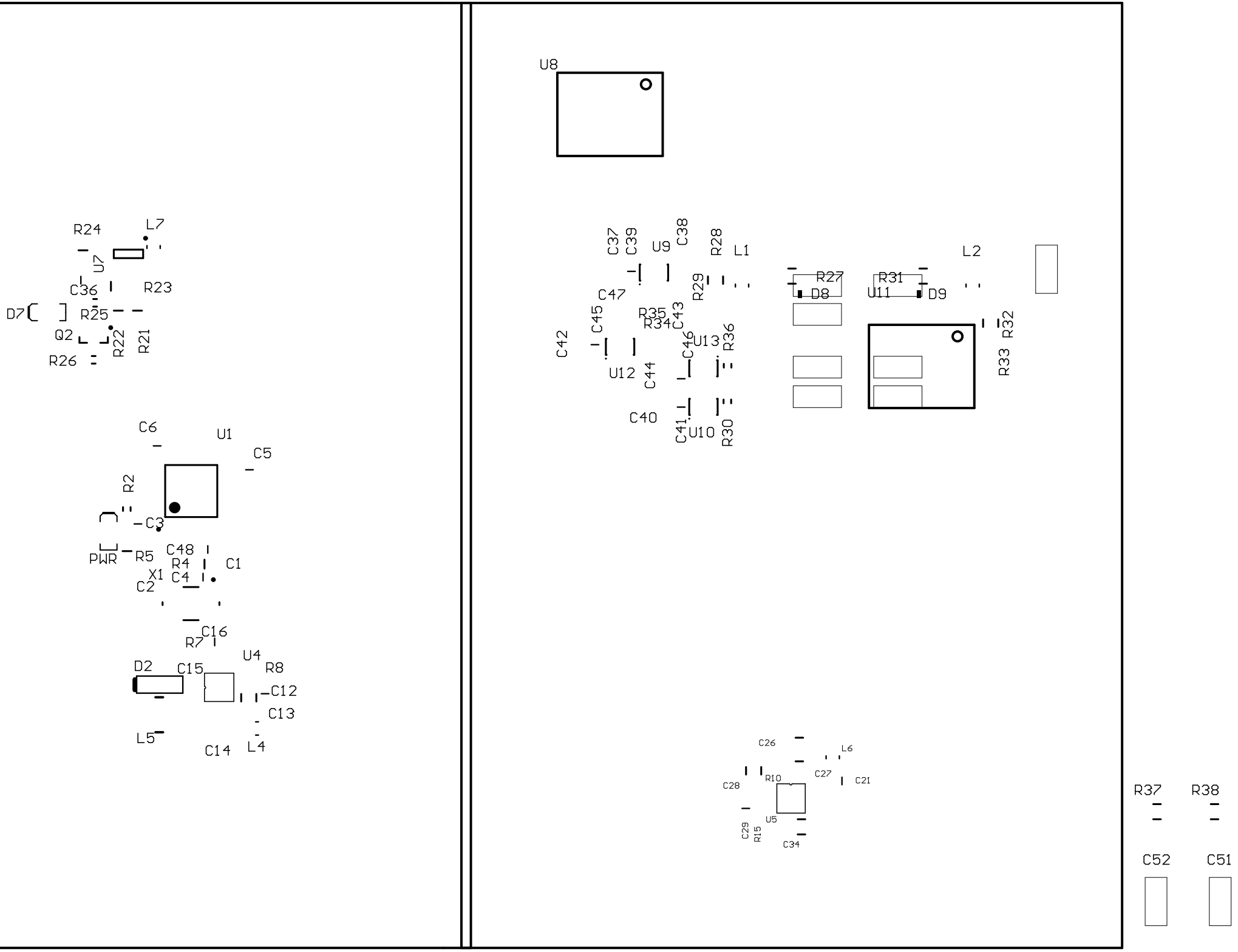
Title		
Size A4	Number	Revision
Date: 2018-05-10		Sheet of
File: D:\Aurimo\..\Step_Down.SchDoc		Drawn By:

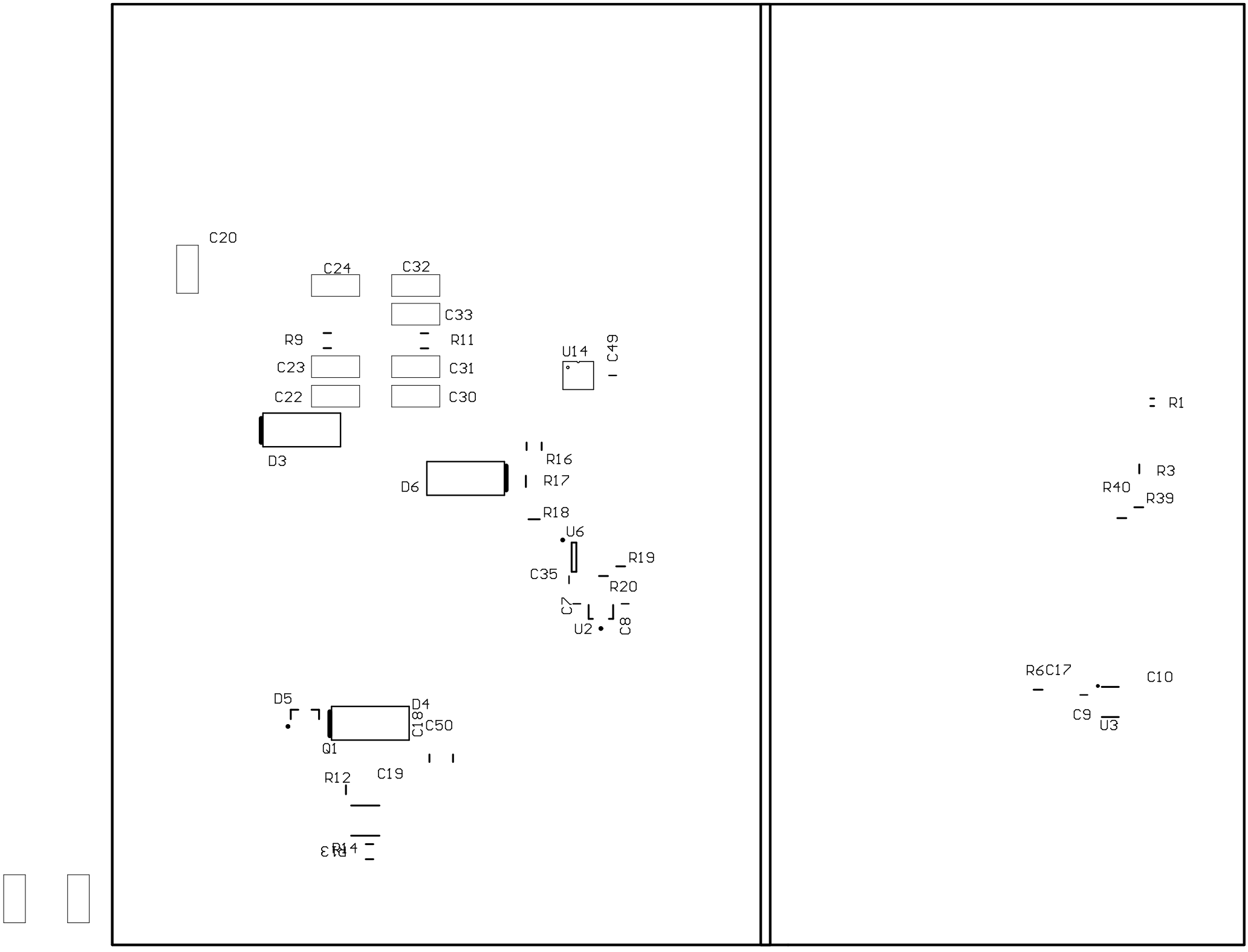


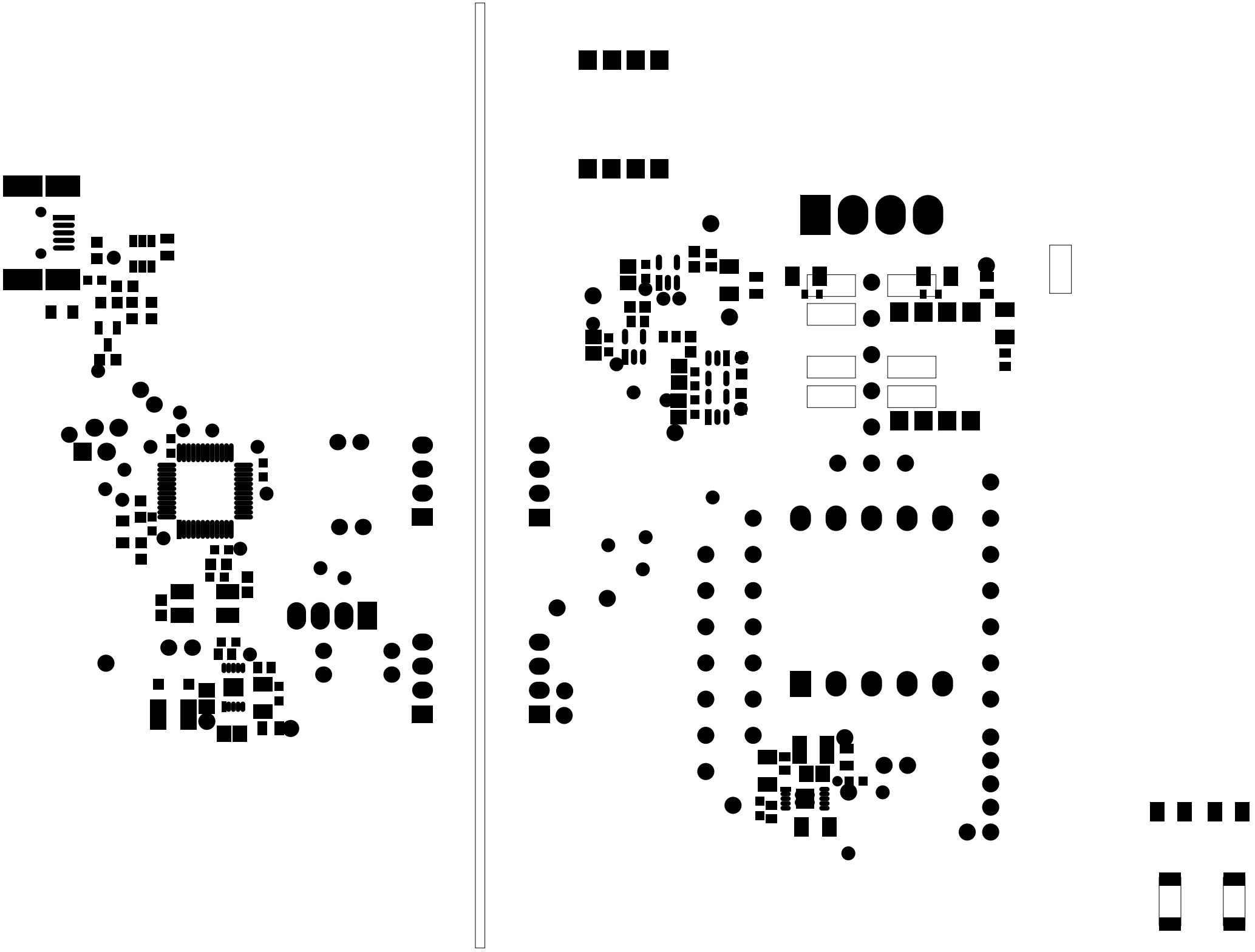
Title		
Size: A4	Number	Revision
Date: 2018-05-10		Sheet of
File: D:\Aurimo\..\USB.SchDoc		Drawn By:

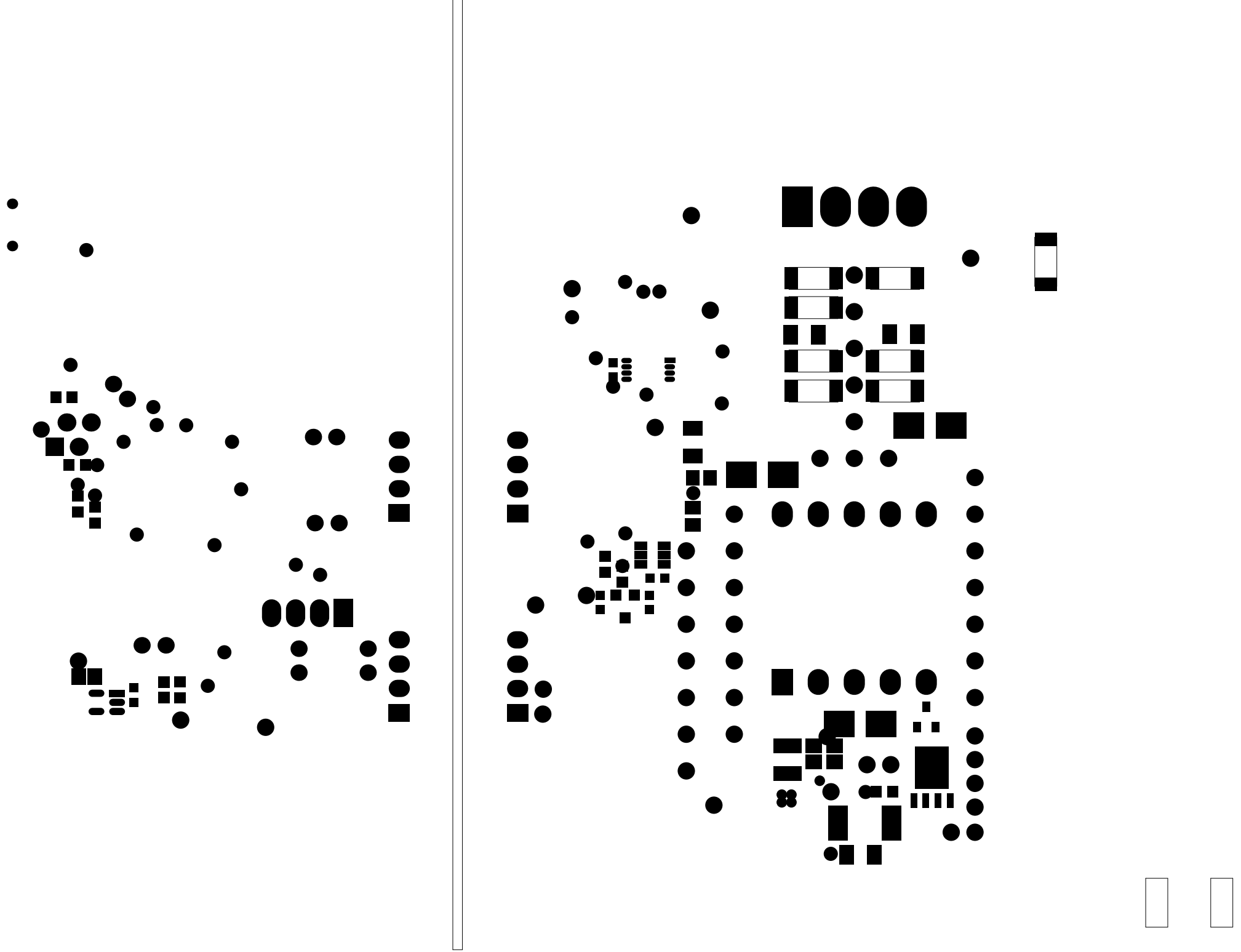

SE-HV00-02
SE-HV00-02

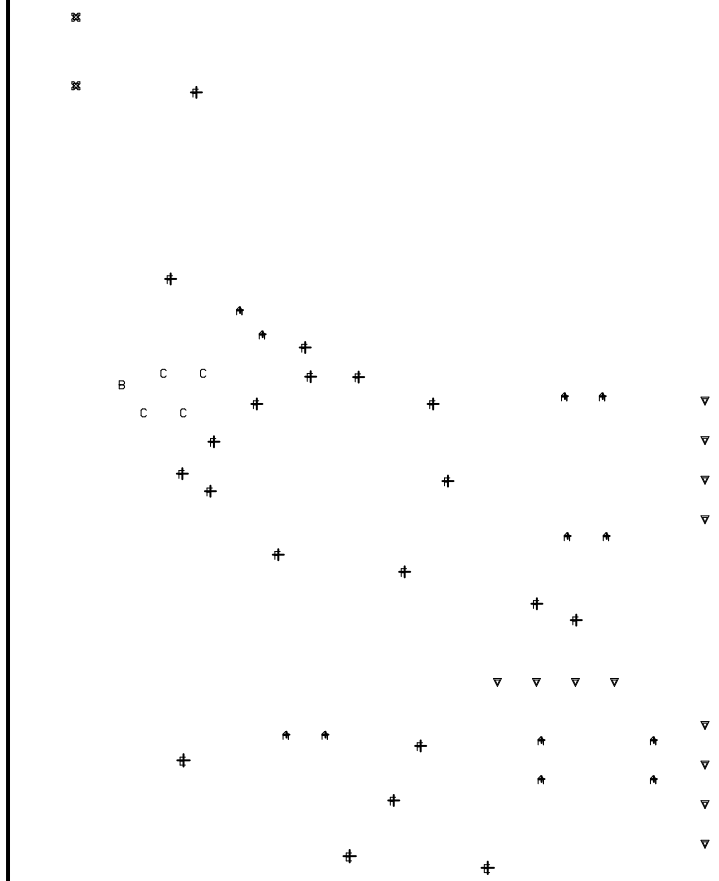


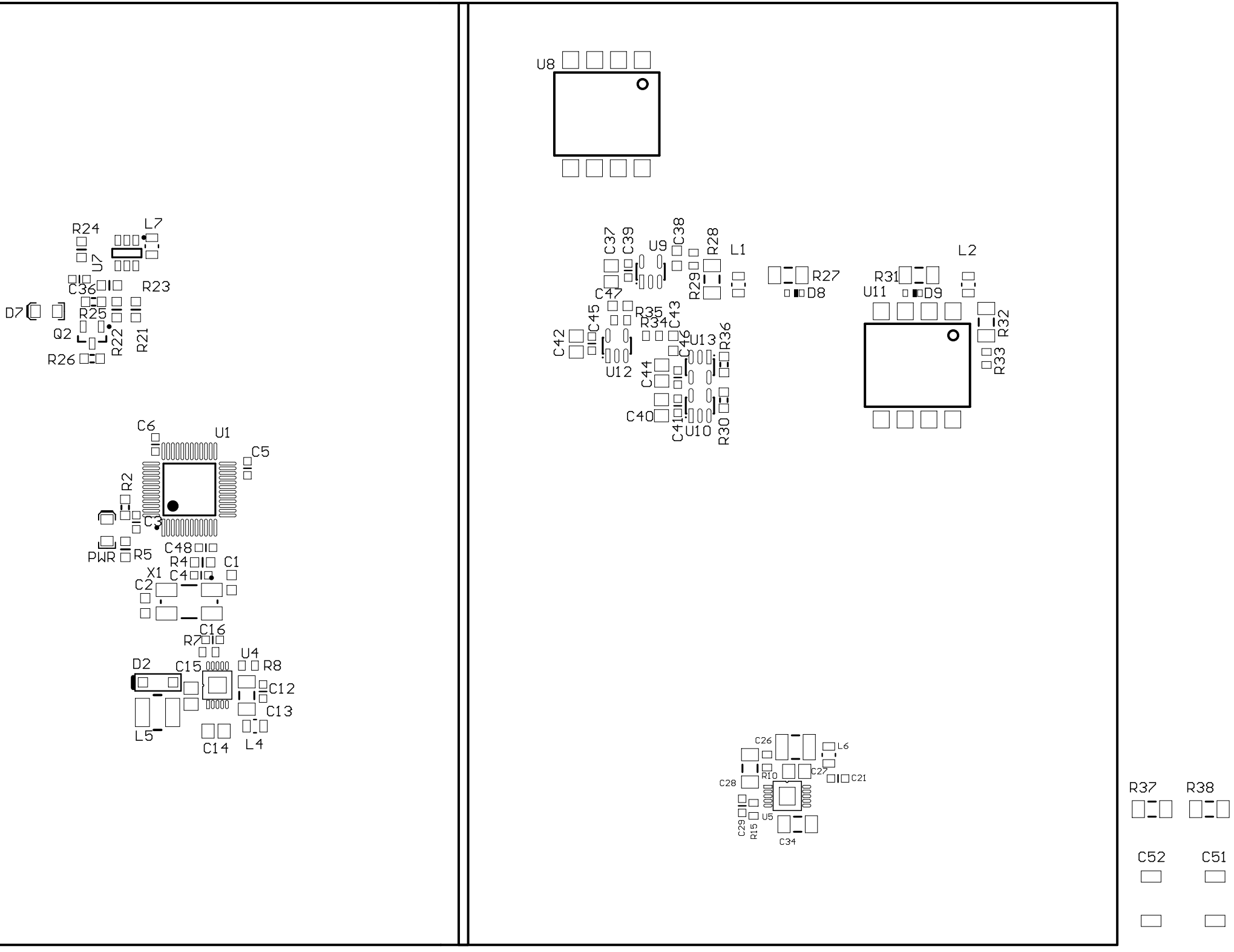

U8
R24
L7
U7
C36
R23
D7
R25
Q2
R22
R21
R26
C37
C39
U9
C38
R28
L1
R29
C47
C45
R35
R34
C43
C42
C46
U13
R36
U12
C44
C40
C41
U10
R30
R27
D8
R31
U11
D9
L2
R32
R33
C6
U1
C5
R2
C3
PWR
R5
C48
R4
C1
X1
C4
C2
C16
R7
U4
D2
C15
R8
C12
C13
L5
C14
L4
C26
L6
C27
C21
C28
R10
U5
C29
R15
C34
R37
R38
C52
C51

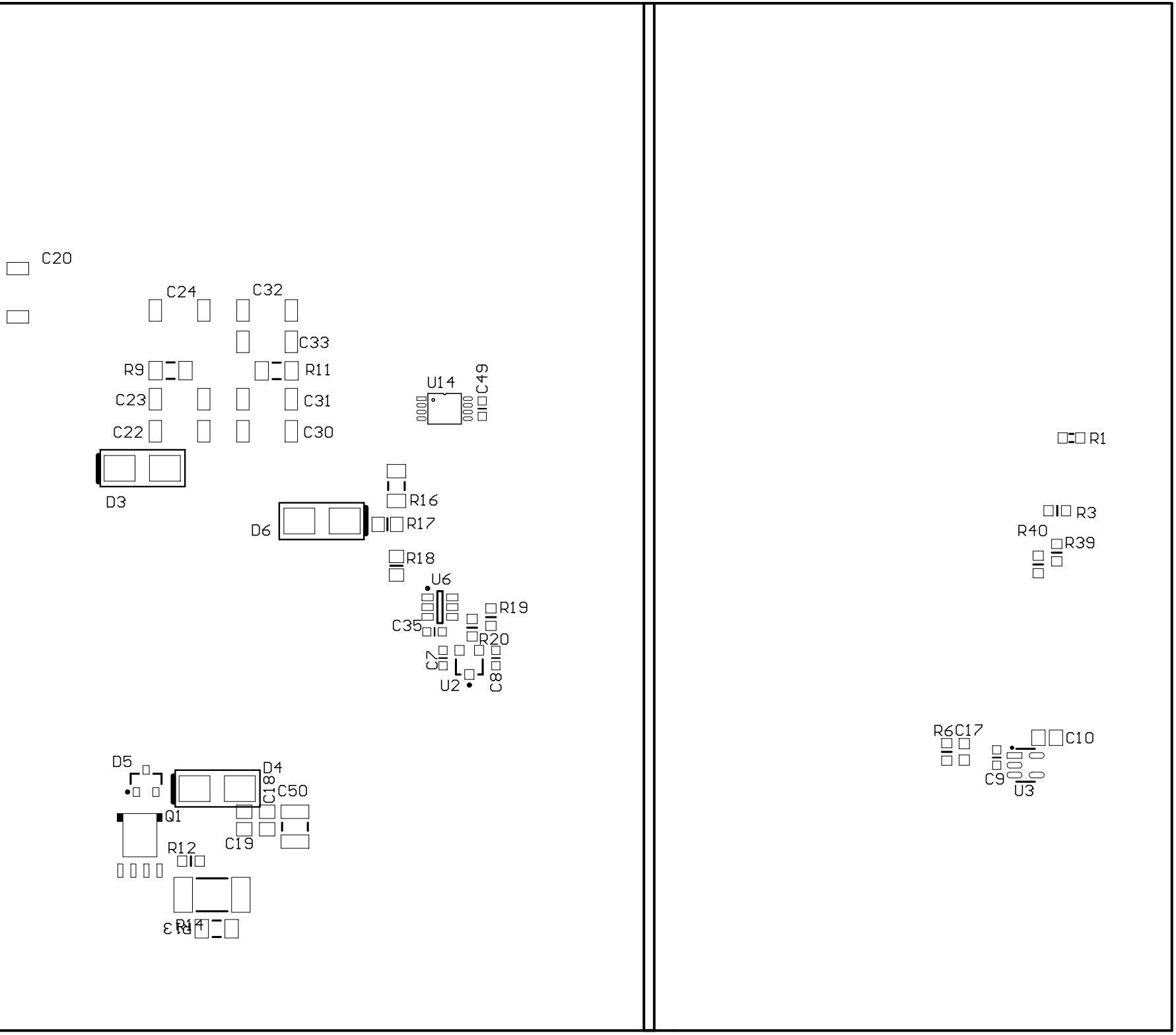

C20
C24
C32
C33
R9
R11
C23
C31
C22
C30
D3
U14
C49
R1
R16
D6
R17
R3
R40
R39
R18
U6
R19
C35
R20
C7
C8
U2
R6
C17
C10
C9
U3
D5
D4
C18
C50
Q1
C19
R12
R14






B
C
C
C
C


U8
R24
L7
U7
C36
R23
D7
R25
Q2
R22
R21
R26
C37
C39
U9
C38
R28
L1
R29
R27
D8
C47
C45
R35
R34
C43
C42
C46
U13
R36
U12
C44
C40
C41
U10
R30
R31
L2
U11
D9
R32
R33
C6
U1
C5
R2
C3
PWR
R5
C48
R4
C1
X1
C4
C2
C16
R7
U4
D2
C15
R8
C12
C13
L5
C14
L4
C26
L6
C27
C28
R10
C21
C29
R15
U5
C34
R37
R38
C52
C51


C20
C24
C32
C33
R9
R11
C23
C31
C22
C30
D3
U14
C49
R1
R16
D6
R17
R3
R40
R39
R18
U6
R19
C35
R20
C7
C8
U2
R6
C17
C10
C9
U3
D5
D4
C18
C50
Q1
C19
R12
R14

Quantity	Comment	Description	Manufacturer	Manufacturer Part Number	Supplier 1	Supplier 2	Note	Supplier Part Number 1	Supplier Part Number 2	Footprint	Designator
1	PMEG3010ER	1A, 30V	NXP	PMEG3010ER	SAMPLE					SOD123	D2
1	1M6	1M6 5% 0603 1/10W								RESC1608N	R6
2	6N136	6N136.SDM Optocoupler, Transistor Output, 1 Channel, Surface Mount DIP, 8 Pins, 50 mA, 5 kV, 19 %			TME			6N136S-5		SMP-8	U8, U11
1	MCP4725A0T-E/CH	12-Bit Digital-to-Analog Converter with EEPROM Memory in SOT-23-6, 6-Pin SOT-23, Extended Temperature, Tape and Reel			Digi-Key	Digi-Key		MCP4725A0T-E/CHTR-ND	MCP4725A0T-E/CHCT-ND	SOT-23-CH6_N	U6
1	22R	22R 5% 1206 1/4W								RESC3216N	R13
2	30k	30K 5% 0603 1/10W								RESC1608N	R30, R36
1	LT3991	55V, 1.2A Step-Down Regulator with 2.8µA Quiescent Current			Newark			24T2758		10-LEAD PLASTIC MSOP - duplicate	U4
6	75R	75R 5% 1206 1/4W								RESC3216N	R9, R11, R27, R31, R37, R38
2	FB 1.5k 1A	BEAD FERRITE, SMD 1500OHM, 0805	Wurth Elektronik	74279209 7	Farnell	Mouser		1635738	710-7427920 97	LL0805\2P	L4, L6
3		BEAD SMD EMI FERRITE 0805 220 OHM	Murata	BLM21PG 221SN1D						LL0805\2P	L1, L2, L7
1	LT3757	Boost, Flyback, SEPIC and Inverting Controller			Farnell			2294952		10-LEAD PLASTIC MSOP - duplicate	U5
1	HDR 4	CONNECTOR, 4 way , HEADER, THT, 3.96MM,10A,250Vac	JST	B4P-VH(LF)(SN)	Farnell	Digi-Key	S	9492020	455-1641-ND	HDR1X4_3M96V	HV_Out
1	8 MHz	CRYSTAL, 8M, 18PF CL, 6X3.6MM SMT	ABRACON	ABM2-8.000MHZ-E2-T	Farnell	Mouser	1	1611803	815-ABM2-8-E2T	2SMX-3SMXB	X1
2	STTH1R06A	DIODE, fast, 1A, 600V, SMD, SMA	ST Microelectronics	STTH1R06A	Farnell			1697520		DO-214AC	D3, D6
2	BAS516	DIODE, SWITCHING, SOD-523	NXP	BAS516	Farnell			8734402		SODS23	D8, D9
1	USBLC6-2SC6	DIODE ARRAY, TVS, USB2	STMICROELECTRONICS	USBLC6-2SC6	Farnell			1269406		SOT95P280X145-6N	U7
1	BAV99	DIODE SW DUAL 75V 350mW SOT23		BAV99-7-F	Farnell	Mouser	0,289 €	1843683	621-BAV99-F	SOT95P240-3L	D5
1	EFD20	EFD 20			Newark			56W066 7		EFD_20	T1
1	10uH	INDUCTOR, SMD, 4X4MM, 10UH, 20%	Murata	LQH44PN 100MP0L	Farnell			1782801		L1515	L5
1	TLV70433DBVT	LDO, 3.3V, 150MA, SOT23-5 Vin - 24V	Texas Instruments	TLV70433 DBVT	Farnell			2075417		SO-G5/P.95	U3
2	KPHCM-2012SGC-T	LED, SMD, 0805, GREEN, 25mcd	KINGBRIGHT	KPHCM-2012SGC-T	Farnell	Mouser	S	1686074	859-LTST-C171CKT	D0805	D7, PWR
1	MAX6104EUR+T	Low-Cost, Micropower, Low-Dropout, High-Output-Current, SOT23 Voltage References			Farnell			2511340		SOT23-3	U2
1	MAX11612EUA	MAXIM INTEGRATED PRODUCTS MAX11612EUA+ Analogue to Digital Converter, Low Power, 12 bit, 94.4 kSPS, Single, 4.5 V, 5.5 V, µMAX			Farnell			2513417		uMAX-8	U14
1	7-215079-4	Micro-match SOCKET, TOP ENTRY, 4WAY PTH, 1.27mm	TE Connectivity	7-215079-4	Farnell		S	148507		ZIP-T4	P2
4	MCP6001T-I/OT	MICROCHIP MCP6001T-I/OT Operational Amplifier, Single, 1 Amplifier, 1 MHz, 0.6 V/µs, 1.8V to 6V, SOT-23, 5 Pins			Farnell			4974992		SO-G5/X.45	U9, U10, U12, U13
19	100nF	MLCC, 0.1uF 16V 10% X7R 0603	KEMET	C0603C104K4RACTU	Farnell	Mouser	0,041 €	1414610 RL	80-C0603C104K4R	C0603V	C3, C4, C5, C6, C7, C8, C9, C12, C16, C21, C29, C35, C36, C39, C41, C45, C46, C48, C49
1	0.47uF	MLCC,0.47uF,50V, 0805,	MURATA	GRM219 R60J104K E19D	Farnell		S	1845738		CAPC2012N	C14
1	10nF	MLCC, 10nF, 630V, ±10% ,X7R, 1206	TDK	C3216X7 R2J103M		Mouser	0,182 €		810-C3216X7R2J103M	CAPC3216N	C34
1	10uF	MLCC, 10uF, 16V, 10%, 1206	MURATA	GRM31C R71C106 KAC7L	Farnell	Mouser	1,53 €	1845772	81-GRM31CR71C106KAC7	CAPC3216N	C13
2	18pF	MLCC, 18 pF, ± 5%, C0G / NP0, 50 V, 0603	MULTICOMP	MC0603 N180J50 0CT	Farnell			1759056		CAPC1608N	C1, C2
1	47uF	MLCC,47uF,50V, 0805,	MURATA	GRM219 R60J106K E19D	Farnell		S	1845738		CAPC2012N	C15
4	10pF	MLCC, 0603, NP0, 50V, 10PF	MULTICOMP	MCCA00 0192	Farnell	Mouser		1759053	77-VJ0603A100JXACBC	CAPC1608N	C17, C38, C43, C47
1	680pF	MLCC, 680PF, 630V, C0G/NP0, 1206	MULTICOMP	MC1206 N681J63 1CT	Farnell			1875399		CAPC3216N	C28
8	1uF	MLCC, 0805, X5R, 16V, 1UF	MULTICOMP	MCCA00 0596	Farnell	Mouser		1759479	77-VJ0805V105ZXJCBC	CAPC2012N	C10, C18, C19, C27, C37, C40, C42, C44
2	47uF	MLCC, 1210, 47uF, 16V	MURATA	GRM32E R61C476 ME15L	Farnell			1735538		CAPC3225N	C26, C50
10	100nF	MLCC, X7R, 100NF, 630V, 1808	KEMET	C1808V104KBRACTU	Farnell	Mouser			208S209	C1808	C20, C22, C23, C24, C30, C31, C32, C33, C51, C52
1	PSMN069-100YS	MOSFET 100V, 72.4 mOhm standard level, LFPAK	NXP	PSMN069-100YS	Farnell			1845662 RL		SOT669	Q1
1	NDS332P	P-Channel Logic Level Enhancement Mode Field Effect Transistor	FAIRCHILD SEMICONDUCTOR	NDS332P	Farnell	Mouser		1471069	512-NDS332P	SOT23-3L	Q2
1	0R05	Res, 0.05R 1% 2512 1W	WELWYN	LR2512-R05FW	Farnell		S	1100065		RESC6432AN	R14
3	1M2	Res, 1M 0.25W, 1% 1206	Bourns	CR1206-FX-1004ELF		Mouser	0,041 €		652-CR1206F X-1004ELF	RESC3216N	R16, R28, R32
7	10k	Res, 10K 0.063W 75V 1% 0603	MULTICOMP	MCMR06 X1002FTL	Farnell	Mouser	0,033 €	2073349	594-MCT060 30C1002FPS	RESC1608N	R1, R3, R19, R20, R26, R39, R40
1	11k3	Res, 11K 5% 0805 1/8W								RESC2012N	R17
1	15k	Res, 11K 5% 0805 1/8W								RESC2012N	R18
1	12k	Res, 12K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1202V	Farnell					RESC1608X50N	R29
1	14k	Res, 14K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1402V	Farnell					RESC1608X50N	R33
1	22k	Res, 22K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF2202V	Farnell					RESC1608X50N	R10
2	22R	Res, 22R 5% 0603 1/10W	MULTICOMP	MC0063 W060312 2R	Farnell	Mouser		9330844	660-RK73H1JTTD22R0F	RESC1608N	R21, R22
3	100k	Res, 100K 5% 0603 1/10W	YAGEO	RC0603FR-13100KL	Farnell	Mouser		2309107	603-RC0603FR-13100KL	RESC1608N	R2, R4, R24
2	100k	Res, 100K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1003V	Farnell					RESC1608X50N	R34, R35
1	100R	Res, 100R 5% 0603 1/10W	MULTICOMP	MC 0.063W 0603 5% 100R	Farnell			9331689 RL		RESC1608N	R5
1	118k	Res, 118K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1183V	Farnell					RESC1608X50N	R7
1	140k	Res, 140K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1403V	Farnell					RESC1608X50N	R15
1	309k	Res, 309K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF3093V	Farnell					RESC1608X50N	R8
1	10R	Res, 0603 0.1W 5%	PANASONIC	ERJ-3GEYJ100V	Farnell	Mouser		2059548	667-ERJ-3GEYJ100V	RESC1608N	R12
2	1k0	Res, thin film 1k ±5% 0.1W 0603	MULTICOMP	MCMR06 X102 JTL	Farnell	Mouser	0,083 €	2073354	71-TNPW06031K00DEEA	RESC1608N	R23, R25
2	HDR 4 Pin	Right angle haeder 2.54 mm single row								RA HDR 4Pin	Control_out , Power_out
2	4 Pin Socket	Right angle socket conn 2.54mm single row								RA Socket 4 pin	Control_in, Power_in
1	STM32F103C6T6	STM32 ARM-based 32-bit MCU with 32 Kbytes Flash, 48-pin LQFP, Industrial Temperature								LQFP48_N	U1
1	SMAJ300A	TVS DIODE, 400W, 300V, DO-214AC	LITTELFUSE	SMAJ300A	Farnell	Mouser		1677461	576-SMAJ300A	DO-214AC	D4
1	USB MINI-B	USB MINI-B SMT connector	Molex	67503-1020	Farnell	Mouser		1125348	538-67503-1020	MOLEX_MINI_USB-B	J4
1	B4B-XH-A	Wire to Board Header, THT, 2.5MM, 4WAY	JST	B4B-XH-A(LF)(SN)	Farnell	Digikey	S	1516278	455-2249-ND	B4B-XH-A	LCD

Quantity	LogicalDesignator	Comment	Description	Manufacturer	Manufacturer Part Number	Supplier 1	Supplier 2	Note	Supplier Part Number 1	Supplier Part Number 2	Footprint
19	C3, C4, C5, C6, C7, C8, C9, C12, C16, C21, C29, C35, C36, C39, C41, C45, C46, C48, C49	100nF	MLCC, 0.1uF 16V 10% X7R 0603	KEMET	C0603C104K4RACTU	Farnell	Mouser	0.041 €	1414610RL	80-C0603C104K4R	C0603V
10	C52, C50, C22, C23, C24, C30, C31, C32, C33, C51	100nF	MLCC, X7R, 100NF, 630V, 1808	KEMET	C1808V104KRACTU	Farnell	Mouser			20852-09	C1808
8	C10, C18, C19, C27, C37, C40, C42, C44	1uF	MLCC, 0805, X5R, 16V, 1UF	MULTICOMP	MCCA000586	Farnell	Mouser		1759479	77-VJ0805Y105JXJCC	CAPC2012N
7	R1, R3, R19, R20, R26, R39, R40	10k	Res, 10K, 0.063W 75V 1% 0603	MULTICOMP	MCMR06X1002FTL	Farnell	Mouser	0.033 €	2073349	594-MCT0603C10022P5	RESC1608N
6	R9, R11, R27, R31, R37, R38	75R	75R 5% 1206 1/4W								RESC3216N
4	C17, C38, C43, C47	10pF	MLCC, 0603, NP0, 50V, 10PF	MULTICOMP	MCCA001924	Farnell	Mouser		1759053	77-VJ0603A100JXACBC	CAPC1608N
4	U9, U10, U12, U13	MCP6001T-I/OT	MICROCHIP MCP6001T-I/OT Operational Amplifier, Single, 1 Amplifier, 1 MHz, 0.6 V/µs, 1.8V to 6V, SOT-23, 5 Pins			Farnell			4974992		SO-G5X.45
3	L1, L2, L7		BEAD SMD 1NR RR0HT6 0805 220 OHM	Murata	BLM21PG221SN1D						L0805I2P
3	R16, R28, R32	1M2	Res, 1M 0.25W, 1% 1206	Bourns	CR1206-FX-1004ELF		Mouser	0.041 €		652-CR1206-FX-1004ELF	RESC3216N
3	R2, R4, R24	100k	Res, 100K 5% 0603 1/10W	YAGEO	RC0603JR-07100KL	Farnell	Mouser		2309107	603-RC0603JR-07100KL	RESC1608N
2	C1, C2	18pF	MLCC, 18 pF, ± 5%, C0G / NP0, 50 V, 0603	MULTICOMP	MCD03-SN180J500CT	Farnell			1759056		CAPC1608N
2	C14, C15	0.47uF, 47nF	MLCC,0.47uF-50V, ,0805, MLCC,47uF-50V, 0805,	MURATA	GRM21BR60J474ME19L	Farnell		S	1845738	710-C2012X7R1H474K125AB	CAPC2012N
2	C26, C50	47uF	MLCC, 1210, 47uF, 16V	MURATA	GRM32ER61C476ME15L	Farnell			1736538		CAPC3225N
2	Control_in, Power_in	4 Pin Socket	Right angle socket conn 2.54mm single row								PA Socket 4 pin
2	Control_out, Power_out	HDR 4 Pin	Right angle header 2.54 mm single row								RA HDR 4Pin
2	D3, D6	STTH1R06A	DIODE, fast, 1A, 600V, SMD, SMA	ST Microelectronics	STTH1R06A	Farnell			1697520		DO-214AC
2	D7, PWR	KPHCM-2012SGC-3	LED, SMD, 0805, GREEN, 25mcd	KINGBRIGHT	KPHCM-2012SGC-T	Farnell	Mouser	S	1669074	859-LTST-C171GKT	20805
2	D8, D9	BAS516	DIODE, SWITCHING, SOD-523	NXP	BAS516,115	Farnell			8734402		SOD523
2	L4, L6	FB 1.5k 1A	BEAD FERRITE, SMD 1500OHM, 0805	Würth Elektronik	74279219?7	Farnell	Mouser		1635738	710-74279219?7	L0805I2P
2	R17, R18	11k3, 15k	Res, 11K 5% 0805 1/8W								RESC2012N
2	R21, R22	22R	Res, 22R 5% 0603 1/10W	MULTICOMP	MC0063W06032122R	Farnell	Mouser		9330844	660-RK73H1JTTD22R0F	RESC1608N
2	R23, R25	1k0	Res, thin film 1k ±5% 0.1W 0603	MULTICOMP	MCMR06X1002FTL	Farnell	Mouser	0.083 €	2073354	71-TNPW060310K0DEA	RESC1608N
2	R30, R36	30k	30K 5% 0603 1/10W								RESC1608N
2	R34, R35	100k	Res, 100K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1003V	Farnell					RESC1608X50N
2	U8, U11	6N136	6N136.SDM Optocoupler, Transistor Output, 1 Channel, Surface Mount DIP, 8 Pins, 50 mA, 5 kV, 19 %			TME			6N136-S-1		SMP-8
1	C13	10uF	MLCC, 10uF, 16V 10%, 1206	MURATA	GRM31CR71C106KACTL	Farnell	Mouser	1.53 €	1845772	81-GRM31CR71C106KAC7	CAPC3216N
1	C28	680pF	MLCC, 680PF, 630V, C0G/NP0, 1206	MULTICOMP	MC1206NR681J631CT	Farnell			1675389		CAPC3216N
1	C34	10nF	MLCC, 10nF, 630V, ±10%, X7R, 1206	TDK	C3216X7R2J103M		Mouser	0.182 €		810-C3216X7R2J103M	CAPC3216N
1	D2	PMEG3010EB	1A, 30V	NXP	PMEG3010ER	SAMTEC					SOD123
1	D4	SMA300A	TVS DIODE, 400W, 300V, DO-214AC	LITTELFUSE	SMA300A	Farnell	Mouser		1677491	576-SMA300A	DO-214AC
1	D5	BAV99	DIODE SW DUAL 75V 350mW SOT23		BAV99-7-F	Farnell	Mouser	0.289 €	1843693	621-BAV99-F	SOT95P240-3L
1	HV_Out	HDR 4	CONNECTOR, 4 way , HEADER, THT, 3.96MM,10A,250 Vac	JST	B4P-VH(LF)(SN)	Farnell	Digi-Key	S	9492028	455-1641-ND	HDR1X4_3M96V
1	J4	USB MINI-B	USB MINI-B SMT connector	Molex	675033020	Farnell	Mouser		1125349	538-67503-1020	MOLEX_MINI_USB-B
1	L5	10uH	INDUCTOR, SMD, 4X4MM, 10UH, 20%	Murata	LQH44PN100MR0L	Farnell			1782831		L1515
1	LCD	B4B-XH-A	Wire to Board Header, THT, 2.5MM, 4WAY	JST	B4B-XH-A(LF)(SN)	Farnell	Digikey	S	1516278	455-2249-ND	B4B-XH-A
1	P2	7-215079-4	Micro-match SOCKET, TOP ENTRY, 4WAY PTH, 1.27mm	TE Connectivity	7-215079-4	Farnell		S	1485027		ZIP-T4
1	Q1	PSMN065-100YS	MOSFET 100V, 72.4 mOhm standard level, LFPAK	NXP	PSMN065-100YS	Farnell			1845612		SOT669
1	Q2	NDS332P	P-Channel Logic Level Enhancement Mode Field Effect Transistor	FAIRCHILD SEMICONDUCTOR	NDS332P	Farnell	Mouser		1471049	512-NDS332P	SOT23-3L
1	R10	22k	Res, 22K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF2202V	Farnell					RESC1608X50N
1	R12	10R	Res, 0603 0.1W 5%	PANASONIC	ERJ-3GEY0100V	Farnell	Mouser		2059548	667-ERJ-3GEY0100V	RESC1608N
1	R13	22R	22R 5% 1206 1/4W								RESC3216N
1	R14	5R05	Res, 0.05R 1% 2512 1W	WELWYN	LR2512R05FW	Farnell		S	1100065		RESC6432AN
1	R15	140k	Res, 140K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1403V	Farnell					RESC1608X50N
1	R29	12k	Res, 12K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1202V	Farnell					RESC1608X50N
1	R33	14k	Res, 14K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1402V	Farnell					RESC1608X50N
1	R5	100R	Res, 100R 5% 0603 1/10W	MULTICOMP	MC0.063W 0603 5% 100R	Farnell			9331689RL		RESC1608N
1	R6	1M6	1M6 5% 0603 1/10W								RESC1608N
1	R7	118k	Res, 118K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF1183V	Farnell					RESC1608X50N
1	R8	309k	Res, 309K, 0603 (1608 metric), ±1%, 100mW, 75V	PANASONIC	ERJ3EKF3093V	Farnell					RESC1608X50N
1	T1	EFD20	EFD 20			Newark			06W6867		EFD_20
1	U1	STM32F103C8T6	STM32 ARM-based 32-bit MCU with 32 Kbytes Flash, 48-pin LQFP, Industrial Temperature								LQFP48_N
1	U14	MAX11612EUA	MAXIM INTEGRATED PRODUCTS MAX11612EUA+ Analogue to Digital Converter, Low Power, 12 bit, 94.4 kSPS, Single, 4.5 V, 5.5 V, µMAX			Farnell			2513417		uMAX-8
1	U2	MAX6104UR+T	Low-Cost, Micropower, Low-Dropout, High-Output-Current, SOT23 Voltage References			Farnell			2511340		SOT23-3
1	U3	TLV70433DBVT	LDO, 3.3V, 150MA, SOT23-5 Vin - 24V	Texas Instruments	TLV70433DBVT	Farnell			2075417		SO-G5P.95
1	U4	LT3991	55V, 1.2A Step-Down Regulator with 2.8µA Quiescent Current			Newark			24T2758		10-LEAD PLASTIC MSOP - duplicate
1	U5	LT3757	Boost, Flyback, SEPIC and Inverting Controller			Farnell			2294952		10-LEAD PLASTIC MSOP - duplicate
1	U6	MCP4725A0T-E/CH	12-Bit Digital-to-Analog Converter with EEPROM Memory in SOT-23-6, 6-Pin SOT-23, Extended Temperature, Tape and Reel			Digi-Key	Digi-Key		MCP4725A0T-E/CHCH-ND	MCP4725A0T-E/CHCT-ND	SOT-23-CH6_N
1	U7	USBLC6-2SC6	DIODE ARRAY, TVS, USB2	STMICROELECTRONICS	USBLC6-2SC6	Farnell			1269496		SOT95P280X145-6N
1	Y1	8 MHz	CRYSTAL, 8M, 18PF CL, 6X3.6MM SMT	ABRACON	ABM3-8.000MHZ-E2-T	Farnell	Mouser	1	1611803	815-ABM3-8-E2T	2SMX-3SMXB

Design Rules Verification Report

Filename : D:\Aurimo\altium\SE_HV_Power_supply\SE-HV00-02.PcbDoc

Warnings 4
Rule Violations 500

Warnings	
Design contains shelved or modified (but not repoured) polygons. The result of DRC is not correct. Recommended to	4
Total	4

Rule Violations	
Clearance Constraint (Gap=0.2mm) (All),(All)	45
Clearance Constraint (Gap=0.1mm) (All),(InNetClass('HV'))	0
Clearance Constraint (Gap=0.1mm) (All),(InNetClass('HV1'))	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	107
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.1mm) (Max=3mm) (Preferred=0.254mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=3mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	70
Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)	192
Silk to Silk (Clearance=0.254mm) (All),(All)	32
Net Antennae (Tolerance=0mm) (All)	54
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Silk primitive without silk layer	0
Total	500

Design contains shelved or modified (but not repoured) polygons. The result of DRC is not correct. Recommende
Polygon named: Top Layer-No Net In net GND On Top Layer
Polygon named: Top Layer-No Net In net GND On Bottom Layer
Polygon named: Top Layer-No Net In net GND On Top Layer
Polygon named: Top Layer-No Net In net GND On Bottom Layer

Clearance Constraint (Gap=0.2mm) (All),(All)
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.4mm,59.775mm)(75.4mm,62.3mm) on Top Layer And Pad U4-1(74.9mm,62.3mm) on Top
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U4-2(75.4mm,62.3mm) on Top Layer And Pad U4-1(74.9mm,62.3mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U4-3(75.9mm,62.3mm) on Top Layer And Pad U4-2(75.4mm,62.3mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,58.875mm)(75.9mm,62.3mm) on Top Layer And Pad U4-2(75.4mm,62.3mm) on Top
Clearance Constraint: (0.198mm < 0.2mm) Between Track (73.125mm,62.3mm)(74.9mm,62.3mm) on Top Layer And Pad U4-2(75.4mm,62.3mm) on Top
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U4-4(76.4mm,62.3mm) on Top Layer And Pad U4-3(75.9mm,62.3mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.4mm,59.775mm)(75.4mm,62.3mm) on Top Layer And Pad U4-3(75.9mm,62.3mm) on Top
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,58.875mm)(75.9mm,62.3mm) on Top Layer And Pad U4-4(76.4mm,62.3mm) on Top
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U4-8(75.9mm,66.4mm) on Top Layer And Pad U4-9(75.4mm,66.4mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,66.4mm)(75.9mm,67.665mm) on Top Layer And Pad U4-9(75.4mm,66.4mm) on Top
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U4-7(76.4mm,66.4mm) on Top Layer And Pad U4-8(75.9mm,66.4mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (76.4mm,66.4mm)(76.4mm,69.125mm) on Top Layer And Pad U4-8(75.9mm,66.4mm) on Top
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U4-6(76.9mm,66.4mm) on Top Layer And Pad U4-7(76.4mm,66.4mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,66.4mm)(75.9mm,67.665mm) on Top Layer And Pad U4-7(76.4mm,66.4mm) on Top
Clearance Constraint: (0.198mm < 0.2mm) Between Track (76.9mm,66.4mm)(78.46mm,66.4mm) on Top Layer And Pad U4-7(76.4mm,66.4mm) on Top
Clearance Constraint: (0.198mm < 0.2mm) Between Track (76.4mm,66.4mm)(76.4mm,69.125mm) on Top Layer And Pad U4-6(76.9mm,66.4mm) on Top
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.175mm,89.15mm)(71.179mm,89.154mm) on Top Layer And Pad
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.179mm,89.154mm)(71.179mm,91.221mm) on Top Layer And Pad
Clearance Constraint: (0.124mm < 0.2mm) Between Track (74.175mm,80.2mm)(75.438mm,78.938mm) on Top Layer And Pad
Clearance Constraint: (0.198mm < 0.2mm) Between Pad U5-4(134.2mm,52.075mm) on Top Layer And Pad U5-3(134.2mm,52.575mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Track (132.91mm,52.075mm)(134.2mm,52.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-5(134.2mm,51.575mm) on Top Layer And Pad U5-4(134.2mm,52.075mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Track (132.91mm,52.075mm)(134.2mm,52.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Track (138.3mm,53.075mm)(138.975mm,53.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-9(138.3mm,53.075mm) on Top Layer And Pad U5-10(138.3mm,53.575mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-8(138.3mm,52.575mm) on Top Layer And Pad U5-9(138.3mm,53.075mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,53.575mm)(138.3mm,55.175mm) on Top Layer And Pad
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-7(138.3mm,52.075mm) on Top Layer And Pad U5-8(138.3mm,52.575mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Track (138.3mm,53.075mm)(138.975mm,53.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Track (138.3mm,52.075mm)(143.275mm,52.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-6(138.3mm,51.575mm) on Top Layer And Pad U5-7(138.3mm,52.075mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Track (138.3mm,52.075mm)(143.275mm,52.075mm) on Top Layer And Pad
Clearance Constraint: (0.12mm < 0.2mm) Between Track (59.95mm,89.255mm)(59.95mm,94.4mm) on Bottom Layer And Pad
Clearance Constraint: (0.175mm < 0.2mm) Between Track (109.695mm,77.025mm)(121.05mm,77.025mm) on Bottom Layer And Pad
Clearance Constraint: (0mm < 0.2mm) Between Area Fill (153.025mm,57.1mm) (153.58mm,57.9mm) on Bottom Layer And Pad
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.675mm,89.15mm)(71.675mm,92.4mm) on Top Layer And Track
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.675mm,89.15mm)(71.675mm,92.4mm) on Top Layer And Track
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.675mm,89.15mm)(71.675mm,92.4mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,53.075mm)(138.975mm,53.075mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (140.15mm,52.575mm)(140.85mm,53.275mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Track

Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net +12V Between Pad Power_in-1(108.2mm,61.5mm) on Multi-Layer And Via (110.825mm,61.375mm) from Top Layer to
Un-Routed Net Constraint: Net +12V Between Via (110.825mm,61.375mm) from Top Layer to Bottom Layer And Track
Un-Routed Net Constraint: Net GND Between Pad C50-1(136.325mm,55.175mm) on Bottom Layer And Pad C27-2(136.4mm,55.2mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad U13-3(126.05mm,99.15mm) on Top Layer And Pad U13-2(127mm,99.15mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad U12-2(118.2mm,99.275mm) on Top Layer And Pad U12-3(119.15mm,99.275mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad U10-2(127mm,92.925mm) on Top Layer And Pad U10-3(127.95mm,92.925mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Track
Un-Routed Net Constraint: Net GND Between Via (119.4mm,106.45mm) from Top Layer to Bottom Layer And Pad C39-1(119.4mm,107.575mm) on Top
Un-Routed Net Constraint: Net GND Between Pad C29-2(131.475mm,50.8mm) on Top Layer And Pad R15-1(132.675mm,50.485mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad C42-2(113.9mm,99.675mm) on Top Layer And Pad C45-1(115.55mm,99.825mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad C38-1(124.55mm,110.425mm) on Top Layer And Pad R29-1(126.325mm,110.215mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad C40-2(122.925mm,92.925mm) on Top Layer And Pad C41-1(124.65mm,93.225mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Via (122.525mm,91.275mm) from Top Layer to Bottom Layer And Pad C40-2(122.925mm,92.925mm) on
Un-Routed Net Constraint: Net GND Between Pad U5-5(134.2mm,51.575mm) on Top Layer And Via (135.725mm,52.175mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Pad C18-2(139.025mm,56.375mm) on Bottom Layer And Pad C19-2(141.25mm,56.375mm) on Bottom Layer
Un-Routed Net Constraint: Net GND Between Pad C37-2(117.6mm,107.125mm) on Top Layer And Via (119.4mm,106.45mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Pad C32-1(141.375mm,106.825mm) on Bottom Layer And Via (143.275mm,107.175mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (143.275mm,107.175mm) from Top Layer to Bottom Layer And Pad C24-1(145.175mm,106.825mm) on
Un-Routed Net Constraint: Net GND Between Pad C44-2(122.95mm,98.3mm) on Top Layer And Pad C46-1(124.65mm,97.725mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad C33-1(141.375mm,103.775mm) on Bottom Layer And Via (143.275mm,103.35mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (155.85mm,51.675mm) from Top Layer to Bottom Layer And Via (155.85mm,54.15mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (153.375mm,49.05mm) from Top Layer to Bottom Layer And Via (155.85mm,49.05mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (155.85mm,54.15mm) from Top Layer to Bottom Layer And Via (155.85mm,56.625mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (155.85mm,56.625mm) from Top Layer to Bottom Layer And Via (155.85mm,59.1mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (144.6mm,56.1mm) from Top Layer to Bottom Layer And Via (147.075mm,56.1mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Pad U6-2(123.45mm,77.975mm) on Bottom Layer And Pad C35-1(123.525mm,75.575mm) on Bottom Layer
Un-Routed Net Constraint: Net GND Between Pad C30-1(141.375mm,95.075mm) on Bottom Layer And Via (143.275mm,95.7mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Pad C43-1(124.175mm,99.8mm) on Top Layer And Pad U13-3(126.05mm,99.15mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Via (143.275mm,95.7mm) from Top Layer to Bottom Layer And Pad C22-1(145.175mm,95.075mm) on
Un-Routed Net Constraint: Net GND Between Pad Power_in-3(108.2mm,66.58mm) on Multi-Layer And Pad Power_in-4(108.2mm,69.12mm) on Multi-Layer
Un-Routed Net Constraint: Net GND Between Via (119.4mm,106.45mm) from Top Layer to Bottom Layer And Via (122.975mm,105.45mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Pad R10-1(134.1mm,55.61mm) on Top Layer [Unplated] And Pad C50-1(136.325mm,55.175mm) on Bottom
Un-Routed Net Constraint: Net GND Between Via (135.725mm,52.175mm) from Top Layer to Bottom Layer And Pad C34-1(135.9mm,49.6mm) on Top
Un-Routed Net Constraint: Net GND Between Pad U6-2(123.45mm,77.975mm) on Bottom Layer And Via (125.775mm,78.4mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Pad C19-2(141.25mm,56.375mm) on Bottom Layer And Pad C21-1(142.375mm,54.45mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Via (143.275mm,95.7mm) from Top Layer to Bottom Layer And Pad C23-1(145.175mm,98.2mm) on Bottom
Un-Routed Net Constraint: Net GND Between Pad C31-1(141.375mm,98.2mm) on Bottom Layer And Via (143.275mm,95.7mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Via (126.3mm,113.375mm) from Top Layer to Bottom Layer And Pad R29-1(126.325mm,110.215mm) on
Un-Routed Net Constraint: Net GND Between Via (143.275mm,95.7mm) from Top Layer to Bottom Layer And Via (143.275mm,99.525mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Pad C26-2(135.675mm,57.775mm) on Top Layer And Pad C50-1(136.325mm,55.175mm) on Bottom Layer
Un-Routed Net Constraint: Net GND Between Via (125.775mm,74.575mm) from Top Layer to Bottom Layer And Via (125.775mm,78.4mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Pad U6-6(121.05mm,78.925mm) on Bottom Layer And Via (125.775mm,78.4mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Pad U2-3(119.4mm,71.425mm) on Bottom Layer And Pad C7-1(121.95mm,72.275mm) on Bottom Layer
Un-Routed Net Constraint: Net GND Between Pad C8-1(116.825mm,72.275mm) on Bottom Layer And Pad U2-3(119.4mm,71.425mm) on Bottom Layer
Un-Routed Net Constraint: Net GND Between Via (143.275mm,88.05mm) from Top Layer to Bottom Layer And Via (146.85mm,88.05mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (139.7mm,88.05mm) from Top Layer to Bottom Layer And Via (143.275mm,88.05mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (125.775mm,70.75mm) from Top Layer to Bottom Layer And Via (125.775mm,74.575mm) from Top
Un-Routed Net Constraint: Net GND Between Via (143.275mm,88.05mm) from Top Layer to Bottom Layer And Via (143.275mm,91.875mm) from Top
Un-Routed Net Constraint: Net GND Between Via (130.775mm,59.275mm) from Top Layer to Bottom Layer And Via (130.775mm,63.1mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (125.775mm,55.45mm) from Top Layer to Bottom Layer And Via (125.775mm,59.275mm) from Top
Un-Routed Net Constraint: Net GND Between Via (155.85mm,63.1mm) from Top Layer to Bottom Layer And Via (155.85mm,66.925mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (155.85mm,70.75mm) from Top Layer to Bottom Layer And Via (155.85mm,74.575mm) from Top Layer

Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net GND Between Via (155.85mm,82.225mm) from Top Layer to Bottom Layer And Via (155.85mm,86.05mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (130.775mm,70.75mm) from Top Layer to Bottom Layer And Via (130.775mm,74.575mm) from Top
Un-Routed Net Constraint: Net GND Between Via (155.85mm,74.575mm) from Top Layer to Bottom Layer And Via (155.85mm,78.4mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (130.775mm,78.4mm) from Top Layer to Bottom Layer And Via (130.775mm,82.225mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (125.775mm,63.1mm) from Top Layer to Bottom Layer And Via (125.775mm,66.925mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (130.775mm,63.1mm) from Top Layer to Bottom Layer And Via (130.775mm,66.925mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (128.65mm,51.875mm) from Top Layer to Bottom Layer And Pad C29-2(131.475mm,50.8mm) on Top
Un-Routed Net Constraint: Net GND Between Pad HV_Out-2(141.32mm,114.3mm) on Multi-Layer And Pad HV_Out-3(145.28mm,114.3mm) on Multi-Layer
Un-Routed Net Constraint: Net GND Between Pad D5-1(151.725mm,60mm) on Bottom Layer And Via (155.85mm,59.1mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Via (113.875mm,105.75mm) from Top Layer to Bottom Layer And Via (119.4mm,106.45mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Pad R14-1(141.575mm,50mm) on Bottom Layer And Pad C21-1(142.375mm,54.45mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Pad U13-2(127mm,99.15mm) on Top Layer And Via (128.275mm,103.5mm) from Top Layer to Bottom Layer
Un-Routed Net Constraint: Net GND Between Pad T1-8(143.275mm,82.225mm) on Multi-Layer And Via (143.275mm,88.05mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Pad R17-1(128.25mm,86.025mm) on Bottom Layer And Via (130.775mm,82.225mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Pad C52-1(174.784mm,39.352mm) on Top Layer And Pad C51-1(181.559mm,39.352mm) on Top Layer
Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Pad U11-7(151.25mm,92.525mm) on Top
Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Pad R33-1(157.35mm,98.31mm) on Top
Un-Routed Net Constraint: Net GND Between Pad Free-0(92.6mm,65.7mm) on Multi-Layer And Pad Power_in-3(108.2mm,66.58mm) on Multi-Layer
Un-Routed Net Constraint: Net GND Between Pad Free-0(89.575mm,81.3mm) on Multi-Layer And Pad Control_in-1(108.2mm,82.325mm) on Multi-Layer
Un-Routed Net Constraint: Net GND Between Pad U8-7(118.35mm,119.175mm) on Top Layer And Via (119.4mm,106.45mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Pad C20-1(163.225mm,106.2mm) on
Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Pad U12-2(118.2mm,99.275mm) on Top
Un-Routed Net Constraint: Net GND Between Pad C27-2(136.4mm,55.2mm) on Top Layer And Via (136.725mm,52.975mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Pad C46-1(124.65mm,97.725mm) on Top Layer And Via (128.275mm,103.5mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Via (128.65mm,51.875mm) from Top Layer to Bottom Layer And Via (135.725mm,52.175mm) from Top
Un-Routed Net Constraint: Net GND Between Via (155.85mm,49.05mm) from Top Layer to Bottom Layer And Via (155.85mm,51.675mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (122.525mm,91.275mm) from Top Layer to Bottom Layer And Pad U10-2(127mm,92.925mm) on Top
Un-Routed Net Constraint: Net GND Between Via (143.275mm,103.35mm) from Top Layer to Bottom Layer And Via (143.275mm,107.175mm) from Top
Un-Routed Net Constraint: Net GND Between Pad C45-1(115.55mm,99.825mm) on Top Layer And Via (118.15mm,95.525mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Pad C19-2(141.25mm,56.375mm) on Bottom Layer And Via (144.6mm,56.1mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Via (125.775mm,66.925mm) from Top Layer to Bottom Layer And Via (125.775mm,70.75mm) from Top
Un-Routed Net Constraint: Net GND Between Via (155.85mm,66.925mm) from Top Layer to Bottom Layer And Via (155.85mm,70.75mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Via (143.275mm,95.7mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (155.85mm,78.4mm) from Top Layer to Bottom Layer And Via (155.85mm,82.225mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (130.775mm,74.575mm) from Top Layer to Bottom Layer And Via (130.775mm,78.4mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (125.775mm,59.275mm) from Top Layer to Bottom Layer And Via (125.775mm,63.1mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (130.775mm,66.925mm) from Top Layer to Bottom Layer And Via (130.775mm,70.75mm) from Top
Un-Routed Net Constraint: Net GND Between Pad C7-1(121.95mm,72.275mm) on Bottom Layer And Via (125.775mm,70.75mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Via (122.975mm,105.45mm) from Top Layer to Bottom Layer And Via (126.3mm,113.375mm) from Top
Un-Routed Net Constraint: Net GND Between Pad HV_Out-2(141.32mm,114.3mm) on Multi-Layer And Via (143.275mm,107.175mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Pad Free-0(92.6mm,65.7mm) on Multi-Layer And Via (125.775mm,66.925mm) from Top Layer to Bottom
Un-Routed Net Constraint: Net GND Between Via (155.85mm,49.05mm) from Top Layer to Bottom Layer And Pad C52-1(174.784mm,39.352mm) on Top
Un-Routed Net Constraint: Net GND Between Via (136.725mm,52.975mm) from Top Layer to Bottom Layer And Via (144.6mm,56.1mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (143.275mm,99.525mm) from Top Layer to Bottom Layer And Via (143.275mm,103.35mm) from Top
Un-Routed Net Constraint: Net GND Between Via (155.85mm,59.1mm) from Top Layer to Bottom Layer And Via (155.85mm,63.1mm) from Top Layer to
Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Via (128.275mm,103.5mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Via (122.525mm,91.275mm) from Top
Un-Routed Net Constraint: Net GND Between Via (125.775mm,66.925mm) from Top Layer to Bottom Layer And Via (130.775mm,66.925mm) from Top
Un-Routed Net Constraint: Net GND Between Via (122.975mm,105.45mm) from Top Layer to Bottom Layer And Via (128.275mm,103.5mm) from Top
Un-Routed Net Constraint: Net GND Between Via (125.775mm,55.45mm) from Top Layer to Bottom Layer And Via (128.65mm,51.875mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (122.525mm,91.275mm) from Top Layer to Bottom Layer And Via (125.775mm,78.4mm) from Top Layer
Un-Routed Net Constraint: Net GND Between Via (147.075mm,56.1mm) from Top Layer to Bottom Layer And Via (155.85mm,56.625mm) from Top Layer

Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net GND Between Via (146.85mm,88.05mm) from Top Layer to Bottom Layer And Via (155.85mm,86.05mm) from Top Layer to
Un-Routed Net Constraint: Net NetC30_2 Between Pad R16-2(126.475mm,91.2mm) on Bottom Layer And Pad D6-2(131.5mm,86.35mm) on Bottom Layer
Un-Routed Net Constraint: Net NetC30_2 Between Pad D6-2(131.5mm,86.35mm) on Bottom Layer And Pad C30-2(136.675mm,95.075mm) on Bottom

Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C44-1(122.95mm,96.6mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad C43-1(124.175mm,99.8mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C40-1(122.925mm,94.625mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.206mm < 0.254mm) Between Via (116.35mm,98.5mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C42-1(113.9mm,101.375mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.238mm < 0.254mm) Between Pad R35-2(117.91mm,103.05mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Pad R35-1(119.29mm,103.05mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.149mm < 0.254mm) Between Via (121.3mm,105.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.227mm < 0.254mm) Between Via (121.3mm,105.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.138mm < 0.254mm) Between Via (122.975mm,105.45mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.247mm < 0.254mm) Between Pad C15-2(73.1mm,62.325mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Via (73.1mm,60.75mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.013mm < 0.254mm) Between Via (76.625mm,79mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-4(58mm,111.6mm) on Top Layer And Pad J4-5(58mm,110.8mm) on Top
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-3(58mm,112.4mm) on Top Layer And Pad J4-4(58mm,111.6mm) on Top
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-2(58mm,113.2mm) on Top Layer And Pad J4-3(58mm,112.4mm) on Top
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-1(58mm,114mm) on Top Layer And Pad J4-2(58mm,113.2mm) on Top
Minimum Solder Mask Sliver Constraint: (0.234mm < 0.254mm) Between Pad U1-36(70.175mm,89.15mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.203mm < 0.254mm) Between Via (70.6mm,91.525mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.148mm < 0.254mm) Between Pad U1-36(70.175mm,89.15mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.253mm < 0.254mm) Between Pad R7-2(74.31mm,67.85mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.253mm < 0.254mm) Between Pad R7-1(75.69mm,67.85mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C37-1(117.6mm,108.825mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.212mm < 0.254mm) Between Via (119.4mm,106.45mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-5(66.3mm,108.825mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-4(65.35mm,108.825mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-2(66.3mm,111.525mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-1(67.25mm,111.525mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.123mm < 0.254mm) Between Pad R2-2(66.125mm,82.35mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.124mm < 0.254mm) Between Pad U1-47(68.875mm,82.85mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.123mm < 0.254mm) Between Pad U1-48(68.875mm,82.35mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.104mm < 0.254mm) Between Via (68.525mm,80.1mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.17mm < 0.254mm) Between Pad U1-1(70.175mm,81.05mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.149mm < 0.254mm) Between Via (77.65mm,67.825mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C14-1(76.6mm,59.475mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.229mm < 0.254mm) Between Via (73.1mm,60.75mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C15-1(73.1mm,64.025mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.153mm < 0.254mm) Between Pad C29-2(131.475mm,50.8mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.153mm < 0.254mm) Between Pad C29-1(131.475mm,52.3mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.217mm < 0.254mm) Between Pad C29-2(131.475mm,50.8mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.253mm < 0.254mm) Between Pad C28-1(132.3mm,56.975mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Pad C28-1(132.3mm,56.975mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.227mm < 0.254mm) Between Pad C26-2(135.675mm,57.775mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C27-1(138.1mm,55.2mm) on Top Layer And Pad C27-2(136.4mm,55.2mm)
Minimum Solder Mask Sliver Constraint: (0.222mm < 0.254mm) Between Via (140.45mm,59.025mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.198mm < 0.254mm) Between Via (139.675mm,54.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.108mm < 0.254mm) Between Pad P2-1(59.95mm,89.255mm) on Multi-Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-6(119.55mm,96.95mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-7(119.55mm,97.6mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-8(119.55mm,98.25mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-3(124.05mm,96.95mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-2(124.05mm,97.6mm) on Bottom Layer And Pad

Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-1(124.05mm,98.25mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.098mm < 0.254mm) Between Via (115.375mm,73.75mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.233mm < 0.254mm) Between Pad R20-2(119.125mm,75.125mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.222mm < 0.254mm) Between Pad R20-2(119.125mm,75.125mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-5(121.05mm,77.975mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-4(121.05mm,77.025mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-2(123.45mm,77.975mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-1(123.45mm,78.925mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.087mm < 0.254mm) Between Via (126.5mm,84.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.062mm < 0.254mm) Between Via (126.5mm,84.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad U3-2(66.475mm,62.6mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad U3-3(66.475mm,61.65mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C10-1(64.15mm,65.275mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C19-1(141.25mm,58.075mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C18-1(139.025mm,58.075mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.162mm < 0.254mm) Between Via (140.825mm,46.8mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Via (121.3mm,105.425mm) from Top Layer to Bottom Layer And Via
Minimum Solder Mask Sliver Constraint: (0.191mm < 0.254mm) Between Via (140.85mm,53.275mm) from Top Layer to Bottom Layer And Via

Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Arc (128.5mm,99.325mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Arc (125.5mm,92.75mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Arc (116.7mm,99.1mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Arc (120.275mm,106.95mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Arc (68.1mm,111.825mm) on Top Overlay And Pad L7-2(68.95mm,111.8mm)
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Arc (75.262mm,75.734mm) on Top Overlay And Pad C4-1(74.925mm,76mm)
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Arc (136.225mm,54.15mm) on Top Overlay And Pad C27-2(136.4mm,55.2mm)
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Area Fill (149.525mm,105.525mm) (149.925mm,106.325mm) on Top Overlay
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,98.925mm)(128.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,97.225mm)(128.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,98.925mm)(125.575mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,97.225mm)(125.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,97.225mm)(128.5mm,97.225mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,97.225mm)(128.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,97.225mm)(125.575mm,97.225mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,97.225mm)(125.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,93.15mm)(125.575mm,93.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,93.15mm)(125.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,93.15mm)(128.5mm,93.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,93.15mm)(128.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,94.85mm)(125.575mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,93.15mm)(125.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,94.85mm)(128.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,93.15mm)(128.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.18mm < 0.254mm) Between Text "C41" (125.125mm,90.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.134mm < 0.254mm) Between Text "C40" (119.325mm,92.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "C47" (116.01mm,105.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.16mm < 0.254mm) Between Text "C45" (116.25mm,101.935mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (116.7mm,99.5mm)(116.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (116.7mm,99.5mm)(116.775mm,99.5mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (119.7mm,99.5mm)(119.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (119.625mm,99.5mm)(119.7mm,99.5mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (116.7mm,101.2mm)(116.775mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (116.7mm,99.5mm)(116.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (119.625mm,101.2mm)(119.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (119.7mm,99.5mm)(119.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.126mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (120.275mm,107.35mm)(120.35mm,107.35mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (120.275mm,107.35mm)(120.275mm,109.05mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (123.2mm,107.35mm)(123.275mm,107.35mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (123.275mm,107.35mm)(123.275mm,109.05mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (120.275mm,109.05mm)(120.35mm,109.05mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (120.275mm,107.35mm)(120.275mm,109.05mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (123.2mm,109.05mm)(123.275mm,109.05mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (123.275mm,107.35mm)(123.275mm,109.05mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (63.311mm,78.87mm)(64.2mm,78.87mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (63.311mm,78.87mm)(63.311mm,79.505mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (65.089mm,78.87mm)(65.089mm,79.505mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (64.2mm,78.87mm)(65.089mm,78.87mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (63.635mm,82.775mm)(64.765mm,82.775mm) on Top Overlay And Pad

Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (63.311mm,82.451mm)(63.635mm,82.775mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (63.311mm,81.861mm)(63.311mm,82.451mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (65.089mm,81.861mm)(65.089mm,82.451mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (64.765mm,82.775mm)(65.089mm,82.451mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (60mm,106.05mm)(60mm,118.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (60mm,106.05mm)(60mm,118.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "C16" (74.125mm,69.775mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.084mm < 0.254mm) Between Text "R7" (72.45mm,68.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "C16" (74.125mm,69.775mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.025mm < 0.254mm) Between Track (60mm,106.05mm)(60mm,118.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.254mm) Between Text "C47" (116.01mm,105.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Text "C38" (125.25mm,111.175mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (59.045mm,103.111mm)(59.68mm,103.111mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (59.045mm,104.889mm)(59.68mm,104.889mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (59.68mm,104mm)(59.68mm,104.889mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (59.68mm,103.111mm)(59.68mm,104mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (55.775mm,103.435mm)(56.099mm,103.111mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (56.099mm,103.111mm)(56.688mm,103.111mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (56.099mm,104.889mm)(56.688mm,104.889mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (55.775mm,104.565mm)(56.099mm,104.889mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (55.775mm,103.435mm)(55.775mm,104.565mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Area Fill (137mm,105.525mm) (137.4mm,106.325mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,99.375mm)(62.825mm,99.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,98.575mm)(62.825mm,98.575mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,99.375mm)(62.825mm,99.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,98.575mm)(62.825mm,98.575mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.125mm,94.375mm)(129.125mm,94.775mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.925mm,94.375mm)(129.925mm,94.775mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.125mm,94.375mm)(129.125mm,94.775mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.925mm,94.375mm)(129.925mm,94.775mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.092mm < 0.254mm) Between Text "R31" (145.625mm,107.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.15mm,98.15mm)(129.15mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.95mm,98.15mm)(129.95mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.15mm,98.15mm)(129.15mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.95mm,98.15mm)(129.95mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,104.6mm)(62.975mm,104.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,105.4mm)(62.975mm,105.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,104.6mm)(62.975mm,104.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,105.4mm)(62.975mm,105.4mm) on Top Overlay And Pad

Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.184mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (65.725mm,83mm)(65.725mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (66.525mm,83mm)(66.525mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (65.725mm,83mm)(65.725mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (66.525mm,83mm)(66.525mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (68.925mm,89.875mm)(69.725mm,89.875mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.059mm < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.235mm < 0.254mm) Between Track (72.038mm,63.736mm)(72.038mm,65.539mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.235mm < 0.254mm) Between Track (67.162mm,63.736mm)(72.038mm,63.736mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Text "C27" (138.85mm,54.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (131.075mm,51.55mm)(131.875mm,51.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "R10" (133.625mm,54.45mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (136.35mm,54.15mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (134.75mm,54.15mm)(136.1mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (136.35mm,54.15mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "C27" (138.85mm,54.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm)(108.52mm,91.165mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm)(108.52mm,91.165mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm)(108.52mm,91.165mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm)(108.52mm,91.165mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,88.025mm)(65.473mm,88.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,88.025mm)(65.473mm,88.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.145mm < 0.254mm) Between Track (58.205mm,89.222mm)(58.805mm,89.222mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,93.025mm)(65.473mm,93.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.049mm < 0.254mm) Between Track (64.809mm,89.222mm)(64.809mm,91.828mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.254mm) Between Track (64.809mm,91.828mm)(65.505mm,91.828mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,93.025mm)(65.473mm,93.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.089mm < 0.254mm) Between Text "R7" (72.45mm,68.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And Pad

Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Text "R20" (118.325mm,74.375mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "R20" (118.325mm,74.375mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.134mm < 0.254mm) Between Text "C35" (126.775mm,75.7mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Text "C7" (122.525mm,71.975mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.059mm < 0.254mm) Between Text "C35" (126.775mm,75.7mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R20" (118.325mm,74.375mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (125.875mm,82mm)(127.075mm,82mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (125.875mm,82mm)(127.075mm,82mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (127.35mm,85.425mm)(127.35mm,86.625mm) on Bottom Overlay And
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (127.35mm,85.425mm)(127.35mm,86.625mm) on Bottom Overlay And
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm,94.825mm)(61.15mm,94.825mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm,94.025mm)(61.15mm,94.025mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm,94.825mm)(61.15mm,94.825mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm,94.025mm)(61.15mm,94.025mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Text "R6" (74.2mm,65.475mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (64.5mm,64.2mm)(66.3mm,64.2mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "C19" (142.975mm,54.5mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (139.725mm,58.55mm)(147.95mm,58.55mm) on Bottom Overlay And
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (139.725mm,58.55mm)(139.725mm,62.125mm) on Bottom Overlay And
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Text "C18" (138.35mm,59mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (139.725mm,58.55mm)(147.95mm,58.55mm) on Bottom Overlay And
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Text "C33" (135.8mm,103.225mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.251mm < 0.254mm) Between Text "R12" (148.55mm,54.075mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R12" (148.55mm,54.075mm) on Bottom Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Text "R14" (147.775mm,46.575mm) on Bottom Overlay And Pad

Silk to Silk (Clearance=0.254mm) (All),(All)
Silk To Silk Clearance Constraint: (0.251mm < 0.254mm) Between Text "C41" (125.125mm,90.275mm) on Top Overlay And Arc (125.5mm,92.75mm) on
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Arc (69.475mm,81.05mm) on Top
Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "HV_Out" (132.9mm,118.675mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "HV_Out" (132.9mm,118.675mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.032mm < 0.254mm) Between Text "L5" (67.3mm,58.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "C16" (74.125mm,69.775mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.064mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.201mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.201mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.222mm < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.222mm < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.225mm < 0.254mm) Between Text "C12" (81.475mm,63.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.185mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.185mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.234mm < 0.254mm) Between Text "R10" (133.625mm,54.45mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.225mm < 0.254mm) Between Text "R10" (133.625mm,54.45mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.057mm < 0.254mm) Between Text "C7" (122.525mm,71.975mm) on Bottom Overlay And Track
Silk To Silk Clearance Constraint: (0.2mm < 0.254mm) Between Text "U3" (66.375mm,59.65mm) on Bottom Overlay And Track
Silk To Silk Clearance Constraint: (0.249mm < 0.254mm) Between Text "Q1" (148.825mm,57.175mm) on Bottom Overlay And Track
Silk To Silk Clearance Constraint: (0.249mm < 0.254mm) Between Text "C18" (138.35mm,59mm) on Bottom Overlay And Track
Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "R12" (148.55mm,54.075mm) on Bottom Overlay And Track
Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "X1" (68.525mm,75.8mm) on Top Overlay And Text "C2" (67.275mm,74.525mm)
Silk To Silk Clearance Constraint: (0.148mm < 0.254mm) Between Text "R7" (72.45mm,68.6mm) on Top Overlay And Text "C16" (74.125mm,69.775mm)
Silk To Silk Clearance Constraint: (0.21mm < 0.254mm) Between Text "C43" (124.8mm,102.325mm) on Top Overlay And Text "R34'
Silk To Silk Clearance Constraint: (0mm < 0.254mm) Between Text "R35" (120.275mm,103.425mm) on Top Overlay And Text "R34'
Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "U13" (126.05mm,100.5mm) on Top Overlay And Text "C46" (125.85mm,99.25mm)
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R14" (147.775mm,46.575mm) on Bottom Overlay And Text "R13'

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (110.875mm,63.975mm) from Top Layer to Bottom Layer
Net Antennae: Via (118.15mm,95.525mm) from Top Layer to Bottom Layer
Net Antennae: Via (122.975mm,105.45mm) from Top Layer to Bottom Layer
Net Antennae: Via (126.3mm,113.375mm) from Top Layer to Bottom Layer
Net Antennae: Via (113.875mm,105.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (128.275mm,103.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (119.4mm,106.45mm) from Top Layer to Bottom Layer
Net Antennae: Via (122.525mm,91.275mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,59.275mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,63.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (128.65mm,51.875mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,63.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,59.275mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,55.45mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,107.175mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,103.35mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,99.525mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,95.7mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,88.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,91.875mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,78.4mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,74.575mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,66.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,70.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (126.5mm,84.425mm) from Top Layer to Bottom Layer
Net Antennae: Via (110.825mm,61.375mm) from Top Layer to Bottom Layer
Net Antennae: Via (110.075mm,72.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,82.225mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,86.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,78.4mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,63.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,66.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,74.575mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,70.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,70.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,66.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,74.575mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,78.4mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,82.225mm) from Top Layer to Bottom Layer
Net Antennae: Via (146.85mm,88.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (139.7mm,88.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (153.375mm,49.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,49.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,51.675mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,54.15mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,56.625mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,59.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (147.075mm,56.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (144.6mm,56.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (139.675mm,54.425mm) from Top Layer to Bottom Layer
Net Antennae: Track (118.55mm,97.1mm)(119.05mm,97.6mm) on Bottom Layer
Net Antennae: Track (108.155mm,78.025mm)(121.05mm,78.025mm) on Bottom Layer

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Track (107.975mm,75.305mm)(109.695mm,77.025mm) on Bottom Layer
Net Antennae: Track (108.215mm,84.85mm)(110.3mm,84.85mm) on Bottom Layer