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1	Comment PMEG3010ER	Description 1A, 30V	er NXP	Number PMEG301 CER	r 1 SAMPLE	r 2	Note	1	2	Footprint SOD123	Designate D2
	1M6	1M6 5% 0603 1/10W 6N136 SDM	IVAF	CER	SAMPLE					RESC1608N	R6
		Optocoupler, Transistor Output, 1									
2	6N136	Channel, Surface Mount DIP, 8 Pins, 50 mA, 5 kV, 19 %			TME			6N136S-		SMP-8	U8. U11
1	UN 130	12-Bit Digital-to- Analog Converter with			THE .					Jan - O	00,011
		EEPROM Memory in SOT-23-6, 6-Pin SOT- 23. Extended						MCP472 SAUT-	MCP472 SAOT		
1	MCP4725A0T- E/CH	Zs, Extended Temperature, Tape and Reel			Digi- Key	Digi- Key		E/CHTR- ND	E/CHCT- ND	SOT-23-CH6_N	U6
1	22R 30k	22R 5% 1206 1/4W 30K 5% 0603 1/10W								RESC3216N RESC1608N	R13 R30, R36
Ī		55V, 1.2A Step-Down Regulator with 2.8µA								10-LEAD PLASTIC MSOP -	
1	LT3991	Quiescent Current			Newark			2412758		duplicate	U4 R9, R11,
6	75R	75R 5% 1206 1/4W							710-	RESC3216N	R27, R31, R37, R38
2	FB 1.5k 1A	BEAD FERRITE, SMD 1500OHM, 0805	Wurth Elektronik	74279209 7	Farnell	Mouser		1635738	7427920 97	LL0805\2P	L4, L6
		BEAD SMD EMI FERRITE 0805 220 OHM	Murata	BLM21PG 221SN1D						LL0805\2P	L1, L2, L7
3		Boost, Flyback, SEPIC and Inverting	Murata	2215N1D						10-LEAD PLASTIC MSOP -	L1, L2, L7
1	LT3757	Controller CONNECTOR, 4 way ,		B4P-	Farnell			2294952	455-	duplicate	U5
1	HDR 4	HEADER, THT, 3.96MM,10A,250Vac	JST	VH(LF)(S N)	Farnell	Digi- Key	s	9492020	1641- ND	HDR1X4_3M96V	HV_Out
,	8 MHz	CRYSTAL, 8M, 18PF CL, 6X3.6MM SMT	ABRACON	ABMM2- 8.000MH Z-E2-T	Farnell	Mouser	,	1611803	815- ABMM2- 8-E2T	2SMX-3SMXB	X1
i		DIODE, fast, 1A, 600V,	ST Microelectr	STTH1R0							
2	STTH1R06A BASS16	SMD, SMA DIODE, SWITCHING, SOD-523	onics	6A BASS 16	Farnell Farnell			1697520 8734402		DO-214AC SOD523	D3, D6
2	BASS16 USBLC6-25C6	DIODE ARRAY, TVS, USB2	STMICROEL ECTRONICS	USBLC6- 25C6	Farnell			1269406		SOD523 SOT95P280X145-6N	D8, D9 U7
1	BAV99	DIODE SW DUAL 75V 350mW SOT23	ECTRONICS	BAV99-7- F	Farnell	Mouser	0,289 €	1843683	621- BAV99-F	50193P280X143-8N 50195P240-3L	D5
1	EFD20	EFD_20			Newark			06W066 7		EFD_20	T1
		INDUCTOR, SMD,		LQH44PN							
1	10uH TLV70433DBV T	4X4MM, 10UH, 20% LDO, 3.3V, 150MA, SOT23-5 Vin - 24V	Murata Texas Instruments	100MPOL TLV70433 DBVT	Farnell Farnell	H		1782801 2075417		L1515 SO-G5/P.95	L5 U3
				крнсм-	(MI				859- LTST-		Ī
2	KPHCM- 2012SGC-T	LED, SMD, 0805, GREEN, 25mcd	KINGBRIGH T	2012SGC- T	Farnell	Mouser	s	1686074	C171CK T	D0805	D7, PWR
1		Low-Cost, Micropower, Low-Dropout, High-		_							
1	MAX6104EUR +T	Output-Current, SOT23 Voltage References	L	L	Farnell	L	L	2511340	L	SOT23-3	U2
Ì		MAXIM INTEGRATED PRODUCTS									
		MAX11612EUA+ Analogue to Digital Converter Low Power	1	l							
	MAX11612EU	Converter, Low Power, 12 bit, 94.4 kSPS, Single, 4.5 V, 5.5 V,	1	l							
1	A	μMAX Micro-match SOCKET,	-	<u> </u>	Farnell	Н		2513417	<u> </u>	uMAX-8	U14
1	7-215079-4	TOP ENTRY, 4WAY PTH, 1.27mm	TE Connectivity	7-215079 4	Farnell		s	148507	L	ZIP-T4	P2
Ì		MICROCHIP MCP6001T-I/OT Operational Amplifier,									
	MCP6001T-	Single, 1 Amplifier, 1 MHz, 0.6 V/µs, 1.8V to									U9, U10,
4	VOT	6V, SOT-23, 5 Pins			Farnell			4974992		SO-G5/X.45	U12, U13
											C6, C7, C8 C9, C12,
											C16, C21, C29, C35, C36, C39,
		MLCC, 0.1uF 16V 10%		C0603C1 04K4RAC				1414610	80- C0603C1		C41, C45, C46, C48,
19	100nF	X7R 0603	KEMET	TU GRM219	Farnell	Mouser	0,041 €	RL	04K4R	C0603V	C49
1	0.47uF	MLCC,0.47uF,50V, 0805,	MURATA	R60J106K E19D	Farnell		s	1845738		CAPC2012N	C14
		MLCC 10nF 630V		C3216X7					810- C3216X7 R2J103		
1	10nF	±10% X7R, 1206	TDK	R2J103M		Mouser	0,182 €		M 81-	CAPC3216N	C34
		MLCC, 10uF, 16V, 10%,		GRM31C R71C106					GRM310 R71C106		
1	10uF	1206	MURATA	KAC7L MC0603 N180J50	Farnell	Mouser	1,53 €	1845772	KAC7	CAPC3216N	C13
2	18pF	MLCC, 18 pF, ± 5%, COG / NPO, 50 V, 0603	P	N180J50 OCT GRM219	Farnell			1759056		CAPC1608N	C1, C2
1	47uF	MLCC,47uF,50V, 0805,	MURATA	R60J106K E19D	Farnell	L	s	1845738	L	CAPC2012N	C15
٦									77- VJ0603A 100JXAC		
4	10pF	MLCC, 0603, NP0, 50V, 10PF	MULTICOM P	MCCAD0 0192 MC1206	Farnell	Mouser		1759053	BC BC	CAPC1608N	C17, C38, C43, C47
1	680pF	MLCC, 680PF, 630V, COG/NPO, 1206	MULTICOM P	N681.63 1CT	Farnell			1875399		CAPC3216N	C28
									77- VJ0805V		C10, C18, C19, C27,
8	luF	MLCC, 0805, X5R, 16V, 1UF	MULTICOM P	MCCA00 0596 GRM32E	Farnell	Mouser		1759479	105ZXJC BC	CAPC2012N	C37, C40, C42, C44
2	47uF	MLCC, 1210, 47uF, 16V	MURATA	R61C476 ME15L	Farnell			1735538		CAPC3225N	C26, C50
											C20, C22, C23, C24,
		MLCC, X7R, 100NF,		C1808V1 04KBRAC							C30, C31, C32, C33,
10	100nF PSMN069-	630V, 1808 MOSFET 100V, 72.4 mOhm standard level,	KEMET	TU PSMN06	Farnell	Mouser		1845662	2085209	C1808	C51, C52
1	100YS	LFPAK P-Channel Logic Level	NXP FAIRCHILD	9-100YS	Farnell			RL	512-	SOT669	Q1
	NDS332P	Enhancement Mode Field Effect Transistor	SEMICOND UCTOR								U1
1	0R05		UCTUR	NDS332P	Farnell	Mouser		1471069	NDS332 P	SOT23-3L	Q2
1		Res, 0.05R 1% 2512 1W	WELWYN	NDS332P LR2512- R05FW	Farneli Farneli	Mouser	s	1471069 1100065	NDS332 P	SOT23-3L RESC6432AN	
1		Res, 0.05R 1% 2512 1W		LR2512- R05FW CR1206-		Mouser	s				Q2 R14
1	1M2	Res, 0.05R 1% 2512		LR2512- R05FW		Mouser Mouser	S 0,041 €		NDS332 P 652- CR1206F X- 1004ELF		Q2 R14 R16, R28, R32 R1, R3.
1		Res, 0.05R 1% 2512 1W Res, 1M 0.25W, 1% 1206 Res, 10K 0.063W 75V	WELWYN	LR2512- R05FW CR1206- FX- 1004ELF	Farnell	Mouser Mouser			652- CR1206F X- 1004ELF 594- MCT060 30C1002	RESC643ZAN RESC3216N	R14 R16, R28, R32 R1, R3, R19, R20, R26, R39,
3	10k	Res, 0.05R 1% 2512 TW  Res, 1M 0.25W, 1% 1206  Res, 10K 0.063W 75V 1% 0603  Res, 11K 5% 0805	WELWYN Bourns	LR2512- R05FW CR1206- FX- 1004ELF		Mouser Mouser Mouser	S 0,041 € 0,033 €		NDS332 P 652- CR1206F X- 1004ELF 594- MCT060	RESC3216N RESC3216N	R16, R28, R32 R1, R3, R19, R20, R26, R39, R40
3		Res, 0.05R 1% 2512 1W Res, 1M 0.25W, 1% 1206 Res, 10K 0.063W 75V 1% 0603	WELWYN Bourns	LR2512- R05FW CR1206- FX- 1004ELF	Farnell	Mouser Mouser Mouser			652- CR1206F X- 1004ELF 594- MCT060 30C1002	RESC643ZAN RESC3216N	R14 R16, R28, R32 R1, R3, R19, R20, R26, R39,
3	10k 11k3 15k	Res, 10K 0.058 1% 2512 1W  Res, 11M 0.25W, 1% 1206  Res, 10K 0.063W 75V 1% 0603 Res, 11K 5% 0805 1/8W  Res, 11K 5% 0805 1/8W  Res, 12K 0603 (1608 metric), 2 1%, 100mW, 100mW	WELWYN Bourns	LR2512- R05FW CR1206- FX- 1004ELF MCMR06 X1002FTL	Farnell	Mouser Mouser Mouser			652- CR1206F X- 1004ELF 594- MCT060 30C1002	RESC6432AN  RESC3216N  RESC1608N  RESC2012N	R14 R16, R28, R32 R1, R3, R19, R20, R26, R39, R40 R17
3 7 1	10k 11k3	Res. 0.05R 1% 2512 TW  Res. 1M 0.25W, 1% 1206  Res. 10K 0.063W 75V 1% 6063  Res. 11K 50 6005 1/5W	MELWYN BOURNS MULTICOM P PANASONI C	LR2512- R05FW CR1206- FX- 1004ELF MCMR06 X1002FTL ERJ3EKF1 202V	Farnell	Mouser Mouser Mouser			652- CR1206F X- 1004ELF 594- MCT060 30C1002	RESCG432AN RESCG3216N RESCG160BN RESCG012N	Q2 R14 R16, R28, R32 R1, R3, R19, R20, R26, R39, R40
3 3 7 1 1	10k 11k3 15k	Res. D.GSR 1% 2512 1 Res. 1M 0.25W, 1% 1206 Res. 10K 0.063W 75V 1% 1206 Res. 11K 5% 6805 1/8W Res. 11K 5% 6805 Res. 11K 5% 6805 1/8W Res. 12K 5% 6805 1/8W Res. 12K 5% 6805 Res. 14K 5% 6805 Res. 14K 5% 6805 Res. 14K 5% 6805 1/8W Res. 14K 5% 6805 Res. 14K 5	MELWYN  Bourns  MULTICOM P	LR2512- R05FW CR1206- FX- 1004ELF MCMR06 X1002FTL	Farnell Farnell	Mouser Mouser Mouser			652- CR1206F X- 1004ELF 594- MCT060 30C1002	RESC6432AN  RESC3216N  RESC1608N  RESC2012N	R14 R16, R28, R32 R1, R3, R19, R20, R26, R39, R40 R17
3 3 1 1 1	10k 11k3 15k	Res. D.OSR 1% 2512 1 Res. 1M 0.25W, 1% 1 1206  Res. 10K 0.063W 75V 1% 0.003  Res. 11K 5% 0805 1/8W 8605 1/2W 8605	MELWYN BOURNS MULTICOM P PANASONI C	LR2512- R05FW CR1206- FX- 1004ELF MCMR06 X1002FTL ERJ3EKF1 202V ERJ3EKF1	Farnell Farnell	Mouser Mouser Mouser			652- CR1206F X- 1004ELF 594- MCT060 30C1002	RESC6-122AN  RESC3216N  RESC3216N  RESC32012N  RESC32012N  RESC32012N	Q2 R14 R16, R28, R32 R1, R3, R19, R20, R26, R39, R40 R17 R18
3 7 1 1	10k 11k3 15k	Res, DOSR 1% 2512 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	WELWYN BOURNS MULTICOM P PANASONI C PANASONI C	LR2512- R05FW CR1206- FX- 1004ELF MCMR06 X1002FTL ERJ3EKF1 202V ERJ3EKF1 402V ERJ3EKF2 202V	Farnell Farnell	Mouser Mouser			NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS	RESCEASION RESCENSION RESCENSION RESCENSION RESCENSION RESCENSION RESCENSION RESCENSION	Q2 R14 R16, R28, R32 R1, R3, R19, R20, R26, R39, R40 R17 R18
3 3 7 1 1 1 2	10k 11k3 15k	Res., D.OSR 1%, 2512 1 Res., 1M 0.25W, 1%, 1206 Res., 10K 0.063W 75V 1% 1006 Res., 10K 0.063W 75V 1% 0603 Res., 11K 5% 0805 1/8W 86s, 12K, 0603 (1468 motric), ±1%, 100mW, 75V Res., 21K, 0603 (1468 motric), ±1%, 100mW, 75V Res., 22K, 0603 (1468 motric), ±1%, 100mW, 75V Res., 22K, 0603 (1468 motric), ±1%, 100mW, 75V	WELWYN BOURNS MULTICOM P PANASONI C PANASONI C	LR2512- R05FW CR1206- FX- 1004ELF MCMR06 X1002FTL ERJ3EKF1 202V ERJ3EKF1 402V ERJ3EKF2 202V	Farnell Farnell	Mouser  Mouser			NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS 660- RK73H11 T7D2R 0F	RESCEASION RESCENSION RESCENSION RESCENSION RESCENSION RESCENSION RESCENSION RESCENSION	Q2 R14 R16, R28, R32 R1, R3, R19, R20, R26, R39, R40 R17 R18
3 3 7 1 1 1 1 2	10k 11k3 15k	Res., GORR 1% 2512 7 100 100 110 110 110 110 110 110 110 1	WELWYN BOURNS MULTICOM P PANASONI C PANASONI C	LR2512- R05FW CR1206- FX- 1004ELF MCMR06 X1002FTL ERJ3EKF1 202V ERJ3EKF1 402V ERJ3EKF2 202V MC0063 W060312	Farnell  Farnell  Farnell  Farnell	Mouser  Mouser  Mouser		2073349	NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS	RESCH-132/AN RESCH-12/AN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BINSON	02 R14 R16, R28, R32 R32 R1, R3, R19, R20, R26, R39, R40 R17 R18 R29 R33
3 7 1 1 1 2 3	10k 11k3 15k	Beat, DOER TN, 2512 2 190 2 19	WELWYN BOURTS MULTICOM P PANASONI C PANASONI C MULTICOM P YAGEO	ER3512- ROSFW CR1206- FIX- 1004ELF MCMR06- X1002FTL ER35KFT ER	Farnell  Farnell  Farnell  Farnell	Mouser  Mouser  Mouser		2073349	NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 FPS 660- RK 73H11 T1D22R 0F 6603- RG00603F	RESCH-132/AN RESCH-12/AN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BIN RESCH-14/BINSON	Q2 R14 R16, R28, R32 R1, R3, R19, R20, R40 R17 R18 R29 R33 R10 R21, R22
3 3 7 1 1 1 1 2 2 3	10k 11k3 15k 12k 14k 22k	Box., DOER 196, 2512 2 197 Box., 130 C DOEN 79, 2512 2 197 Box., 130 C DOEN 79, 198 Box., 130 C DOES 1	WELWYN BOURNS MULTICOM P PANASONI C PANASONI C PANASONI C MULTICOM P	ER312F7 ROSPW CR1206-F7X- 1004ELF MCMR06 X1002F1L 402W ER3EKF1 402W MC0063 RC0603F R. 13100KL ER3EKF1 203W	Farnell  Farnell  Farnell  Farnell  Farnell	Mouser  Mouser  Mouser		2073349	NDS332 P 652- CR1206F X- 1004ELF MCT060 30C1002 FPS 660- RK73H11 TTD22R 603- RC0603- RC0678- RC1- RC1- RC2- RC3- RC3- RC3- RC3- RC3- RC3- RC3- RC3	RESC1274AN  RESC1274AN  RESC1274AN  RESC1274AN  RESC20727A  RESC1460BNGON  RESC1460BNGON  RESC1460BNGON	02 R14 R16, R28, R32 R32 R1, R3, R19, R20, R26, R39, R40 R17 R18 R29 R33
3 3 2 2	10k 11k3 15k 12k 14k 22k 22k	Res., TO CORR TN 2-251-2 TW 12-251-2 TW 12	MELWYN BOURTS MULTICOM PANASONI C PANASONI C MULTICOM PANASONI C VAGEO PANASONI C C	ERJSEYS  MCMR06312  REGENT  MCMR06  ANDOZFTL  ERJSEKF1  AOZV  ERJSEKF2  AOZV  ERJSEKF3  AOZV  ERJSEKF4  AOZV	Farnell  Farnell  Farnell  Farnell  Farnell	Mouser  Mouser  Mouser		9330844 2209107	NDS332 P 652- CR1206F X- 1004ELF MCT060 30C1002 FPS 660- RK73H11 TTD22R 603- RC0603- RC0678- RC1- RC1- RC2- RC3- RC3- RC3- RC3- RC3- RC3- RC3- RC3	RESC1216N  RESC1216N  RESC12016N  RESC102N  RESC100N  RESC100N  RESC100N  RESC100N  RESC100N  RESC100N  RESC100N	Q2 R14 R16, R28, R1, R3, R19, R20, R17 R18 R29 R33 R10 R21, R22 R2, R4, R.
3 3 7 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10k 11k3 15k 12k 14k 22k 22k	Box., DOSR TN, 2512 TM  Box., TM O.2009, TN  Box., TM O.2009, TN  Box., TOK O.063W TDV	WELWYN BOURTS MULTICOM P PANASONI C PANASONI C MULTICOM P YAGEO	ER2312 CR 1004ELF 1002FT 1004ELF 1002FT 1004ELF 1002FT 1004ELF	Farnell  Farnell  Farnell  Farnell  Farnell	Mouser  Mouser  Mouser		2073349	NDS332 P 652- CR1206F X- 1004ELF MCT060 30C1002 FPS 660- RK73H11 TTD22R 603- RC0603- RC0678- RC1- RC1- RC2- RC3- RC3- RC3- RC3- RC3- RC3- RC3- RC3	RESC1216N  RESC1216N  RESC12016N  RESC102N  RESC100N  RESC100N  RESC100N  RESC100N  RESC100N  RESC100N  RESC100N	Q2 R14 R16, R28, R1, R3, R19, R20, R17 R18 R29 R33 R10 R21, R22 R2, R4, R.
3 3 7 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10k 11k3 15k 12k 14k 22k 22k 100k	Sec., GOSR 196, 2512 2 196 2 1	MELWYN BOURTS MULTICOM PANASONI C PANASONI C MULTICOM PANASONI C VAGEO PANASONI C C	IR2312_ROSFW  CR1206-FW  CR1206-FW  CR1206-FW  MCARRO6 ST  ERJSEKF1 202V  ERJSEKF1 202V  MC0063  W060312 2R  RC0603F  RC	Farnell Farnell Farnell Farnell Farnell Farnell Farnell	Mouser  Mouser  Mouser		9330844 2073349	NDS332 P 652- CR1206F X- 1004ELF MCT060 30C1002 FPS 660- RK73H11 TTD22R 603- RC0603- RC0678- RC1- RC1- RC2- RC3- RC3- RC3- RC3- RC3- RC3- RC3- RC3	RESCENSION	O2 R14 R16, R28, R32 R1, R3, R19, R20, R26, R39, R17 R18 R29 R33 R10 R21, R22 R2, R4, R2 R34, R35
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10k 11k3 15k 12k 14k 22k 100k 100k 1100R	Box., DOER TN, 2312 TW.  Box., TM O. 2009, TN.  Box., TM O. 2009, TM.  Box., TM O. 2009, TM	MELWYN BOURTS MAULTICOM PANASONI C PANASONI C PANASONI C MULTICOM PANASONI C MULTICOM MULTICOM PANASONI MULTICOM PANASONI MULTICOM PANASONI MULTICOM	IRCS12-X- ROSEW CR1206-FX- 1004ELF MCMR06-SX- 1002FTL ERJSEKF1 202V ERJSEKF1 A02V MC0063 RC0603F- R- 1100KL ERJSEKF1 OD3V MC 0063W MC 100R ERJSEKF1 ERJSEKF2 ERJSEKF2 ERJSEKF2 ERJSEKF2 ERJSEKF2 ERJSEKF1 ERJSEKF1 ERJSEKF1	Farnell Farnell Farnell Farnell Farnell Farnell Farnell Farnell Farnell	Mouser  Mouser  Mouser		9330844 2073349	NDS332 P 652- CR1206F X- 1004ELF MCT060 30C1002 FPS 660- RK73H11 TTD22R 603- RC0603- RC0678- RC1- RC1- RC2- RC3- RC3- RC3- RC3- RC3- RC3- RC3- RC3	RESC1216N  RESC1216N  RESC12012N  RESC102N  RESC100N	O2 814 816, R28, R32 813, R3, R19, R20, R19, R20, R19, R20, R29, R40 817 818 829 823 810 824, R29 823 810 827, R22 827, R44, R35
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10k 11k3 15k 12k 14k 22k 100k 100k	Sec., COSS 19, 2312 1  Sec., 114 O 2004, 19, 100  Sec., 114 O 2004, 19, 100  Sec., 115 O COSS 19	MELWYPE  BOURS  MULTICOM  PANASONI  C  PANASONI  C  PANASONI  T  AMULTICOM  P  PANASONI  C  AMULTICOM  P  P  P  P  P  P  P  P  P  P  P  P  P	ER2312 (FE) (FE) (FE) (FE) (FE) (FE) (FE) (FE)	Farnell  Farnell  Farnell  Farnell  Farnell  Farnell  Farnell	Mouser  Mouser  Mouser		9330844 2073349	NDS332 P 652- CR1206F X- 1004ELF MCT060 30C1002 FPS 660- RK73H11 TTD22R 603- RC0603- RC0678- RC1- RC1- RC2- RC3- RC3- RC3- RC3- RC3- RC3- RC3- RC3	RESC140BN RESC1214AN RESC1214AN RESC12014AN RESC12012AN RESC140BNSGN RESC140BNSGN RESC140BNSGN RESC140BNSGN RESC140BNSGN RESC140BNSGN RESC140BNSGN RESC140BNSGN	O2 R14 R16, R28, R32 R19, R20, R19, R20, R17 R18 R29 R33 R10 R21, R22 R2, R4, R35
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10k 11k3 15k 12k 14k 22k 100k 100k 1100R	Beat, DOSR 196, 293-27  Beat, TAM O 2009, 196, 1030-1030-1030-1030-1030-1030-1030-1030	WELWYPK BOURTS MULTICOM PANASONI C PANASONI C PANASONI C MULTICOM P PANASONI C MULTICOM P PANASONI C C	IRCS12-X- ROSEW CR1206-FX- 1004ELF MCMR06-SX- 1002FTL ERJSEKF1 202V ERJSEKF1 A02V MC0063 RC0603F- R- 1100KL ERJSEKF1 OD3V MC 0063W MC 100R ERJSEKF1 ERJSEKF2 ERJSEKF2 ERJSEKF2 ERJSEKF2 ERJSEKF2 ERJSEKF1 ERJSEKF1 ERJSEKF1	Farnell Farnell Farnell Farnell Farnell Farnell Farnell Farnell Farnell	Mouser  Mouser  Mouser		9330844 2073349	NDS332 P 652- CR1206F X- 1004ELF MCT060 30C1002 FPS 660- RK73H11 TTD22R 603- RC0603- RC0678- RC1- RC1- RC2- RC3- RC3- RC3- RC3- RC3- RC3- RC3- RC3	RESC1216N  RESC1216N  RESC12012N  RESC102N  RESC100N	G2 R14 R16, R28, R32 R17, R3, R19, R20, R17, R18 R29 R33 R10 R21, R22 R2, R4, R2 R34 R35 R55 R5
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10k 11k3 12k 12k 14k 22k 100k 100k 100k 110k	Beat, DOSR TN, 2312 TM Beat, TM O.2007, TN Beat, TM O.2007, TM Bea	MELWYPE  BOURS  MULTICOM  PANASONI  C  PANASONI  C  PANASONI  T  AMULTICOM  P  PANASONI  C  AMULTICOM  P  P  P  P  P  P  P  P  P  P  P  P  P	ER23EX- IOO4ELF IOO4EL	Farnell  Farnell  Farnell  Farnell  Farnell  Farnell  Farnell  Farnell  Farnell	Mouser  Mouser  Mouser		9330844 2073349	NDS332 P 652- CST206F X- 1004ELF 594- MCT069 30C1002 FFS 0F 660- RX 7381171122R RC0603F RC0605F RC0605	RESC-12-16N RESC-12-16N RESC-12-16N RESC-12-16N RESC-12-16N RESC-12-16N RESC-16-16N RESC-16N RES	O2 R14 R16, R28, R22 R1, R3, R19, R20 R17 R18 R29 R33 R10 R21 R22 R2, R4, R2 R34, R35 R5 R7 R15
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10k 11k3 15k 12k 14k 12k 100k 100k 100k 100k 100k 100k	Beat, DOER TN, 293-27  The ST MA O 2009, TN  TO ST MA O 2009, TN  TO ST MA O 2009, TN  TO MA O 2009, T	MELWYPS  BOUTTS  MULTICOM PANASONI C  PANASONI C  MULTICOM PPANASONI C  PANASONI C	IRCS12-X- ROSEW CR1206-FY. 1004ELF MCMR06-X1002FTL ERJEKF1 602W MC00633 W060312 R. 13100KL ERJEKF1 003W MC0063-X- R. 13100KL ERJEKF1 402W MC0063-X- R. ERJEKF1 402W MC0063-X- R. ERJEKF1 403W MC063-X- R. ERJEKF1 403W MC0063-X- R	Farnell			9330844 2073349 9330844 2309107	NDS332 P 652- CR1206F X. 1004ELF 594- MCT060 30C1002 FFS 0F 667-ER1- 30GEV110 0V 71- TNEWMO604	NESC 14080N NESC 1271AN NESC 1271AN NESC 2012N NESC 2012N NESC 2012N NESC 2012N NESC 14080SON	Q2 R14 R16 P28 R29 R27 R26 R27
3 3 7 1 1 1 1 1 1 1 2 2 2 3	10k 11k3 15k 12k 14k 12k 100k 100k 100k 100k 100k 100k	Res., 1008 1% 2312 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	MELHYPR MULTICOM P PANASONI C PANASONI C MULTICOM P PPANASONI C MULTICOM P P PANASONI C MULTICOM P P P P P P P P P P P P P P P P P P P	ER2312-  CR1206- FX. 1004ELF MCMR06- KX1002FTL  ER23EKF1- 200V  ER23EKF2- 200V  ER23EKF1- 13100KL  ER23EKF1- 1330WL  ER23EKF1-  ER	Farnell			9330844 2073349 9330844 2309107	NDS332 P 662- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS 663- RC73H11 T1D22R 0F 603- RC0603F R13100KL	NESC 14080N NESC 1271AN NESC 1271AN NESC 2012N NESC 2012N NESC 2012N NESC 2012N NESC 14080SON	O2 R14 R16, R28, R32 R17, R32 R19, R20 R17 R18 R29 R33 R10 R21, R22 R2, R4, R R34, R35 R5 R7 R15 R8
3 3 7 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	198 1193 128 128 128 128 129 129 129 129 129 129 129 129 129 129	Sec., GOSR 196, 293-22  Sec., 150 COSR 196, 293-22  Sec., 150 COSR 297, 196, 196, 196, 196, 196, 196, 196, 196	MELWYPS  BOUTTS  MULTICOM PANASONI C  PANASONI C  MULTICOM PPANASONI C  PANASONI C	IRCS12-CANDENS OF THE PROPERTY	Farnell		0.033 €	1100065 2073349 9330844 2309107 9331689 8L	NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS 660- RX 73H717212 FR 7171212 FR 713100KL 11100KL	RESC 14080N RESC 1	O2 R14 R16, R28, R28 R17, R20 R17, R20 R19, R20, R20 R17, R18 R29 R33 R10 R21, R22 R2, R4, R R34, R35 R7 R15 R8 R12 R22, R25, R25 R24, R35 R5 R7 R15 R8
3 3 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	100 100 100 100 100 100 100 100 100 100	Sec., COSS 196, 293-21  Sec., 11M O 2009, 196, 1200  Sec., 11M O 2009, 196, 196, 196, 196, 196, 196, 196, 19	MELWYPS  BOUTTS  MULTICOM PANASONI C  PANASONI C  MULTICOM PPANASONI C  PANASONI C	IRCS12-CANDENS OF THE PROPERTY	Farnell		0.033 €	1100065 2073349 9330844 2309107 9331689 8L	NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS 660- RX 73H717212 FR 7171212 FR 713100KL 11100KL	RESCHARGESON RESCHARGE RESCHARGE RESCHARGE RESCHARGE RESCHARGE RESCHARGESON	O2 R14 R16, R28, R32 R1, R3, R18, R20, R17, R3, R18, R33 R10 R21, R22 R2, R4, R5 R7 R15 R8 R12 R22, R4, R5 R8 R12 R22, R23, R25
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	198 1193 128 128 128 128 129 129 129 129 129 129 129 129 129 129	Sec., COSE 196, 293.21  Sec., 134 O.2009, 196, 1300  Sec., 130 O.0009, 1300  Sec.,	MELWYPS  BOUTTS  MULTICOM PANASONI C  PANASONI C  MULTICOM PPANASONI C  PANASONI C	IRCS12-CANDENS OF THE PROPERTY	Farnell		0.033 €	1100065 2073349 9330844 2309107 9331689 8L	NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS 660- RX 73H717212 FR 7171212 FR 713100KL 11100KL	RESC 14080N RESC 1	02 R14 R16, R28, R32 R3,
3 3 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	196 193 193 193 193 193 193 193 193 193 193	Sec. 1008 19 2312 19 20 20 20 20 20 20 20 20 20 20 20 20 20	MELWYPS  BOUTTS  MULTICOM PANASONI C  PANASONI C  MULTICOM PPANASONI C  PANASONI C	IRCS12-CANDENS OF THE PROPERTY	Farnell		0.033 €	1100065 2073349 9330844 2309107 9331689 8L	NDS332 P 652- CR1206F X- 1004ELF 594- MCT060 30C1002 FPS 660- RX 73H717212 FR 7171212 FR 713100KL 11100KL	RESC1274AN  RESC1274AN  RESC1274AN  RESC1274AN  RESC1274AN  RESC20727N  RESC140BNSON	02 R14 R16, R28, R32 R1, R3, R32 R1, R3, R34, R35 R5 R7 R15 R12 R23, R35 R5 R7 R15
3 3 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	106 1113 158 128 128 146 129 1200 1000 1000 1100 1100 1100 100 100 1	Beat, 1008 196 2932 2  The ST 100 COST 196 2932 2  Beat, 100 COST 196 2032 1  Beat, 110 COST 196 2032	MELWYPS  BOUTTS  MULTICOM PANASONI C  PANASONI C  MULTICOM PPANASONI C  PANASONI C	IRCS12-CANDENS OF THE PROPERTY	Farnell		0.033 €	1100065 2073349 9330844 2309107 9331689 8L	NDS332 P 652-CR1206F K D411-F MCT040 R RC73411 TTD22R RC7341-T T1D22R RC0402F RS T3411 T1022R R00402F R13100K R00402F	RESC1274AN  RESC1274AN  RESC1274AN  RESC1274AN  RESC1274AN  RESC20727N  RESC140BNSON	02 R14 R16, R28, R32 R1, R3, R32 R1, R3, R34, R35 R5 R7 R15 R12 R23, R35 R5 R7 R15
3 3 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	108 1113 128 128 128 128 129 1200 1000 1000 1000 1000 1000 1000	Res., 100 Ft 19, 2512 T 19  Res., 11M 0.20W, 11s  Res., 11M 0.20W, 11s  Res., 10K 0.063W 70V  Res., 10K 0.063W 70V  Res., 110K 0.063W 70W 70V  Res., 110K 0.063W	MELWYN  BULITIO  MULTICOM  PANASONI  PANASONI  MULTICOM  PANASONI  C  MULTICOM  PANASONI  C  MULTICOM  PANASONI  C  MULTICOM  PANASONI  MULTICOM  MULTICOM	IRCS12-CANDENS OF THE PROPERTY	Farnell  Farnell		0.033 €	93316844 93316844 9331689 9331689 9331689 9331689	NDS332 P 652-CR1206F 1004EF 1004EF MCT060 MCT060 RK73H11 TTD22R RT RT RC0603F 13100KL	RESC1274AN  RESC146BNSON  RESC146BNS	02 8114 R116, F228, R128 R137 R118 R129 R138 R10 R137 R118 R29 R133 R10 R21, R22 R21, R22 R33, R35 R5 R7 R15 R8 R7 R15 R8 R17 R18 R19 R22, R4, R2 R34, R35 R5 R7 R19 R10 R112 R23, R25 R6 R112 R12 R12 R12 R13 R10 R112 R12 R13 R10 R112 R13 R10
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	108 1113 108 108 108 108 108 1008 1008 1	Sec. 1008 19 2312 19 20 20 20 20 20 20 20 20 20 20 20 20 20	MELWYPS  BOUTTS  MULTICOM PANASONI C  PANASONI C  MULTICOM PPANASONI C  PANASONI C	INCRIDATE  CRI 206- MCMR06  MCMR06  MCMR06  ERJIEKF1  BOMELF  BERJIEKF1	Farnell		0.033 €	1100065 2073349 9330844 2309107 9331689 8L	NDS332 P 652-CR1206F 652-CR1206F ND04ELF 576-MCT040 NCT040	RESC-12-16N  RESC-	02 814 816, R28, R29 819, R20, R37 818 819, R20, R37 818 810 821, R22 82, R4, R3 811 811 821, R22 82, R4, R3 815 816 821, R22 82, R4, R3 815 817 815 818 817 819 819 810 810 810 811 811 811 811 811 811 811

ntity				Manufact	Manuf acturer Part Numbe	Suppli	Suppli		Suppli er Part Numb	Suppli or Part Numb	
	LogicalDesignator  C3, C4, C5, C6, C7, C8, C9, C12, C16, C21, C19, C15, C36, C19, C41, C45, C46, C48, C49	Comment	Description  MLCC, 0.1uF 16V	urer	00603 C104K 4RACT	er 1	er 2 Mous	Note	er 1	er 2 50- C0603 C104K 4R	Footprint
19	C29, C35, C36, C39, C41, C45, C46, C48, C49 C20, C32, C23, C24, C30, C31, C32, C33, C51,	100nf	10% X7R 0603 MLCC, X7R, 100NF, 630V,	KEMET	U C1808 V104K BRACT	farret	or Moun	0,041 €	1081	4R 20852	CD603V
10	C52	100n/	1808	KEMET	U	-	er			09	C1808
8	C10, C18, C19, C27, C37, C40, C42, C44	tuf	MLCC, 0805, XSR, 16V, 1UF	MULTIC OMP	MCCA0 00596	farnet	Mous		17594 79	V3080 SV105 ZX3CB C S94-	CAPC2012N
7	R1, R3, R19, R20, R26, R39, R40	100	Res, 10K 0.063W	MULTIC OMP	MCMR 06X100 2FTL	farnel	Mous	0.033.6	20733	MCT0 603DC 1000F 85	DESCIADIN
6	R9, R11, R27, R31, R37, R38	75R	75V 1% 0603 75R 5% 1206 1/4W							77 - V3060	RESC3216N
4	C17, C38, C43, C47	10p/	MLCC, 0603, NPO, 50V, 10PF	MULTIC OMP	MCCA0	farnet	Mous		17590	JA100 JXACB C	CAPC1608N
			NPO, 50V, 10PF MICROCHIP MCP6001T-I/OT Operational Amplifier, Single, 1 Amplifier, 1								
		MCP6001T				farnet			49749		
4	UR, U10, U12, U13	TOV	1.8V to 6V, SOT- 23, 5 Pirs. BEAD SMID EMI FERRITE 0805 220 OHM	Marin	BLM21 PG221 SN1D				92		50-G5/X 45
Г					CR120 6-FX- 1004EL					652- CR120 6FX- 1004E	
3	R16, R28, R32	1M2	Res, 1M 0.25W, 1% 1206	Sourns	F RCD60		Mous	0,041 €		1004E LF 603- RC060	RESC3216N
3	R2, R4, R24	100k	Res, 100K 5% 0603 1/10W MLCC, 18 pF, s 5%, COG / NPO,	YAGEO	3FR- 13100K L	farnet	Mous er		23091	3FR- 13100 KL	RESC160BN
2	c1, c2	18př	50 V, 0603 MLCC 0.47uf 50V	MULTIC OMP	MC060 3N1803 500CT GRM21	farnet			17590 56		CAPC1608N
2	C14, C15	0.47uf, 47uf	, 0805., MLCC,47uF,50V, 0805,	MURATA	9R6GJ1 DAKE 19 D GRM32	farnel		s	18457 38		CAPC2012N
2	C26, C50	42uf	MLCC, 1210, 47uf, 16V Right angle	MURATA	ER61C 476ME 15L	farnet			17355		CAPC3225N
	Control in Donne in	4 Pin	socket corn 2.54mm single								DA Sankal A sin
2	Control_out, Power_out	HDR 4 Pin	Right angle haeder 2.54 mm single row								RA HDR 4Pin
2	D3, D6	STTH1R06 A	DIODE, fast, 1A, 600V, SMD, SMA	ST Microele ctronics	STTH1 ROSA	farnet			16975	***	DO-214AC
2	D7, PWR	кРНСМ- 20125GC- Т	LED, SMD, 0805, GREEN, 25mcd	KINGBRI GHT	M- 2012S GC-T	farnel	Mous er	93	16860	LTST- C171C KT	00005
2	D8, D9	BASS16	DIODE, SWITCHING, SOD-523 BEAD FERRITE,	NXP Wurth	BASS 16	farnet	Ĺ		87344 02	710-	500623
2	14,16	FB 1.5k 1A	SMD 15000HM, 0805 Res. 11K 5%	Wurth Elektroni k	742792 097	farnet	Mous		16357 38	710- 74279 2097	LL0805\2P
2	R17, R18	11k3, 15k	0905 1/8W		MC00s		H			660- RK 73H	RESC2012N
2	R21, R22	22R	Res, 22R 5% 0603 1/10W	MULTIC	3W060 3122R	farnel	Mous		93308 44	1JTTD 2290F	RESC160BN
	R23, R25	160	Res, thin film 1k ±5% 0.1W 0603	MULTIC	MCMR 06X102 JTL	farnel	Mous	0,083 €	20733	71- INPW 06031 KOODE EA	RESCIEGEN
2	R20, R26	30k	25% 0.1W 0603 30K 5% 0603 1/10W Res, 100K, 0603 (1608 metric)		Ė		H	, and W		Ė	RESC1608N
2	R34, R35	100k	(1608 metric), ±1%, 100mW, 75V 6N136.SDM	PANASO NIC	ERJ3EX F1003V	farnet					RESC1608XSQN
			Optocoupler, Transistor Output, 1								
2	U8, U11	6N136	Channel, Surface Mount DIP, 8 Plns, 50 mA, 5 kV, 19 %			TME			6N136 S-L		SMP-8
П					GRM31 CR71C 106KA	fare	M		18457	81- GRM3 1CR71 C106K	
1	C13	10uf	MLCC, 10uf, 16V, 10%, 1206 MLCC, 680PF, 630V, CDG/NPO,	MURATA MULTIC	C7L MC120 6N681J	farnel	er	1,53 €	72 18753	AC7	CAPC3216N
1	C28	stOpf	1206 MLCC, 10HF, 630V, ± 10% X7R, 1206	OMP	631CT C3216 X7R2J1 03M		L		90	810- C3216 X7R2J 103M	CAPC3216N
1	C34	nonf PMEG3010	X7R, 1206	TDK	PMEG3	SAMP	er	0,182 €		103M	CAPC3216N
1	02	ER SMAIRONA	1A, 30V TVS DIODE, 400W, 300V, DO- 2144C	NXP LITTELFU	DIDER SMAI3 DOA	LE farnet	Mous		16774	576- 5MAJ3 00A	S00123 DO-214AC
1	05	BAV99	214AC DIODE SW DUAL 75V 350HW SOT23		BAV99- 7-F	farnet	Mous er	0,289 €	18436	621- 8AV99 f	SOT95P240-3L
			CONNECTOR, 4 way, HEADER, THT, 3.96MM, 10A.250		BIP- VH(LF)(	Farnel	Diai-		94920	455- 1641-	
1	HV_Out	HDR 4 USB MINI-	Vac USB MINI-B SMT	ST	SN) 675031	farnet	Key Mous	5	20 11253	ND 538- 67503-	HDR1X4_3M96V
1	15	S TOUH	INDUCTOR, SMD, 4X4MM, 10UH, 20%	Murata	LOH44 PN100 MPGL	farnel	er		17828 01	1020	MOLEX,MIN,USS-8
		B4B-XH-A	Wire to Board Header, THT, 2.5MM, 4WAY		B4B-XH A (LF)(SN	farnel	Digiko		15162 78	455- 2249- ND	
Ħ	N.W	7-215079-	Micro-match SOCKET, TOP ENTRY, 4WAY	TE Connecti	7- 215079	farnet			14850	100	ane area
_1	92	4	MOSFET 100V,	vity	4			5	7		2P-T4
lĪ		200 100 100 100	72.4 mOhm		PSMN0						
	01	PSMN069- 100YS	standard level, LFPAK P-Channel Logic Level	NXP FARCHL D	PSMN0 69- 100YS	Farnel			18456 629L		SOT669
_1	Q1 Q2	PSMN069- 3001'S NDS332P	standard level, LFPAK P-Channel Logic Level Enhancement Morte Field	NXP FARCHE D SEMICO NDUCTO R	69-	Farnel Farnel	Mous		18456 6291. 14710 69	512- NDS3 32P	SOT069 SOT23-3L
	O2 02	PSMN069- 300YS NDS332P	standard level, LFPAK P-Channel Logic Level Enhancement	D SEMICO	69- 100YS NDS33	Farnel Farnel Farnel	Mous		629L	32P	SOT669 SOT23-3L BESC1608X50N
1	22 22 810	PSMN069- 100YS NDS332P 22k	standard level, LFPAK LFPAK Po-Chainnel Logic Level Enhancement Mode Filed Effect Translator Res, 25K, C603 a 15k, 100mW, 75V Res, 0603 0.1W 5%	D SEMICO NDUCTO R PANASO	69- 100YS ND533 29 ERUSEK	Farnel  Farnel  Farnel  I	Mous er Mous		629L	512- NDS3 32P 667- ERJ- 3GEYJ 100V	SOT669 SOT23-3L RESC1608NSON
1 1 1	97 22 972 973	PSMM069- 100YS NDS332P 22k 10R	standard level, LEPAK LEPAK PC-Channel Logic Level Enhancement Mode Field Effect Translator Res, 22K, 000 (1608 me/kic), a 1%, 100enW, 25V Res, 0603 0.1W 5% 22R 5% 1206 1/4W	D SEMICO NDUCTO R PANASO NIC	69- 100YS NIDS33 29 ERJ3EK F2202V ERJ- 3GEY31 00V	Farnel  Farnel  Farnel	Mous er Mous se		14710 69 20595 48	32P 667- ERJ-	SOT669 SOT23-3L RESC1608NSON RESC1608N
1 1 1	22 22 25 26 26 27 27 27 27 27 27 27 27 27 27 27 27 27	PSMN069- 300YS NDS332P 22k 30R 22R 0805	Standard Invest, LPPMK P-Channel Logic Level Tonahors resent Model Pilde Transhold Riss, 226, 0603 (1008 molvis), a 15s, 100mW, 27V Res, 0603 0.1W 505 Res, 0603 0.1W	D SEMICO NDUCTO R PANASO NIC PANASO NIC	6/9- 100YS NIDS33 2P ERJ3EK F2202V ERJ- 3GE111 00V LR3512 R05FW	farnel I farnel I farnel I farnel	Mous er Mous er	9	629L 14710 69	32P 667- ERJ-	50723-3L 85 521 6083/50N 85 521 6083/50N 85 523216N 85 5264324N
1 1 1 1	22 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	PSAMNOOP 100YS ND5332P 22k 10R 22R 0R05	standard level, LPPMK P-Channel Logic Level Infratro-sment Model Field Model Field Franshtor Res, 225, 0603 (1003 me/six), 31%, 300mW, 797  Res, 0603 0.1W Res, 0605 0.0W R	D SEMICO NDUCTO R PANASO NIC	69- 100YS ND533 2P ERJSEX F220ZY ERJ- 3GF01 00V LR2512 R05FW ERJSEX F1403Y	farnel I farnel I farnel I farnel I farnel	Mous er Mous er	s	14710 69 20595 48	32P 667- ERJ-	SOT669 SOT23-3L RESC160BNSON RESC3216N RESC3216N
1 1 1	22 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	PSAMNOOD 1001YS NDS3332P 22R 30R 22R 0805	shandard level, LIPPAK P.Chaweel Logic Level Lovel Ershancement Model Field Bitter, 17 (1998) Bitter, 200, 1003 Bitter, 2003 Bitter, 200, 1003 Bitter, 200, 1003 Bitter, 200,	D SEMICO NDUCTO R PANASO NIC PANASO NIC	69- 100YS NDS33 2P ERJSEX F220ZV ERJ- 3GF01 00V	Farnel  Farnel  Farnel  Farnel  Farnel  Farnel	Mous er Mous er	s	14710 69 20595 48	32P 667- ERJ-	507569 50722-3 85571608050N 85571608050N 85571608050N 85571608050N
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1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	22 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	PSI/MODPS  NDS:332P  228  208  208  30805  140k  128  30605	Standard Invol.  LPRIME P. Channel Logic Levi March Control Res 225, 5003  Standard Standard Logic Levi Model Fishel Res 225, 5003  (1000 merkl)  201  Res, 0003 (1000 merkl)  201  Res, 1000 merkl)	D SEMICO NODICTO R  PANAGO NAC  PANAGO NAC  PANAGO NAC  PANAGO NAC  PANAGO NAC	69- 100YS NDS33 2P ERJSEK F220ZY ERJ- SGF01 OOV LR2512 R05FW ERJSEK F140ZY ERJSEK F140ZY MC	farnel  farnel  farnel  farnel  farnel  farnel	Mous or Mous or	3	14710 69 20595 48	32P 667- ERJ-	10/16/89 10/17/2-78 10
1 1 1 1 1 1 1	22 22 22 22 22 22 22 22 22 22 22 22 22	PSI/M2007- 300VS  NDS3332P  228  308  228  30825  142k  142k  148  2006  1184	Standard Invol. LTPWAK In Chamber Logic In Chamber Logic In Chamber Logic Involved Logic Involve	D SEMICO NOLITO R PANASO NIC  PANASO PANASO NIC	69- 100YS  NDS33 2P  ERJSEK F220ZY  ERJSEK F140ZY  ERJSEK F140ZY  MC 0.0633 5% 100R  ERJSEK ERJSEK F140ZY	Farnel  Farnel  Farnel  Farnel  Farnel  Farnel  Farnel  Farnel	Mous er Mous er	3	6298. 14710 60 20595 48 11000 65	32P 667- ERJ-	RESCIGORNSON RESCIGORN
1 1 1 1 1 1	222 222 273 273 273 273 274 275 275 275 275 275 275 275 275 275 275	PSMN209-1500YS NIDS332P 228 208 228 208 228 2408 1408 1408 1408 158 160 1108	Standard Invol.  LPRIME IN COMMISSION OF COM	D SEMECO NOUCTO RE PANASO NOC PAN	69- 100YS NDS33 2P ERJSEK F220ZV ERJ- 200Y ERJ- 200Y ERJ- 200Y ERJSEK F140ZV MC 0.0633 5% 100R ERJSEK F1183V ERJSEK F1183V	Farnel  Farnel  Farnel  Farnel  Farnel  Farnel  Farnel  Farnel	Mous or Mous or	s	6298. 14710 60 20595 48 11000 65	32P 667- ERJ-	RESCIEGRASION RESCIEGRA
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	22 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	PSAMPLED- 100YS  NIOS-332P  220  308  22R  0803  1408  158  1008  346  1108  2098  EFD200	Secretaria Post   Por Comment Liquid     Comment Liquid    Comment Liquid     Comment Liquid     Comment Liquid     Comment Liquid     Comment Liquid     Comment Liquid     Comment Liquid     Comment Liquid      Comment Liquid       Comment Liquid	D SEMICO NOLICIO R  PANASO NIC	69- 100YS NDS33 NDS33 P ERJEK F220ZY ERJSC R05FW ERJSC	Farnel	Mous er Mous er	s	6298. 14710 60 20595 48 11000 65	32P 667- ERJ-	RESCIGORNSON RESCIGORN
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	225 226 237 237 237 237 237 237 237 237 237 237	118k 200k EFD20 STMCDF10 20016 MAXX11612 EUA MAXX010FE UR-11 TUY70433	Southerland Land Community (1997) (19	D SEMECO NODECTO PANASO NOC NOC NOC NOC NOC NOC NOC NOC NOC NO	69- 100YS NDS33 2P ERJEK 1720ZY 1R2- 1720ZY 1R2- 1720ZY 1R2- 1720ZY 1720	Furnit Fu	Motors or	5	2000 2000 2000 2000 2000 2000 2000 200	32P 667- ERJ-	85 SC 14085099  85 SC 14085099
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	225 22 22 23 24 24 25 25 25 25 25 25 25 25 25 25 25 25 25	118k 200k EFD20 STMCDF10 20016 MAXX11612 EUA MAXX010FE UR-11 TUY70433	Southerland (1) A Company (1)	D SEMECO NODECTO PANASO NOC NOC NOC NOC NOC NOC NOC NOC NOC NO	69- 100YS NDS33 2P ERJEK 1720ZY 1R2- 1720ZY 1R2- 1720ZY 1R2- 1720ZY 1720	farnel	Money	5	2000 2000 2000 2000 2000 2000 2000 200	32P 667- ERJ-	85 SC 14085099  85 SC 14085099
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**Design Rules Verification Report**Filename: D:\Aurimo\altium\SE\_HV\_Power\_supply\SE-HV00-02.PcbDoc

Warnings 4 Rule Violations 500

Warnings	
Design contains shelved or modified (but not repoured) polygons. The result of DRC is not correct. Recommended to	4
Total	4

Rule Violations	
Clearance Constraint (Gap=0.2mm) (All),(All)	45
Clearance Constraint (Gap=0.1mm) (All),(InNetClass('HV'))	0
Clearance Constraint (Gap=0.1mm) (All),(InNetClass('HV1'))	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( (All) )	107
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.1mm) (Max=3mm) (Preferred=0.254mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=3mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	70
Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)	192
Silk to Silk (Clearance=0.254mm) (All),(All)	32
Net Antennae (Tolerance=0mm) (All)	54
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Silk primitive without silk layer	0
Total	500

Design contains shelved or modified (but not repoured) polygons. The result of DRC is not correct. Recommende
Polygon named: Top Layer-No Net In net GND On Top Layer
Polygon named: Top Layer-No Net In net GND On Bottom Layer
Polygon named: Top Layer-No Net In net GND On Top Layer
Polygon named: Top Layer-No Net In net GND On Bottom Layer

Clearance Constraint (Gap=0.2mm) (AII),(AII)
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.4mm,59.775mm)(75.4mm,62.3mm) on Top Layer And Pad U4-1(74.9mm,62.3mm) on
Cleparance Constraint: (0.195mm < 0.2mm) Between Pad U4-2(75.4mm,62.3mm) on Top Layer And Pad U4-1(74.9mm,62.3mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U4-3(75.9mm,62.3mm) on Top Layer And Pad U4-2(75.4mm,62.3mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,58.875mm)(75.9mm,62.3mm) on Top Layer And Pad U4-2(75.4mm,62.3mm) on
© Rearance Constraint: (0.198mm < 0.2mm) Between Track (73.125mm,62.3mm) (74.9mm,62.3mm) on Top Layer And Pad U4-2(75.4mm,62.3mm) on
© Parance Constraint: (0.195mm < 0.2mm) Between Pad U4-4(76.4mm,62.3mm) on Top Layer And Pad U4-3(75.9mm,62.3mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.4mm,59.775mm)(75.4mm,62.3mm) on Top Layer And Pad U4-3(75.9mm,62.3mm) on
© Exparance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,58.875mm) (75.9mm,62.3mm) on Top Layer And Pad U4-4(76.4mm,62.3mm) on
©Parance Constraint: (0.195mm < 0.2mm) Between Pad U4-8(75.9mm,66.4mm) on Top Layer And Pad U4-9(75.4mm,66.4mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,66.4mm) (75.9mm,67.665mm) on Top Layer And Pad U4-9(75.4mm,66.4mm) on
Cleparance Constraint: (0.195mm < 0.2mm) Between Pad U4-7(76.4mm,66.4mm) on Top Layer And Pad U4-8(75.9mm,66.4mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (76.4mm,66.4mm) (76.4mm,69.125mm) on Top Layer And Pad U4-8(75.9mm,66.4mm) on
©Parance Constraint: (0.195mm < 0.2mm) Between Pad U4-6(76.9mm,66.4mm) on Top Layer And Pad U4-7(76.4mm,66.4mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (75.9mm,66.4mm)(75.9mm,67.665mm) on Top Layer And Pad U4-7(76.4mm,66.4mm) on
Teparance Constraint: (0.198mm < 0.2mm) Between Track (76.9mm,66.4mm) (78.46mm,66.4mm) on Top Layer And Pad U4-7(76.4mm,66.4mm) on Top
Clearance Constraint: (0.198mm < 0.2mm) Between Track (76.4mm,66.4mm)(76.4mm,69.125mm) on Top Layer And Pad U4-6(76.9mm,66.4mm) on
© Parance Constraint: (0.196mm < 0.2mm) Between Track (71.175mm,89.15mm)(71.179mm,89.154mm) on Top Layer And Pad
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.179mm,89.154mm)(71.179mm,91.221mm) on Top Layer And Pad
Clearance Constraint: (0.124mm < 0.2mm) Between Track (74.175mm,80.2mm)(75.438mm,78.938mm) on Top Layer And Pad
Uleara(new C5mstma8rit)(50mstma
Clearance Constraint: (0.195mm < 0.2mm) Between Track (132.91mm,52.075mm)(134.2mm,52.075mm) on Top Layer And Pad
<b>切を3(43分を200m 5(24)57(00.11)</b> 5mm < 0.2mm) Between Pad U5-5(134.2mm,51.575mm) on Top Layer And Pad U5-4(134.2mm,52.075mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Track (132.91mm,52.075mm)(134.2mm,52.075mm) on Top Layer And Pad
<b>២៤-៦/(สิ่งชื่อ20งท.รุ่มใสโตวี25(00.11)</b> 5mm < 0.2mm) Between Track (138.3mm,53.075mm)(138.975mm,53.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-9(138.3mm,53.075mm) on Top Layer And Pad U5-10(138.3mm,53.575mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-8(138.3mm,52.575mm) on Top Layer And Pad U5-9(138.3mm,53.075mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,53.575mm)(138.3mm,55.175mm) on Top Layer And Pad
Ute a/(138:30ms/fia) from sina find 5(0.719) 8mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Pad
<b>២៤៩/(៨និសិទ្ធ Corn នាំសិស្ត្រ (០.7)</b> 5mm < 0.2mm) Between Pad U5-7(138.3mm,52.075mm) on Top Layer And Pad U5-8(138.3mm,52.575mm) on Top Layer
Clearance Constraint: (0.195mm < 0.2mm) Between Track (138.3mm,53.075mm)(138.975mm,53.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Track (138.3mm,52.075mm)(143.275mm,52.075mm) on Top Layer And Pad
Clearance Constraint: (0.195mm < 0.2mm) Between Pad U5-6(138.3mm,51.575mm) on Top Layer And Pad U5-7(138.3mm,52.075mm) on Top Layer
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Pad
<b>២៤-៧(ដល់ខ-១០០០១៨)</b> 5mm < 0.2mm) Between Track (138.3mm,52.075mm)(143.275mm,52.075mm) on Top Layer And Pad
Clearance Constraint: (0.12mm < 0.2mm) Between Track (59.95mm,89.255mm)(59.95mm,94.4mm) on Bottom Layer And Pad
<mark>ይሎ a(án</mark> ራኔ² <b>com s</b> rifain 75.025mm) on Bottom Layer And Pad
Clearance Constraint: (0mm < 0.2mm) Between Area Fill (153.025mm,57.1mm) (153.58mm,57.9mm) on Bottom Layer And Pad
Qfe-5a(45de3\tau_5mm_a5de3\tau_5mm_a5de3\tau_5mm_6mm < 0.2mm) Between Track (71.675mm,89.15mm)(71.675mm,92.4mm) on Top Layer And Track
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.675mm,89.15mm)(71.675mm,92.4mm) on Top Layer And Track
Clearance Constraint: (0.196mm < 0.2mm) Between Track (71.675mm,89.15mm)(71.675mm,92.4mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,53.075mm)(138.975mm,53.075mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (140.15mm,52.575mm)(140.85mm,53.275mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Track
Clearance Constraint: (0.198mm < 0.2mm) Between Track (138.3mm,52.575mm)(140.15mm,52.575mm) on Top Layer And Track

## Un-Routed Net Constraint ((All)) Un-Routed Net Constraint: Net +12V Between Pad Power\_in-1(108.2mm,61.5mm) on Multi-Layer And Via (110.825mm,61.375mm) from Top Layer to Un-Routed Net Constraint: Net +12V Between Via (110.825mm,61.375mm) from Top Layer to Bottom Layer And Track Un-Routed Net Constraint: Net GND Between Pad C50-1(136.325mm,55.175mm) on Bottom Layer And Pad C27-2(136.4mm,55.2mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad U13-3(126.05mm,99.15mm) on Top Layer And Pad U13-2(127mm,99.15mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad U12-2(118.2mm,99.275mm) on Top Layer And Pad U12-3(119.15mm,99.275mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad U10-2(127mm,92.925mm) on Top Layer And Pad U10-3(127.95mm,92.925mm) on Top Layer Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Track Un-Routed Net Constraint: Net GND Between Via (119.4mm,106.45mm) from Top Layer to Bottom Layer And Pad C39-1(119.4mm,107.575mm) on Top Un-Routed Net Constraint: Net GND Between Pad C29-2(131.475mm,50.8mm) on Top Layer And Pad R15-1(132.675mm,50.485mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad C42-2(113.9mm,99.675mm) on Top Layer And Pad C45-1(115.55mm,99.825mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad C38-1(124.55mm,110.425mm) on Top Layer And Pad R29-1(126.325mm,110.215mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad C40-2(122.925mm,92.925mm) on Top Layer And Pad C41-1(124.65mm,93.225mm) on Top Layer Un-Routed Net Constraint: Net GND Between Via (122.525mm,91.275mm) from Top Layer to Bottom Layer And Pad C40-2(122.925mm,92.925mm) on Un-Routed Net Constraint: Net GND Between Pad U5-5(134.2mm,51.575mm) on Top Layer And Via (135.725mm,52.175mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Pad C18-2(139.025mm,56.375mm) on Bottom Layer And Pad C19-2(141.25mm,56.375mm) on Bottom Lay-Routed Net Constraint: Net GND Between Pad C37-2(117.6mm,107.125mm) on Top Layer And Via (119.4mm,106.45mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Pad C32-1(141.375mm,106.825mm) on Bottom Layer And Via (143.275mm,107.175mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (143.275mm,107.175mm) from Top Layer to Bottom Layer And Pad C24-1(145.175mm,106.825mm) or Un-Routed Net Constraint: Net GND Between Pad C44-2(122.95mm,98.3mm) on Top Layer And Pad C46-1(124.65mm,97.725mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad C33-1(141.375mm,103.775mm) on Bottom Layer And Via (143.275mm,103.35mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (155.85mm,51.675mm) from Top Layer to Bottom Layer And Via (155.85mm,54.15mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (153.375mm,49.05mm) from Top Layer to Bottom Layer And Via (155.85mm,49.05mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (155.85mm,54.15mm) from Top Layer to Bottom Layer And Via (155.85mm,56.625mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (155.85mm,56.625mm) from Top Layer to Bottom Layer And Via (155.85mm,59.1mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (144.6mm,56.1mm) from Top Layer to Bottom Layer And Via (147.075mm,56.1mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Pad U6-2(123.45mm,77.975mm) on Bottom Layer And Pad C35-1(123.525mm,75.575mm) on Bottom Layer Un-Routed Net Constraint: Net GND Between Pad C30-1(141.375mm,95.075mm) on Bottom Layer And Via (143.275mm,95.7mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Pad C43-1(124.175mm.99.8mm) on Top Layer And Pad U13-3(126.05mm.99.15mm) on Top Layer Un-Routed Net Constraint: Net GND Between Via (143.275mm,95.7mm) from Top Layer to Bottom Layer And Pad C22-1(145.175mm,95.075mm) on Un-Routed Net Constraint: Net GND Between Pad Power\_in-3(108.2mm,66.58mm) on Multi-Layer And Pad Power\_in-4(108.2mm,69.12mm) on Will Reliable to Net Constraint: Net GND Between Via (119.4mm,106.45mm) from Top Layer to Bottom Layer And Via (122.975mm,105.45mm) from Top Layer Un-Routed Net Constraint: Net GND Between Pad R10-1(134.1mm,55.61mm) on Top Layer [Unplated] And Pad C50-1(136.325mm,55.175mm) on Bottom Un-Routed Net Constraint: Net GND Between Via (135.725mm,52.175mm) from Top Layer to Bottom Layer And Pad C34-1(135.9mm,49.6mm) on Top Un-Routed Net Constraint: Net GND Between Pad U6-2(123.45mm,77.975mm) on Bottom Layer And Via (125.775mm,78.4mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Pad C19-2(141.25mm,56.375mm) on Bottom Layer And Pad C21-1(142.375mm,54.45mm) on Top Layer Un-Routed Net Constraint: Net GND Between Via (143.275mm,95.7mm) from Top Layer to Bottom Layer And Pad C23-1(145.175mm,98.2mm) on Bottom Un-Routed Net Constraint: Net GND Between Pad C31-1(141.375mm,98.2mm) on Bottom Layer And Via (143.275mm,95.7mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Via (126.3mm,113.375mm) from Top Layer to Bottom Layer And Pad R29-1(126.325mm,110.215mm) on Un-Routed Net Constraint: Net GND Between Via (143.275mm,95.7mm) from Top Layer to Bottom Layer And Via (143.275mm,99.525mm) from Top Layer Un-Routed Net Constraint: Net GND Between Pad C26-2(135.675mm,57.775mm) on Top Layer And Pad C50-1(136.325mm,55.175mm) on Bottom Layer Un-Routed Net Constraint: Net GND Between Via (125.775mm,74.575mm) from Top Layer to Bottom Layer And Via (125.775mm,78.4mm) from Top Layer Un-Routed Net Constraint: Net GND Between Pad U6-6(121.05mm,78.925mm) on Bottom Layer And Via (125.775mm,78.4mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Pad U2-3(119.4mm,71.425mm) on Bottom Layer And Pad C7-1(121.95mm,72.275mm) on Bottom Layer Un-Routed Net Constraint: Net GND Between Pad C8-1(116.825mm,72.275mm) on Bottom Layer And Pad U2-3(119.4mm,71.425mm) on Bottom Layer Un-Routed Net Constraint: Net GND Between Via (143.275mm,88.05mm) from Top Layer to Bottom Layer And Via (146.85mm,88.05mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (139.7mm,88.05mm) from Top Layer to Bottom Layer And Via (143.275mm,88.05mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (125.775mm,70.75mm) from Top Layer to Bottom Layer And Via (125.775mm,74.575mm) from Top Un-Routed Net Constraint: Net GND Between Via (143.275mm,88.05mm) from Top Layer to Bottom Layer And Via (143.275mm,91.875mm) from Top Un-Routed Net Constraint: Net GND Between Via (130.775mm,59.275mm) from Top Layer to Bottom Layer And Via (130.775mm,63.1mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (125.775mm,55.45mm) from Top Layer to Bottom Layer And Via (125.775mm,59.275mm) from Top Un-Routed Net Constraint: Net GND Between Via (155.85mm,63.1mm) from Top Layer to Bottom Layer And Via (155.85mm,66.925mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (155.85mm,70.75mm) from Top Layer to Bottom Layer And Via (155.85mm,74.575mm) from Top Layer

### Un-Routed Net Constraint ( (All) )

Un-Routed Net Constraint: Net GND Between Via (155.85mm,82.225mm) from Top Layer to Bottom Layer And Via (155.85mm,86.05mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (130.775mm,70.75mm) from Top Layer to Bottom Layer And Via (130.775mm,74.575mm) from Top Un-Routed Net Constraint: Net GND Between Via (155.85mm,74.575mm) from Top Layer to Bottom Layer And Via (155.85mm,78.4mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (130.775mm,78.4mm) from Top Layer to Bottom Layer And Via (130.775mm,82.225mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (125.775mm,63.1mm) from Top Layer to Bottom Layer And Via (125.775mm,66.925mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (130.775mm,63.1mm) from Top Layer to Bottom Layer And Via (130.775mm,66.925mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (128.65mm,51.875mm) from Top Layer to Bottom Layer And Pad C29-2(131.475mm,50.8mm) on Top Un-Routed Net Constraint: Net GND Between Pad HV\_Out-2(141.32mm,114.3mm) on Multi-Layer And Pad HV\_Out-3(145.28mm,114.3mm) on Multi-Layer Un-Routed Net Constraint: Net GND Between Pad D5-1(151.725mm,60mm) on Bottom Layer And Via (155.85mm,59.1mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Via (113.875mm,105.75mm) from Top Layer to Bottom Layer And Via (119.4mm,106.45mm) from Top Layer Un-Routed Net Constraint: Net GND Between Pad R14-1(141.575mm,50mm) on Bottom Layer And Pad C21-1(142.375mm,54.45mm) on Top Layer Un-Routed Net Constraint: Net GND Between Pad U13-2(127mm,99.15mm) on Top Layer And Via (128.275mm,103.5mm) from Top Layer to Bottom Layer Un-Routed Net Constraint: Net GND Between Pad T1-8(143.275mm,82.225mm) on Multi-Layer And Via (143.275mm,88.05mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Pad R17-1(128.25mm,86.025mm) on Bottom Layer And Via (130.775mm,82.225mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Pad C52-1(174.784mm,39.352mm) on Top Layer And Pad C51-1(181.559mm,39.352mm) on Top Layer Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Pad U11-7(151.25mm,92.525mm) on Top Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Pad R33-1(157.35mm,98.31mm) on Top Un-Routed Net Constraint: Net GND Between Pad Free-0(92.6mm,65.7mm) on Multi-Layer And Pad Power\_in-3(108.2mm,66.58mm) on Multi-Layer Un-Routed Net Constraint: Net GND Between Pad Free-0(89.575mm,81.3mm) on Multi-Layer And Pad Control\_in-1(108.2mm,82.325mm) on Multi-Layer Un-Routed Net Constraint: Net GND Between Pad U8-7(118.35mm,119.175mm) on Top Layer And Via (119.4mm,106.45mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Pad C20-1(163.225mm,106.2mm) on Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Pad U12-2(118.2mm,99.275mm) on Top Un-Routed Net Constraint: Net GND Between Pad C27-2(136.4mm,55.2mm) on Top Layer And Via (136.725mm,52.975mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Pad C46-1(124.65mm,97.725mm) on Top Layer And Via (128.275mm,103.5mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Via (128.65mm,51.875mm) from Top Layer to Bottom Layer And Via (135.725mm,52.175mm) from Top Un-Routed Net Constraint: Net GND Between Via (155.85mm, 49.05mm) from Top Layer to Bottom Layer And Via (155.85mm, 51.675mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (122.525mm,91.275mm) from Top Layer to Bottom Layer And Pad U10-2(127mm,92.925mm) on Top Un-Routed Net Constraint: Net GND Between Via (143.275mm.103.35mm) from Top Layer to Bottom Layer And Via (143.275mm.107.175mm) from Top Un-Routed Net Constraint: Net GND Between Pad C45-1(115.55mm,99.825mm) on Top Layer And Via (118.15mm,95.525mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Pad C19-2(141.25mm,56.375mm) on Bottom Layer And Via (144.6mm,56.1mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Via (125.775mm,66.925mm) from Top Layer to Bottom Layer And Via (125.775mm,70.75mm) from Top Un-Routed Net Constraint: Net GND Between Via (155.85mm,66.925mm) from Top Layer to Bottom Layer And Via (155.85mm,70.75mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (143.275mm,91.875mm) from Top Layer to Bottom Layer And Via (143.275mm,95.7mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (155.85mm,78.4mm) from Top Layer to Bottom Layer And Via (155.85mm,82.225mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (130.775mm,74.575mm) from Top Layer to Bottom Layer And Via (130.775mm,78.4mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (125.775mm,59.275mm) from Top Layer to Bottom Layer And Via (125.775mm,63.1mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (130.775mm,66.925mm) from Top Layer to Bottom Layer And Via (130.775mm,70.75mm) from Top Un-Routed Net Constraint: Net GND Between Pad C7-1(121.95mm,72.275mm) on Bottom Layer And Via (125.775mm,70.75mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Via (122.975mm,105.45mm) from Top Layer to Bottom Layer And Via (126.3mm,113.375mm) from Top Un-Routed Net Constraint: Net GND Between Pad HV\_Out-2(141.32mm,114.3mm) on Multi-Layer And Via (143.275mm,107.175mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Pad Free-0(92.6mm,65.7mm) on Multi-Layer And Via (125.775mm,66.925mm) from Top Layer to Bottom Un-Routed Net Constraint: Net GND Between Via (155.85mm,49.05mm) from Top Layer to Bottom Layer And Pad C52-1(174.784mm,39.352mm) on Top Un-Routed Net Constraint: Net GND Between Via (136.725mm,52.975mm) from Top Layer to Bottom Layer And Via (144.6mm,56.1mm) from Top Layer Id Un-Routed Net Constraint: Net GND Between Via (143.275mm,99.525mm) from Top Layer to Bottom Layer And Via (143.275mm,103.35mm) from Top Un-Routed Net Constraint: Net GND Between Via (155.85mm,59.1mm) from Top Layer to Bottom Layer And Via (155.85mm,63.1mm) from Top Layer to Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Via (128.275mm,103.5mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (118.15mm,95.525mm) from Top Layer to Bottom Layer And Via (122.525mm,91.275mm) from Top Un-Routed Net Constraint: Net GND Between Via (125.775mm,66.925mm) from Top Layer to Bottom Layer And Via (130.775mm,66.925mm) from Top Un-Routed Net Constraint: Net GND Between Via (122.975mm,105.45mm) from Top Layer to Bottom Layer And Via (128.275mm,103.5mm) from Top Un-Routed Net Constraint: Net GND Between Via (125.775mm,55.45mm) from Top Layer to Bottom Layer And Via (128.65mm,51.875mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (122.525mm,91.275mm) from Top Layer to Bottom Layer And Via (125.775mm,78.4mm) from Top Layer Un-Routed Net Constraint: Net GND Between Via (147.075mm,56.1mm) from Top Layer to Bottom Layer And Via (155.85mm,56.625mm) from Top Layer

### Un-Routed Net Constraint ((All))

Un-Routed Net Constraint: Net GND Between Via (146.85mm,88.05mm) from Top Layer to Bottom Layer And Via (155.85mm,86.05mm) from Top Layer to Un-Routed Net Constraint: Net NetC30\_2 Between Pad R16-2(126.475mm,91.2mm) on Bottom Layer And Pad D6-2(131.5mm,86.35mm) on Bottom Layer Un-Routed Net Constraint: Net NetC30\_2 Between Pad D6-2(131.5mm,86.35mm) on Bottom Layer And Pad C30-2(136.675mm,95.075mm) on Bottom

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Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C44-1(122.95mm,96.6mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad C43-1(124.175mm,99.8mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C40-1(122.925mm,94.625mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.206mm < 0.254mm) Between Via (116.35mm,98.5mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C42-1(113.9mm,101.375mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.238mm < 0.254mm) Between Pad R35-2(117.91mm,103.05mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Pad R35-1(119.29mm,103.05mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.149mm < 0.254mm) Between Via (121.3mm,105.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.227mm < 0.254mm) Between Via (121.3mm,105.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.138mm < 0.254mm) Between Via (122.975mm,105.45mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.247mm < 0.254mm) Between Pad C15-2(73.1mm,62.325mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.013mm < 0.254mm) Between Via (76.625mm,79mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-4(58mm,111.6mm) on Top Layer And Pad J4-5(58mm,110.8mm) on Top
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-3(58mm,112.4mm) on Top Layer And Pad J4-4(58mm,111.6mm) on Top
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-2(58mm,113.2mm) on Top Layer And Pad J4-3(58mm,112.4mm) on Top
Minimum Solder Mask Sliver Constraint: (0.25mm < 0.254mm) Between Pad J4-1(58mm,114mm) on Top Layer And Pad J4-2(58mm,113.2mm) on Top
Minimum Solder Mask Sliver Constraint: (0.234mm < 0.254mm) Between Pad U1-36(70.175mm,89.15mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.203mm < 0.254mm) Between Via (70.6mm,91.525mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.148mm < 0.254mm) Between Pad U1-36(70.175mm,89.15mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.253mm < 0.254mm) Between Pad R7-2(74.31mm,67.85mm) on Top Layer And Pad
Mildiril (74.65) toler, 6/Pat & 18 in 8 in Wer Constraint: (0.253 mm < 0.254 mm) Between Pad R7-1 (75.69 mm. 67.85 mm) on Top Laver And Pad
Midira (76. Stottler, Maskirshver Constraint: (0.197mm < 0.254mm) Between Pad C37-1(117.6mm, 108.825mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.212mm < 0.254mm) Between Via (119.4mm,106.45mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-5(66.3mm,108.825mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-4(65.35mm,108.825mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-2(66.3mm,111.525mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-1(67.25mm,111.525mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.123mm < 0.254mm) Between Pad R2-2(66.125mm,82.35mm) on Top Layer And Pad
Manifer Mask Manager And Pad U1-47(68.875mm,82.85mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.123mm < 0.254mm) Between Pad U1-48(68.875mm,82.35mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.104mm < 0.254mm) Between Via (68.525mm,80.1mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.17mm < 0.254mm) Between Pad U1-1(70.175mm,81.05mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.149mm < 0.254mm) Between Via (77.65mm,67.825mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C14-1(76.6mm,59.475mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.229mm < 0.254mm) Between Via (73.1mm,60.75mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C15-1(73.1mm,64.025mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.153mm < 0.254mm) Between Pad C29-2(131.475mm, 50.8mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.153mm < 0.254mm) Between Pad C29-1(131.475mm,52.3mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.217mm < 0.254mm) Between Pad C29-2(131.475mm,50.8mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.253mm < 0.254mm) Between Pad C28-1(132.3mm,56.975mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Pad C28-1(132.3mm,56.975mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.227mm < 0.254mm) Between Pad C26-2(135.675mm,57.775mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C27-1(138.1mm,55.2mm) on Top Layer And Pad C27-2(136.4mm,55.2mm)
Minimum Solder Mask Sliver Constraint: (0.222mm < 0.254mm) Between Via (140.45mm,59.025mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.198mm < 0.254mm) Between Via (139.675mm,54.425mm) from Top Layer to Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.108mm < 0.254mm) Between Pad P2-1(59.95mm,89.255mm) on Multi-Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-6(119.55mm,96.95mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-7(119.55mm,97.6mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-8(119.55mm, 98.25mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-3(124.05mm,96.95mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-2(124.05mm,97.6mm) on Bottom Layer And Pad
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## Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All) Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U14-1(124.05mm,98.25mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.098mm < 0.254mm) Between Via (115.375mm, 73.75mm) from Top Layer to Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.233mm < 0.254mm) Between Pad R20-2(119.125mm,75.125mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.222mm < 0.254mm) Between Pad R20-2(119.125mm,75.125mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-5(121.05mm,77.975mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-4(121.05mm,77.025mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-2(123.45mm,77.975mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U6-1(123.45mm,78.925mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.087mm < 0.254mm) Between Via (126.5mm,84.425mm) from Top Layer to Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.062mm < 0.254mm) Between Via (126.5mm,84.425mm) from Top Layer to Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad U3-2(66.475mm,62.6mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad U3-3(66.475mm,61.65mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C10-1(64.15mm,65.275mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C19-1(141.25mm,58.075mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad C18-1(139.025mm,58.075mm) on Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.162mm < 0.254mm) Between Via (140.825mm.46.8mm) from Top Layer to Bottom Layer And Pad Minimum Solder Mask Sliver Constraint: (0.202mm < 0.254mm) Between Via (121.3mm,105.425mm) from Top Layer to Bottom Layer And Via Minimum Solder Mask Sliver Constraint: (0.191mm < 0.254mm) Between Via (140.85mm,53.275mm) from Top Layer to Bottom Layer And Via

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Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Arc (128.5mm,99.325mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Arc (125.5mm, 92.75mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Arc (116.7mm,99.1mm) on Top Overlay And Pad
Bill紀 Tid 1937 전출대서 화용 연구 출자하는 Constraint: (0.225 mm < 0.254 mm) Between Arc (120.275 mm, 106.95 mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Arc (68.1mm,111.825mm) on Top Overlay And Pad L7-2(68.95mm,111.8mm)
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Arc (75.262mm,75.734mm) on Top Overlay And Pad C4-1(74.925mm,76mm)
Stilk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Arc (136.225mm,54.15mm) on Top Overlay And Pad C27-2(136.4mm,55.2mm)
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Area Fill (149.525mm,105.525mm) (149.925mm,106.325mm) on Top Overlay
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,98.925mm)(128.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,97.225mm)(128.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,98.925mm)(125.575mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,97.225mm)(125.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,97.225mm)(128.5mm,97.225mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,97.225mm)(128.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,97.225mm)(125.575mm,97.225mm) on Top Overlay And Pald
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,97.225mm)(125.5mm,98.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,93.15mm)(125.575mm,93.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,93.15mm)(125.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,93.15mm)(128.5mm,93.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,93.15mm)(128.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (125.5mm,94.85mm)(125.575mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (125.5mm,93.15mm)(125.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (128.425mm,94.85mm)(128.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (128.5mm,93.15mm)(128.5mm,94.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.18mm < 0.254mm) Between Text "C41" (125.125mm,90.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.134mm < 0.254mm) Between Text "C40" (119.325mm,92.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "C47" (116.01mm,105.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.16mm < 0.254mm) Between Text "C45" (116.25mm,101.935mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (116.7mm,99.5mm)(116.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (116.7mm,99.5mm)(116.775mm,99.5mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (119.7mm,99.5mm)(119.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (119.625mm,99.5mm)(119.7mm,99.5mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (116.7mm,101.2mm)(116.775mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (116.7mm,99.5mm)(116.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (119.625mm,101.2mm) (119.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (119.7mm,99.5mm)(119.7mm,101.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.126mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (120.275mm,107.35mm)(120.35mm,107.35mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (120.275mm,107.35mm)(120.275mm,109.05mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (123.2mm,107.35mm)(123.275mm,107.35mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (123.275mm,107.35mm)(123.275mm,109.05mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (120.275mm,109.05mm)(120.35mm,109.05mm) on Top Overlay And
Bakl To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (120.275mm,107.35mm)(120.275mm,109.05mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Track (123.2mm,109.05mm)(123.275mm,109.05mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (123.275mm,107.35mm)(123.275mm,109.05mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (63.311mm,78.87mm)(64.2mm,78.87mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (63.311mm,78.87mm)(63.311mm,79.505mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (65.089mm,78.87mm)(65.089mm,79.505mm) on Top Overlay And Pald
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (64.2mm,78.87mm)(65.089mm,78.87mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (63.635mm,82.775mm) (64.765mm,82.775mm) on Top Overlay And
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Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (63.311mm,82.451mm)(63.635mm,82.775mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (63.311mm,81.861mm) (63.311mm,82.451mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (65.089mm,81.861mm) (65.089mm,82.451mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (64.765mm,82.775mm)(65.089mm,82.451mm) on Top Overlay And
BARTo Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (60mm,106.05mm)(60mm,118.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (60mm,106.05mm)(60mm,118.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "C16" (74.125mm,69.775mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.084mm < 0.254mm) Between Text "R7" (72.45mm,68.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "C16" (74.125mm,69.775mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.025mm < 0.254mm) Between Track (60mm,106.05mm) (60mm,118.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.254mm) Between Text "C47" (116.01mm,105.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Text "C38" (125.25mm,111.175mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (59.045mm,103.111mm) (59.68mm,103.111mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (59.045mm,104.889mm) (59.68mm,104.889mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (59.68mm,104.889mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Track (59.68mm,103.111mm)(59.68mm,104mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (55.775mm,103.435mm)(56.099mm,103.111mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (56.099mm,103.111mm) (56.688mm,103.111mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.176mm < 0.254mm) Between Track (56.099mm,104.889mm)(56.688mm,104.889mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.124mm < 0.254mm) Between Track (55.775mm.104.565mm)(56.099mm.104.889mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (55.775mm,103.435mm)(55.775mm,104.565mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Area Fill (137mm,105.525mm) (137.4mm,106.325mm) on Top Overlay And
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,99.375mm)(62.825mm,99.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,98.575mm)(62.825mm,98.575mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,99.375mm)(62.825mm,99.375mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.425mm,98.575mm)(62.825mm,98.575mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.125mm,94.375mm)(129.125mm,94.775mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.925mm,94.375mm)(129.925mm,94.775mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.125mm,94.375mm)(129.125mm,94.775mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.925mm,94.375mm)(129.925mm,94.775mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.092mm < 0.254mm) Between Text "R31" (145.625mm,107.2mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.15mm,98.15mm)(129.15mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.95mm,98.15mm)(129.95mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.15mm,98.15mm)(129.15mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (129.95mm,98.15mm)(129.95mm,98.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,62.85mm)(77.425mm,62.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm) (77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (74.325mm,65.85mm)(77.425mm,65.85mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,104.6mm)(62.975mm,104.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,105.4mm)(62.975mm,105.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,104.6mm)(62.975mm,104.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (62.575mm,105.4mm)(62.975mm,105.4mm) on Top Overlay And Pad
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Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.184mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (65.725mm,83mm)(65.725mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (66.525mm,83mm)(66.525mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (65.725mm,83mm)(65.725mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (66.525mm,83mm)(66.525mm,83.4mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (68.925mm,89.875mm) (69.725mm,89.875mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.059mm < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.235mm < 0.254mm) Between Track (72.038mm,63.736mm) (72.038mm,65.539mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.235mm < 0.254mm) Between Track (67.162mm,63.736mm) (72.038mm,63.736mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Text "C27" (138.85mm,54.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (134.75mm,51.05mm)(134.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.055mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (131.075mm,51.55mm)(131.875mm,51.55mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "R10" (133.625mm,54.45mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (136.35mm,54.15mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (134.75mm.54.15mm)(136.1mm.54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (136.35mm,54.15mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Track (137.75mm,51.05mm)(137.75mm,54.15mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "C27" (138.85mm,54.925mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm) (108.52mm,91.165mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm)(108.52mm,91.165mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm)(108.52mm,91.165mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,81.045mm)(108.52mm,91.165mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,81.04mm)(95.535mm,91.22mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,88.025mm)(65.473mm,88.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,88.025mm)(65.473mm,88.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.145mm < 0.254mm) Between Track (58.205mm,89.222mm) (58.805mm,89.222mm) on Top Overlay And
Bald To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,93.025mm)(65.473mm,93.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.049mm < 0.254mm) Between Track (64.809mm,89.222mm)(64.809mm,91.828mm) on Top Overlay And
Bakl To Solder Mask Clearance Constraint: (0.05mm < 0.254mm) Between Track (64.809mm,91.828mm)(65.505mm,91.828mm) on Top Overlay And Pald
Silk To Solder Mask Clearance Constraint: (0.23mm < 0.254mm) Between Track (58.205mm,93.025mm)(65.473mm,93.025mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (0.089mm < 0.254mm) Between Text "R7" (72.45mm,68.6mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (108.52mm,60.22mm)(108.52mm,70.34mm) on Top Overlay And Pad
Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And
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# Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All) Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And Bald To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And Bald To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (95.535mm,60.215mm)(95.535mm,70.395mm) on Top Overlay And Bald To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay Still To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay 81N To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay 81N To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (120.175mm,95.775mm)(120.175mm,98.75mm) on Bottom Overlay Bitkl To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay Still To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay 81N To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay 81N To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Track (123.425mm,95.775mm)(123.425mm,98.75mm) on Bottom Overlay 81N To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Text "R20" (118.325mm,74.375mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.075mm < 0.254mm) Between Text "R20" (118.325mm,74.375mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.134mm < 0.254mm) Between Text "C35" (126.775mm,75.7mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Text "C7" (122.525mm,71.975mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.059mm < 0.254mm) Between Text "C35" (126.775mm,75.7mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R20" (118.325mm,74.375mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (125.875mm,82mm)(127.075mm,82mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (125.875mm,82mm)(127.075mm,82mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (127.35mm,85.425mm) (127.35mm,86.625mm) on Bottom Overlay And BARTO Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Track (127.35mm,85.425mm)(127.35mm,86.625mm) on Bottom Overlay And BBN To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm.94.825mm)(61.15mm.94.825mm) on Bottom Overlay And Page Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm,94.025mm) (61.15mm,94.025mm) on Bottom Overlay And Page Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm,94.825mm)(61.15mm,94.825mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Track (60.75mm,94.025mm)(61.15mm,94.025mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Text "R6" (74.2mm,65.475mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Track (64.5mm,64.2mm) (66.3mm,64.2mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Text "C19" (142.975mm.54.5mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (139.725mm,58.55mm) (147.95mm,58.55mm) on Bottom Overlay And Bald To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (139.725mm,58.55mm)(139.725mm,62.125mm) on Bottom Overlay And Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Text "C18" (138.35mm,59mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Track (139.725mm,58.55mm) (147.95mm,58.55mm) on Bottom Overlay And BBIX To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Text "C33" (135.8mm,103.225mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.251mm < 0.254mm) Between Text "R12" (148.55mm,54.075mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Text "R12" (148.55mm,54.075mm) on Bottom Overlay And Pad Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Text "R14" (147.775mm,46.575mm) on Bottom Overlay And Pad

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# Silk to Silk (Clearance=0.254mm) (AII),(AII) Silk To Silk Clearance Constraint: (0.251mm < 0.254mm) Between Text "C41" (125.125mm,90.275mm) on Top Overlay And Arc (125.5mm,92.75mm) on Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Arc (69.475mm,81.05mm) on Top Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "C46" (125.85mm,99.25mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "HV\_Out" (132.9mm,118.675mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "HV Out" (132.9mm,118.675mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.032mm < 0.254mm) Between Text "L5" (67.3mm,58.5mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "C16" (74.125mm,69.775mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.064mm < 0.254mm) Between Text "C36" (60.25mm,105.875mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.201mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.201mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "R25" (61.375mm,103.275mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.222mm < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.222mm < 0.254mm) Between Text "C3" (68.25mm,81.25mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.225mm < 0.254mm) Between Text "C12" (81.475mm,63.5mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.185mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.185mm < 0.254mm) Between Text "C15" (71.575mm,65.85mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.234mm < 0.254mm) Between Text "R10" (133.625mm,54.45mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.225mm < 0.254mm) Between Text "R10" (133.625mm,54.45mm) on Top Overlay And Track Silk To Silk Clearance Constraint: (0.057mm < 0.254mm) Between Text "C7" (122.525mm,71.975mm) on Bottom Overlay And Track Silk To Silk Clearance Constraint: (0.2mm < 0.254mm) Between Text "U3" (66.375mm,59.65mm) on Bottom Overlay And Track Silk To Silk Clearance Constraint: (0.249mm < 0.254mm) Between Text "Q1" (148.825mm,57.175mm) on Bottom Overlay And Track Silk To Silk Clearance Constraint: (0.249mm < 0.254mm) Between Text "C18" (138.35mm,59mm) on Bottom Overlay And Track Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "R12" (148.55mm,54.075mm) on Bottom Overlay And Track Silk To Silk Clearance Constraint: (0.175mm < 0.254mm) Between Text "X1" (68.525mm,75.8mm) on Top Overlay And Text "C2" (67.275mm,74.525mm Silk To Silk Clearance Constraint: (0.148mm < 0.254mm) Between Text "R7" (72.45mm.68.6mm) on Top Overlay And Text "C16" (74.125mm.69.775mm Silk To Silk Clearance Constraint: (0.21mm < 0.254mm) Between Text "C43" (124.8mm,102.325mm) on Top Overlay And Text "R34" Silk To Silk Clearance Constraint: (0mm < 0.254mm) Between Text "R35" (120.275mm,103.425mm) on Top Overlay And Text "R34" Silk To Silk Clearance Constraint: (0.1mm < 0.254mm) Between Text "U13" (126.05mm,100.5mm) on Top Overlay And Text "C46" (125.85mm,99.25mm Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R14" (147.775mm,46.575mm) on Bottom Overlay And Text "R13"

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (110.875mm,63.975mm) from Top Layer to Bottom Layer
Net Antennae: Via (118.15mm,95.525mm) from Top Layer to Bottom Layer
Net Antennae: Via (122.975mm,105.45mm) from Top Layer to Bottom Layer
Net Antennae: Via (126.3mm,113.375mm) from Top Layer to Bottom Layer
Net Antennae: Via (113.875mm,105.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (128.275mm,103.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (119.4mm,106.45mm) from Top Layer to Bottom Layer
Net Antennae: Via (122.525mm,91.275mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,59.275mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,63.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (128.65mm,51.875mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,63.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,59.275mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,55.45mm) from T op Layer to Bottom Layer
Net Antennae: Via (143.275mm,107.175mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,103.35mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,99.525mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,95.7mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,88.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (143.275mm,91.875mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,78.4mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,74.575mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,66.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (125.775mm,70.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (126.5mm,84.425mm) from Top Layer to Bottom Layer
Net Antennae: Via (110.825mm,61.375mm) from Top Layer to Bottom Layer
Net Antennae: Via (110.075mm,72.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,82.225mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,86.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,78.4mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,63.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,66.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,74.575mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,70.75mm) from T op Layer to Bottom Layer
Net Antennae: Via (130.775mm,70.75mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,66.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,74.575mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,78.4mm) from Top Layer to Bottom Layer
Net Antennae: Via (130.775mm,82.225mm) from Top Layer to Bottom Layer
Net Antennae: Via (146.85mm,88.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (139.7mm,88.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (153.375mm,49.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,49.05mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,51.675mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,54.15mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,56.625mm) from Top Layer to Bottom Layer
Net Antennae: Via (155.85mm,59.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (147.075mm,56.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (144.6mm,56.1mm) from Top Layer to Bottom Layer
Net Antennae: Via (139.675mm,54.425mm) from Top Layer to Bottom Layer
Net Antennae: Track (118.55mm,97.1mm)(119.05mm,97.6mm) on Bottom Layer
Net Antennae: Track (108.155mm,78.025mm)(121.05mm,78.025mm) on Bottom Layer
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Net Antennae (Tolerance=0mm) (All)
Net Antennae: Track (107.975mm,75.305mm)(109.695mm,77.025mm) on Bottom Layer

Net Antennae: Track (108.215mm,84.85mm)(110.3mm,84.85mm) on Bottom Layer