

FIG. 5-6. Input impedance vs. frequency. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.

### **EXAMPLE 5-1—Cont**

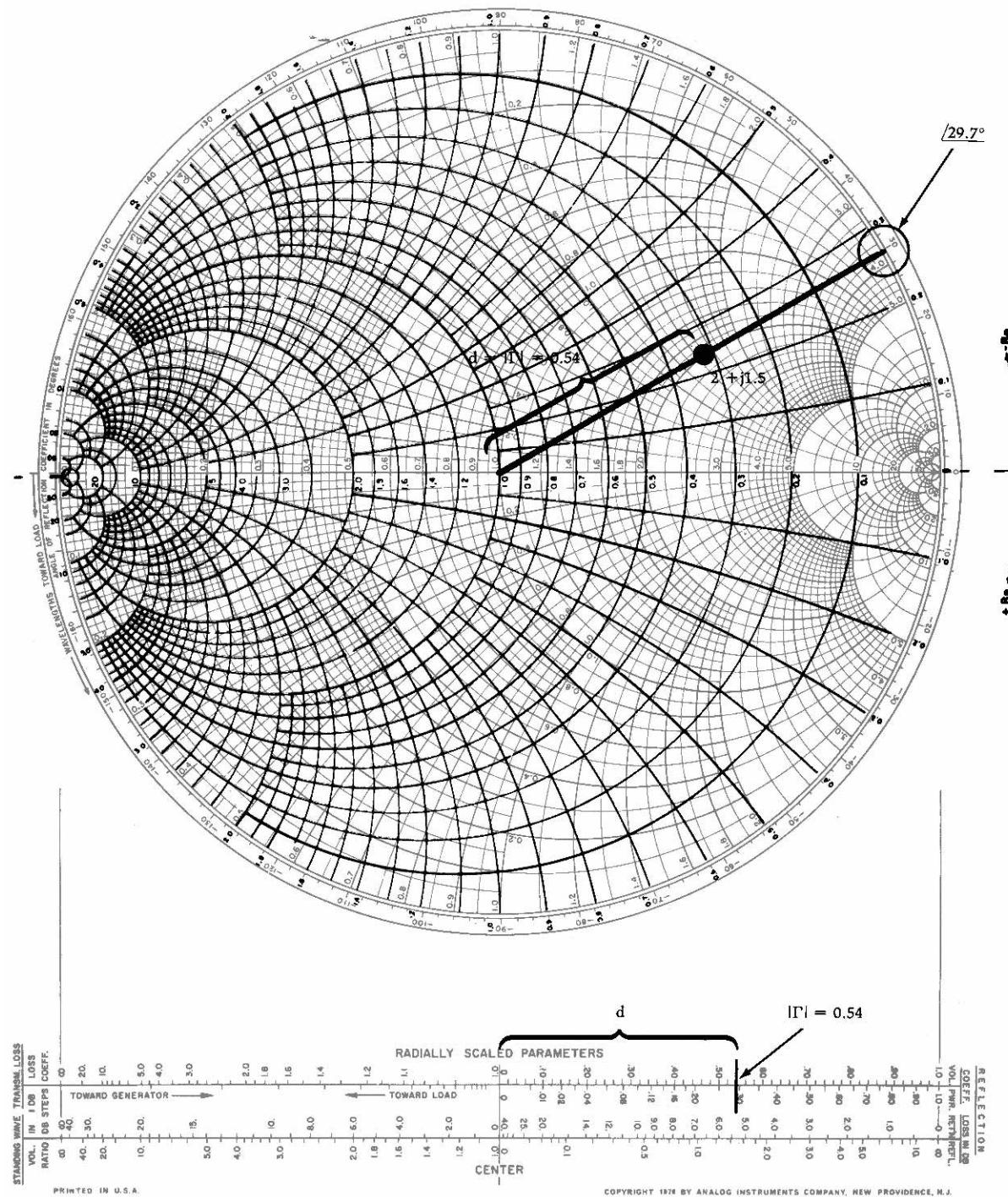


FIG. 5-15. Solution to Example 5-1. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.

## EXAMPLE 6-4—Cont

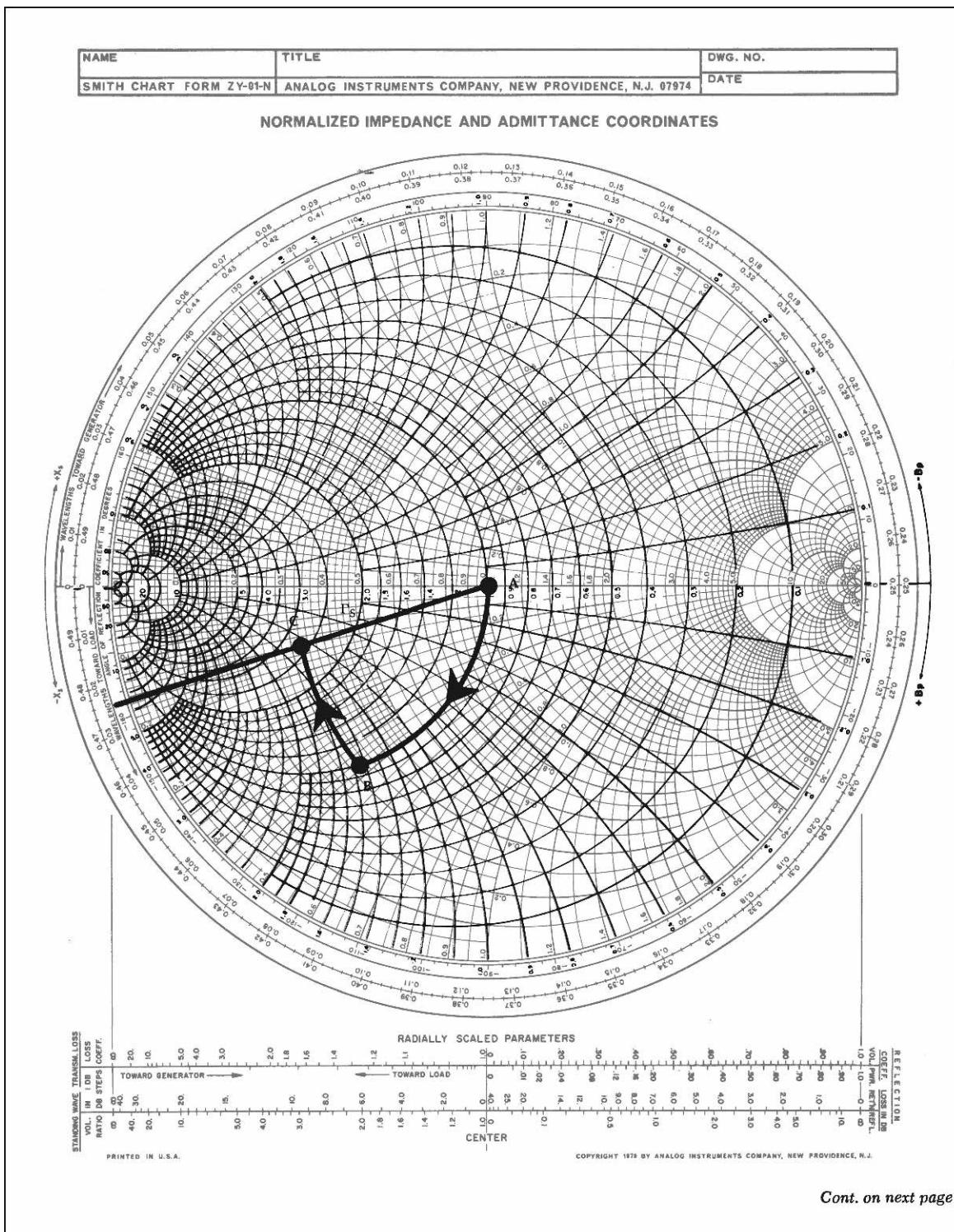


FIG. 6-20. Input network-design values for Example 6-4. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.

## EXAMPLE 6-4—Cont

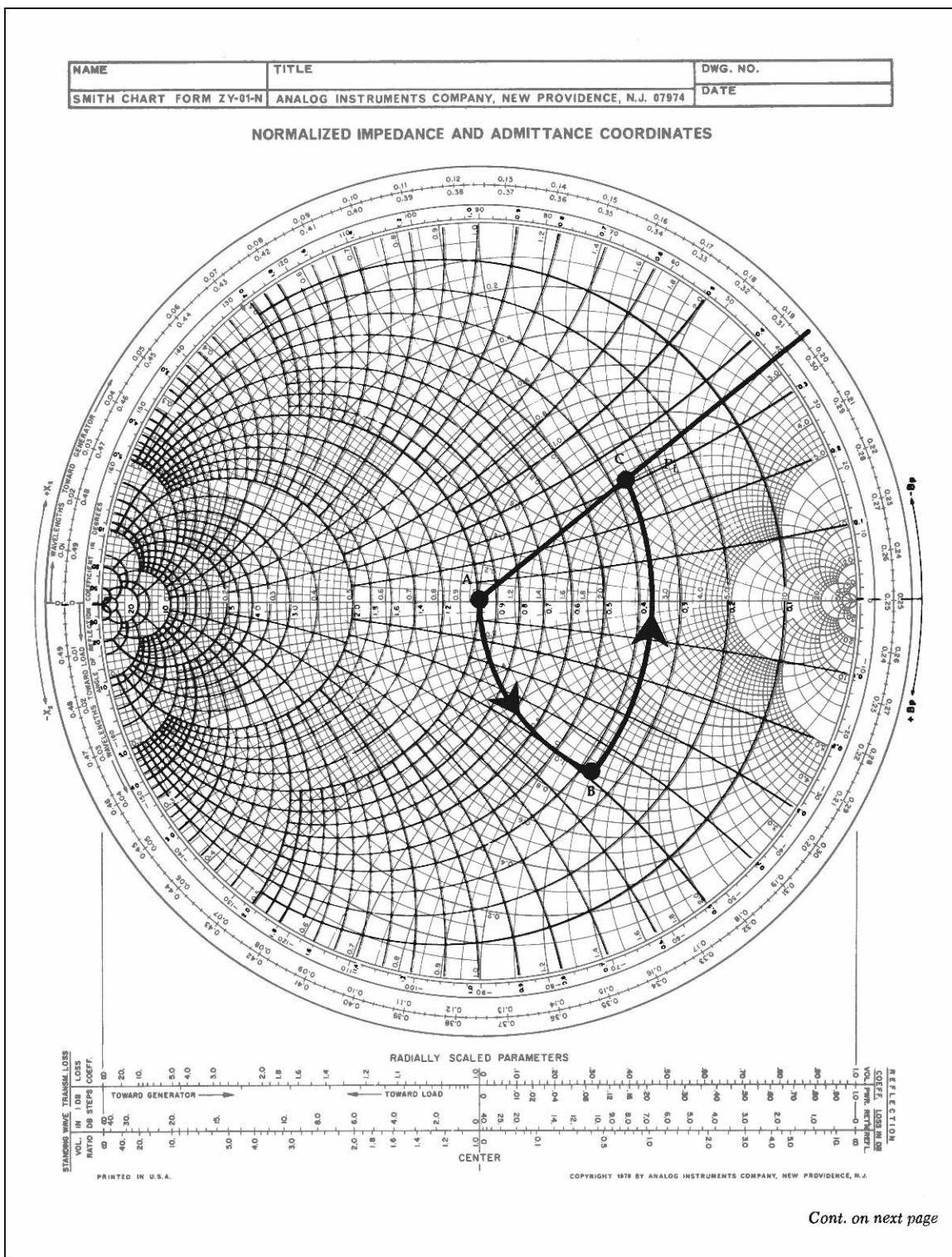
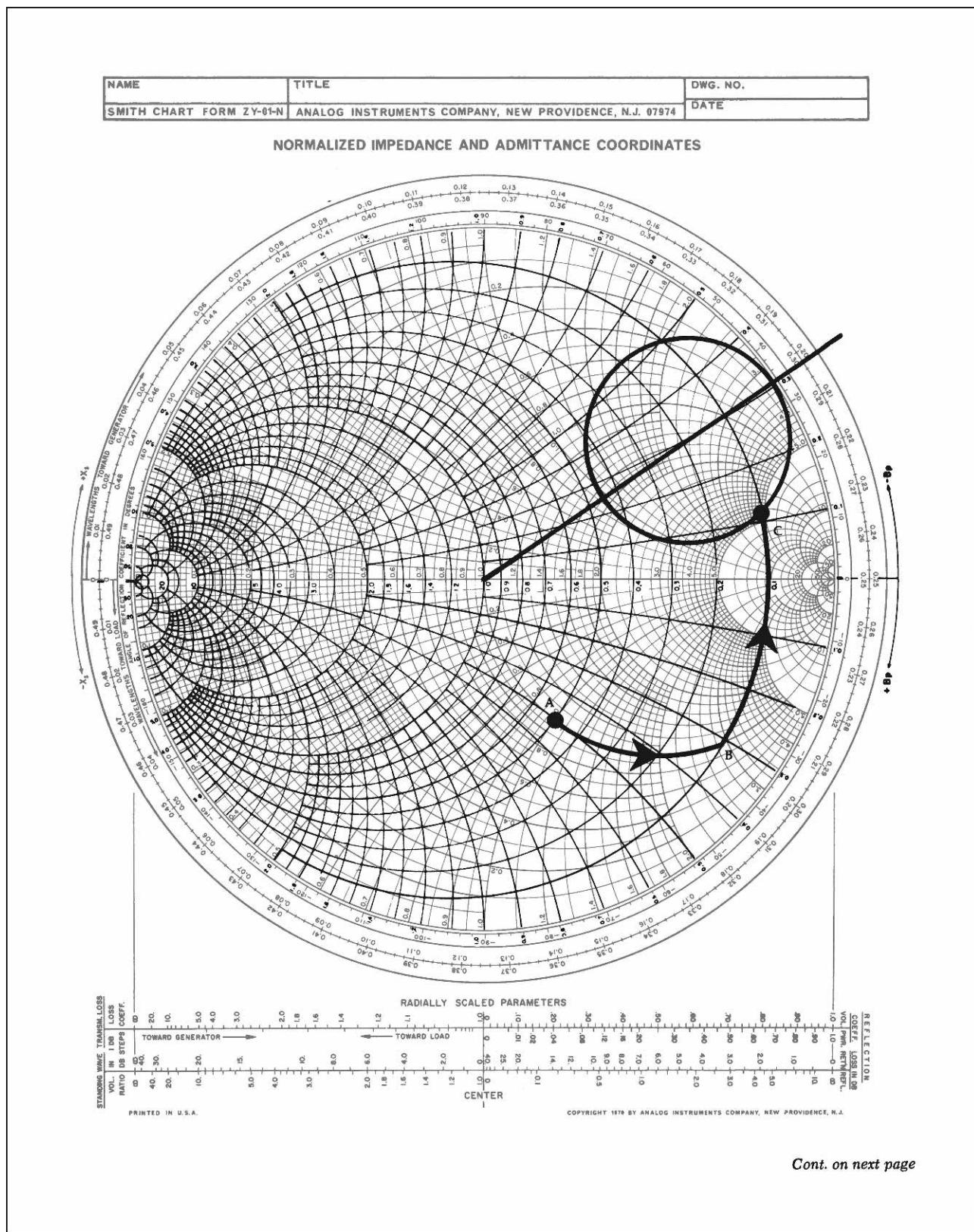


FIG. 6-21. Output network-design values for Example 6-4. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.

## EXAMPLE 6-6—Cont



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FIG. 6-23. Output network-design values for Example 6-6. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.

## EXAMPLE 6-6—Cont

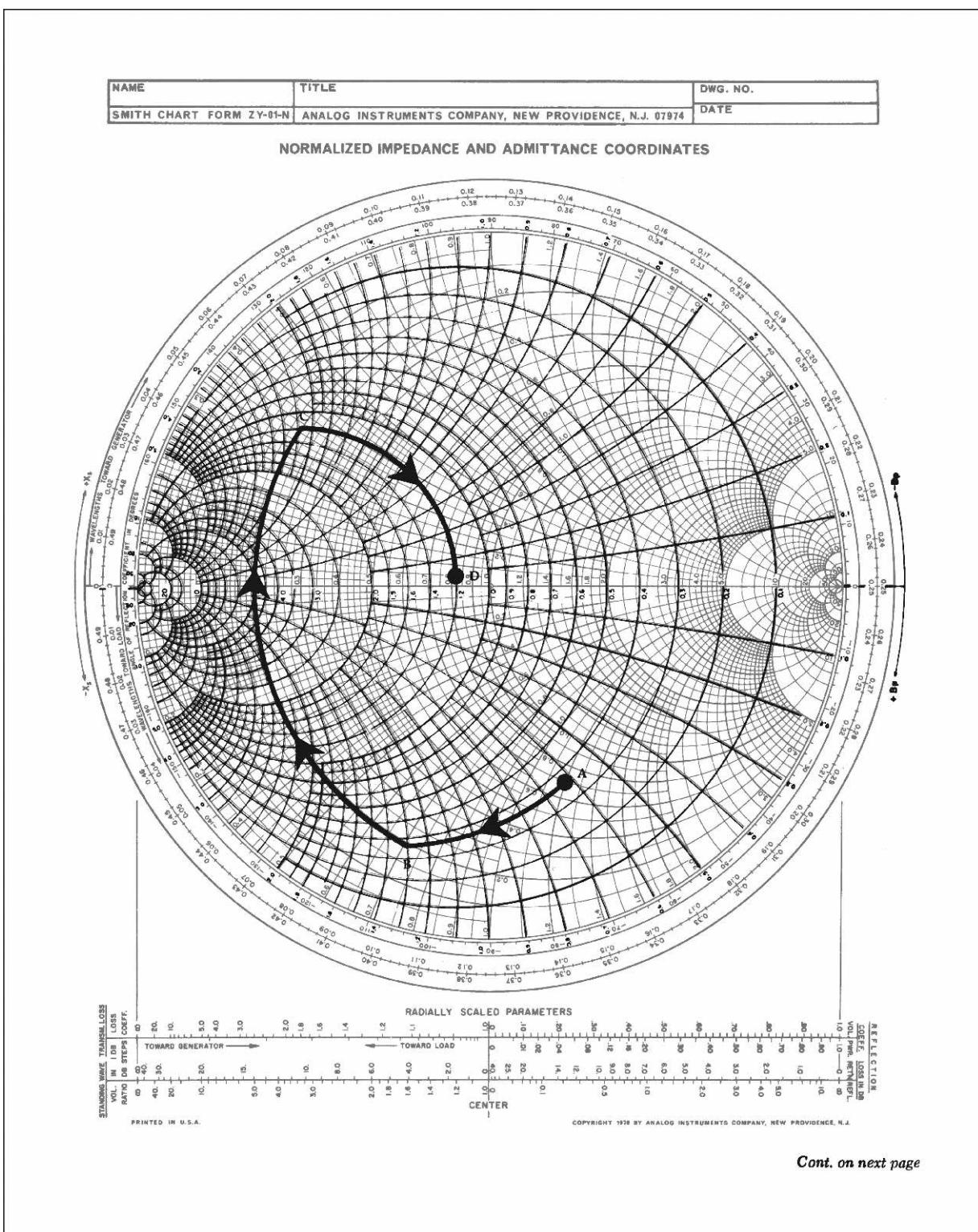


FIG. 6-24. Input network-design values for Example 6-6. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.

### **EXAMPLE 6-8—Cont**

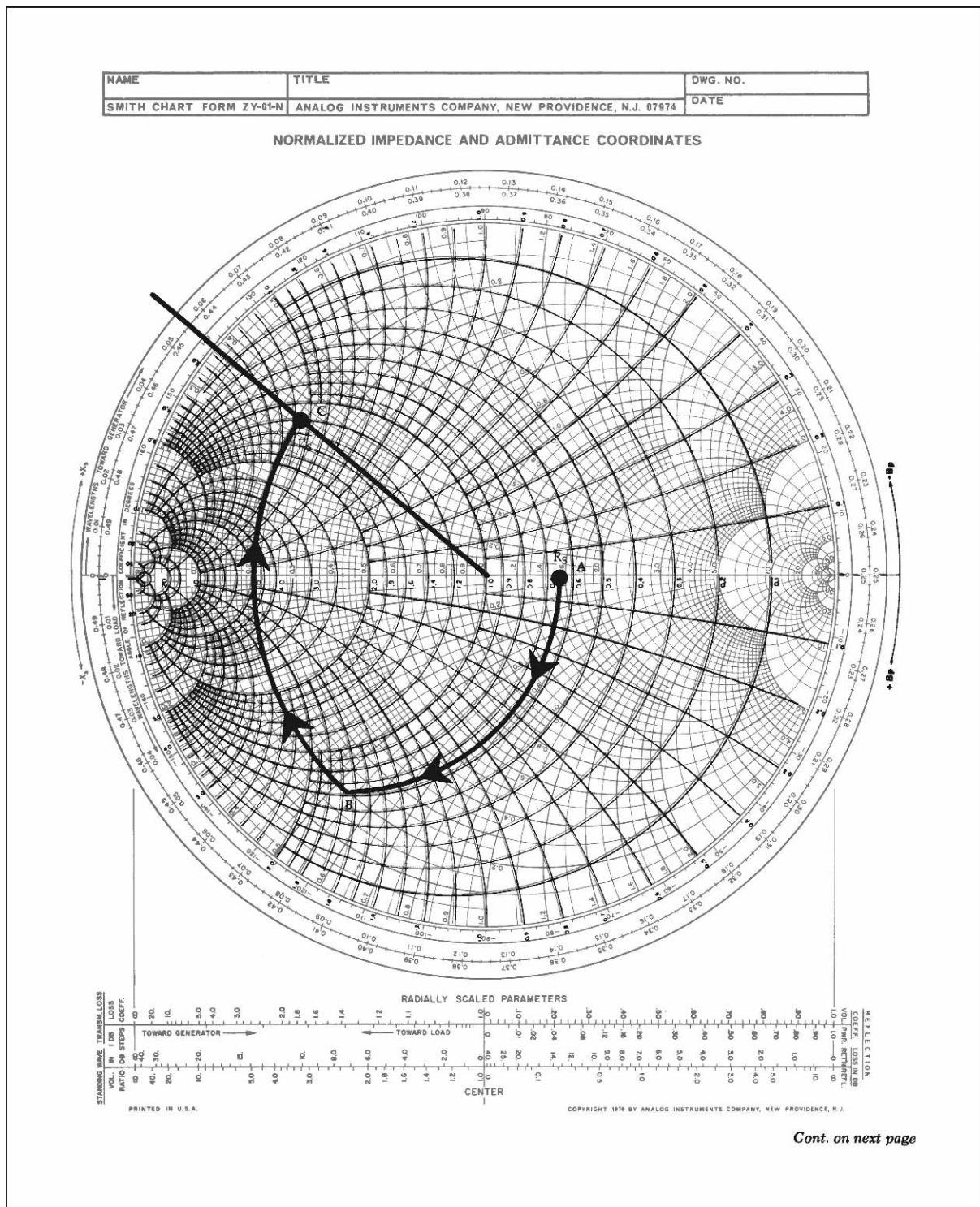


FIG. 6-30. Input network-design values for Example 6-8. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.

## EXAMPLE 6-8—Cont

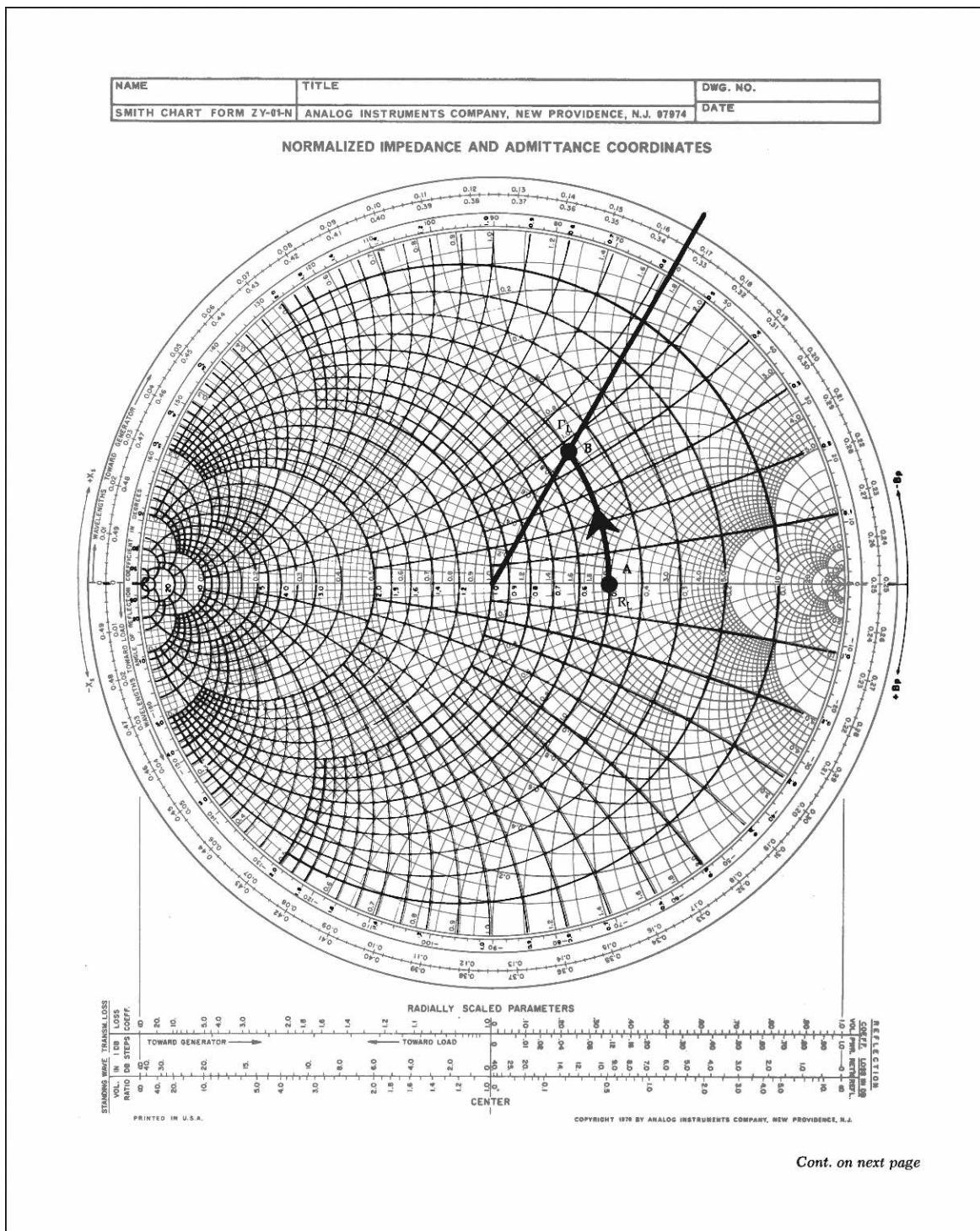


FIG. 6-31. Output network-design values for Example 6-8. For a more detailed full color view of this figure, please visit our companion site at <http://books.elsevier.com/companions/9780750685184>.