

# Lab 6 Report

## 1 Introduction

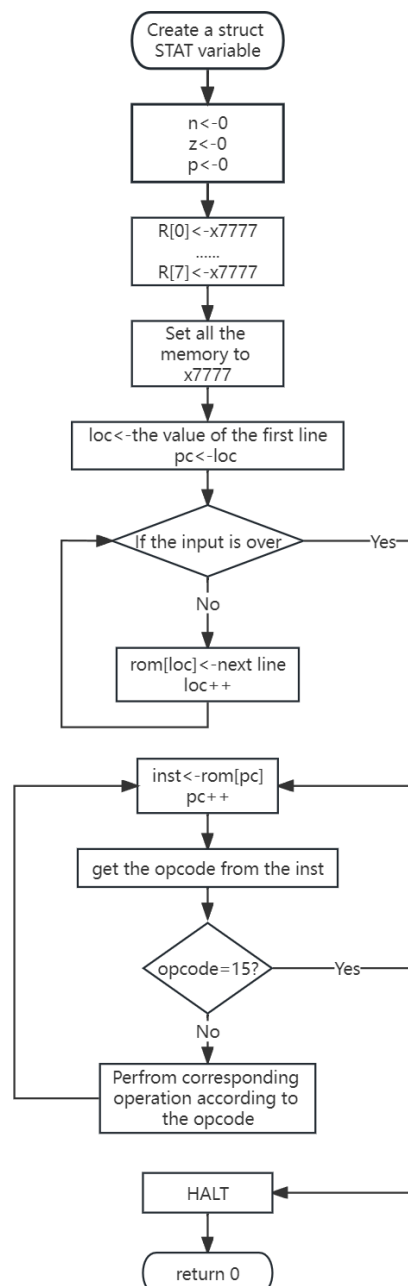
In this lab, we will write a program to execute LC-3 binary code in C. Trap routines, interrupts, exceptions, the instructions RTI, 1101, privilege mode and ACV are not required. The default values of all registers and memory locations are x7777. After the executor halts, the program will print the value of all registers.

## 2 Solution

First we initialize all of the registers and memory locations to x7777. And then we store all the instructions in the corresponding memory locations and set the pc. Next, we loop through the instructions and do the corresponding moves until we meet the HLT instructions and halts.

### 2.1 Algorithm

The flowchart of my algorithm is as followed. The processes of initializing the registers and memories to x7777, store the instructions into the rom and execute the instructions are shown in the flowchart.



## 2.2 Essential part of the code

First the code of data structure definition is as followed. These two structures are often used in the program.

```
1 typedef struct
2 {
3     char opcode[4];
4     char value[12];
5 } INST;//Represents an instruction. It has fields for the opcode and value
6
7 typedef struct
8 {
9     short int pc;
10    char rom[65536][16];
11    int n, z, p;
12    short int R[8];
13 } STAT;//Holds the state of the machine, which includes flags (n, z, p), an array of
    registers (R), and a program counter (pc)
```

Secondly, the initialization part is being showed. Please note that while we store the instructions into the memory, we use function getchar(). When the input ends, the return value of it will be EOF. So we use this characteristic to stop the loop when we meet the end of the input.

```
1 void Initialize(STAT *stat)
2 {
3     //Initialize the cc
4     stat->n = 0;
5     stat->z = 0;
6     stat->p = 0;
7     //Initialize the registers
8     for (int i = 0; i < 8; i++)
9     {
10         stat->R[i] = 0x7777;
11     }
12     //Initialize the rom
13     char value[17] = "0111011101110111";
14     for (int i = 0; i < 65536; i++)
15     {
16         COPY(stat, i, value);//Copy the string value[] to stat->rom[i]
17     }
18     char ins[17] = "/0";
19     gets(ins);//Get the starting location
20     short int loc = SEXT(16, ins);//Transform the string to its value and store it to
    loc
21     stat->pc = loc;//Set the pc
22     int ch;
23     int l = 0;
24     //Store the instruction into the corresponding memory location until the input ends
25     while((ch = getchar()) != EOF)
26     {
27         if(ch >= 48 && ch <= 57)
28         {
29             if(l == 16)
30             {
31                 loc++;
32                 l = 0;
```

```

33         stat->rom[(loc + 65536) % 65536][l++] = ch;
34     }
35     else
36     {
37         stat->rom[(loc + 65536) % 65536][l++] = ch;
38     }
39 }
40 }
41 }

```

Thirdly, we here show the instruction parsing and execution process. Here we use a switch-case to determine which operation to execute based on the opcode.

```

1  int loop(STAT *stat)
2  {
3      INST *inst = malloc(sizeof(INST) + 1);
4      while (1)
5      {
6          PST(stat, (int)stat->pc, inst);
7          stat->pc++;
8          switch (USEXT(4, inst->opcode))
9          {
10             case 0:
11             {
12                 BR(stat, inst);
13                 break;
14             }
15             case 1:
16             {
17                 ADD(inst, stat);
18                 break;
19             }
20             case 2:
21             {
22                 LD(stat, inst);
23                 break;
24             }
25             case 3:
26             {
27                 ST(stat, inst);
28                 break;
29             }
30             case 4:
31             {
32                 JSR(stat, inst);
33                 break;
34             }
35             case 5:
36             {
37                 AND(stat, inst);
38                 break;
39             }
40             case 6:
41             {
42                 LDR(stat, inst);
43                 break;
44             }

```

```

45     case 7:
46     {
47         STR(stat, inst);
48         break;
49     }
50     case 9:
51     {
52         NOT(stat, inst);
53         break;
54     }
55     case 10:
56     {
57         LDI(stat, inst);
58         break;
59     }
60     case 11:
61     {
62         STI(stat, inst);
63         break;
64     }
65     case 12:
66     {
67         JMP(stat, inst);
68         break;
69     }
70     case 14:
71     {
72         LEA(stat, inst);
73         break;
74     }
75     case 15:
76     {
77         if (USEXT(8, inst->value + 4) == 37)
78         {
79             TRAP(stat);
80             return 0;
81         }
82         break;
83     }
84     default:
85         printf("ERROR");
86         break;
87     }
88 }
89 free(inst);
90 }

```

The specific operations are skipped here since they have been shown in detail in the book. If you want to know in specific please read my source code.