Connectors AVR VCC VCC P1 JST-SH 4P VCC ADC7 PC0 (ADC0/PCINT8) PC1 (ADC1/PCINT9) PC2 (ADC2/PCINT10) PC3 (ADC3/PCINT11) PC4 (ADC4/SDA/PCINT12) PC5 (ADC5/SCL/PCINT13) PC6 (RESET/PCINT14) PC6 (RESET/PCINT14) MISO SCK VCC MOSI 3 2 1 0 MOSI AVCC RST GND AREF ISP R1 10K = GND RST PD0 (RXD/PCINT16) PD1 (TXD/PCINT17) 100n 100n PD2 (INT0/PCINT18) DISABLE PD3 (PCINT19/OC2B/INT1) PD5 (PCINT2)/OC08/TI) 06 (PCINT22/OC0A/AINO) PD7 (PCINT23/AINI) PD7 (PCINT23/AINI) ADR1 ADR2 GND PD6 (PCINT22/OC0A/AIN0) PB1 (PCINT0/CLKO/ICPI) PB1 (PCINT1/OCIA) PB2 (PCINT2/SS/OCIB) PB3 (PCINT3/OC2A/MOSI) PB4 (PCINT4/MISO) PB5 (SCK/PCINT5) 16 (PCINT6/XTAL1/TOSCI) 7 (PCINT7/OTT) ADR1 Header 4 GND PB6 (PCINT6/XTAL1/TOSC1) GND PB7 (PCINT7/XTAL2/TOSC2) ATmega328-AU **Status LEDs Magnetic Encoder** VCC LED1 LED2 20 19 NC@1 TestBus3 WS2812B3535 WS2812B3535 MagIncrn MagDecrn VDD5V VDD3V3 **本**本 4 DTestl_A _C5 TestBus0 100n DTest2 B TestBus1 NEOPIXEL 6 TestCoil | TestCon | Mode_Index | VSS | PDIO | NC@10 | SCL / CLK SDA/DIO 2u2 I2C_A0 NSE-5310 NSE-5310-ASSU-ND Drawn By: Chris Barr Aus3D CC-BY-NC-SA Date: 26/3/2016 https://github.com/Aus3D/MagneticEncoder **REV: 0.2A**