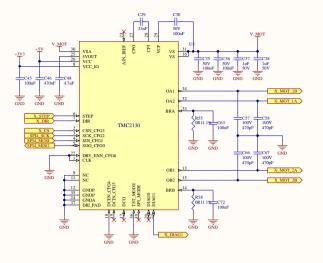
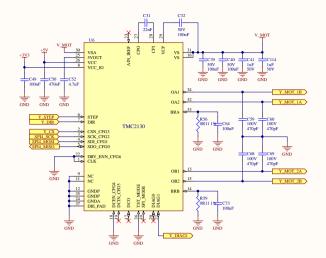
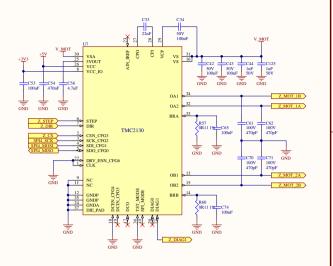
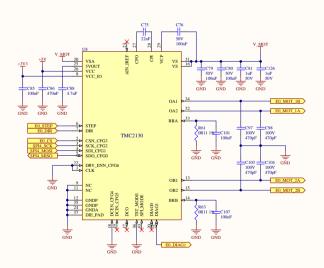


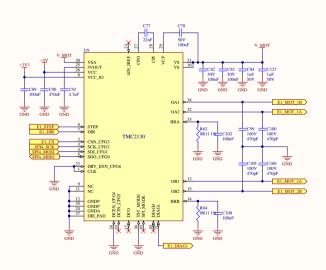
## **Stepper Drivers**

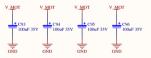












Digital 5V is provided by board's 5V rail, not TMC2130's internal regulator, in order to mi power dissipation. Internal analog domain is still powered from internal regulator. SPI\_MODE has internal pull-up, defaults to SPI mode enabled.

DRV\_ENN\_CFG6 is tied to GND so drivers are always enabled. Can be disabled via SPI command if required.

CLK is tied to GND to enable internal clock.

DIAG1 can be used for sensorless-homing if SPI readout is not supported by firmware. Defaults to open-drain output, and will be left floating unless otherwise configured.

Aus3D

GPLv3 github.com/Aus3D/PicoPrint www.aus3d.com/picoprint

Revision: 0.3A Date: 1/05/2018 Drawn: Chris Barr

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