

Power

Notes

V_IN recommended range is 12-30V, limited by: -Electrolytic capacitors (max. 35V)

-5V Regulator (max. 40V)

V_IN is protected against reverse-polarity by P-FET Q7, which is off with body-diode reverse-biased when input polarity is reversed. Zener D17 protects Q7 from gate voltage exceeding ratings when V IN is high.

V MAIN is measureable by STM32 via voltage divider, with lowpass RC filter.

 V_MOT is separated from V_MAIN by FB1, to reduce magnitude of chopper switching noise returning to V_MAIN .

V REG is separated from V MAIN by FB2 for similar reasons.

D15 prevents capacitance on logic rail from feeding switched MOSFET outputs on V_MAIN, and also protects components on 5V/3V rails from reverse-polarity should Q7 be incorrectly bypassed via V_1MAIN .

Notes

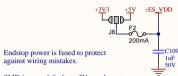
5V regulator's output is set to \sim 5.4V so that it is more likely to take precedence over USB 5V power in the ORing diode setup. This also means the voltage after the diodes should still be \sim 5V and not below.

 V_USB is fused to protect any host computer or power supply from short-circuits on the 5V rail, and to limit current draw to within USB specifications.

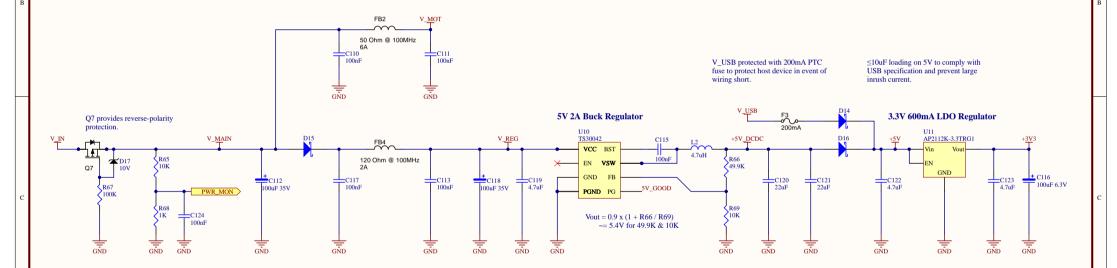
V_USB only directly sees 4.7uF of capacitance in order to comply with the maximum 10uF loading in the USB specification. Additional capacitance ($\sim 100uF$) is present on the output of the 3V regulator; however the regulator's current-limiting behaviour and non-zero startup time limits the inrush current via V USB.

Endstop power is fused to protect both board and external sensor / switch against incorrect wiring. 5V or 3.3V regulators' short-circuit protection should protect board sufficiently, but 200mA maximum on endstop power is safer for external components and allows board to continue operating through a shorted sensor.

Endstop Voltage Select



SMD jumper defaults to 5V, can be cut and soldered to change supply voltage.



Aus3D

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GPLv3 github.com/Aus3D/PicoPrint www.aus3d.com/picoprint

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Date: 2/05/2018
Drawn: Chris Barr

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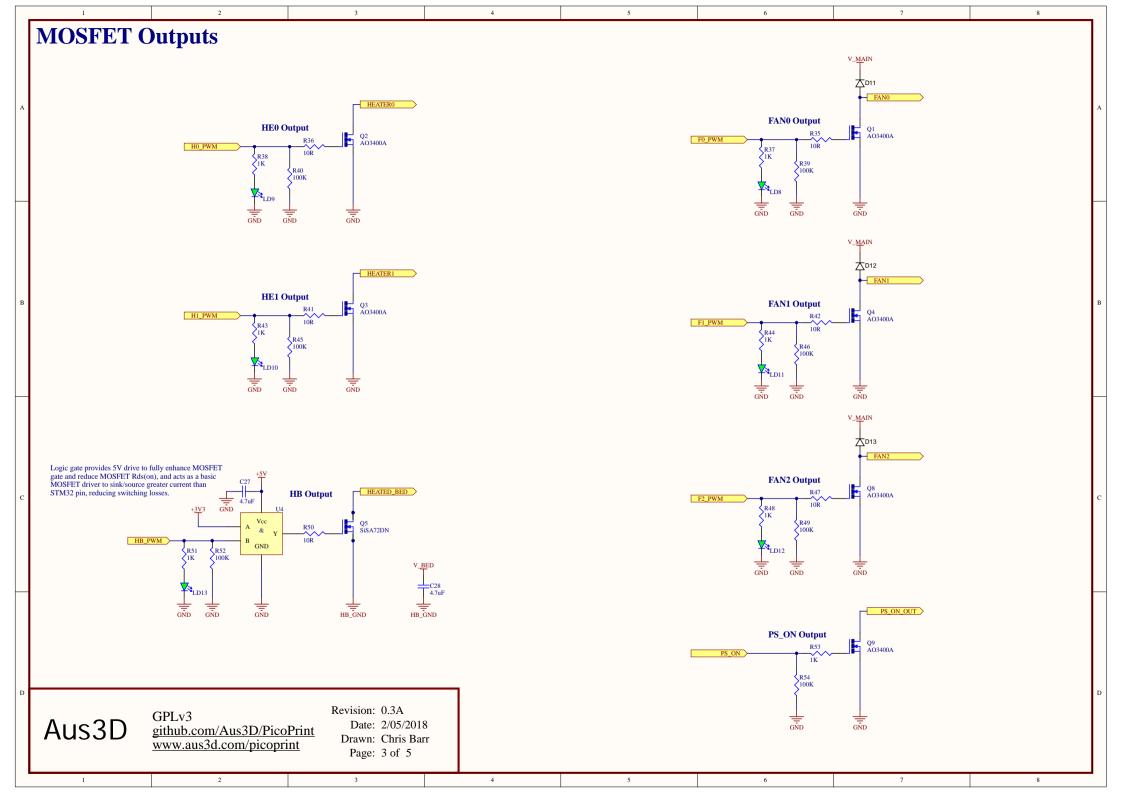
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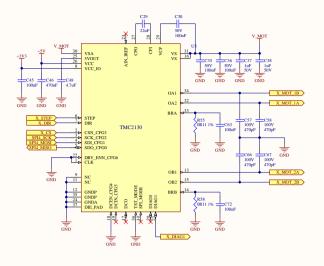
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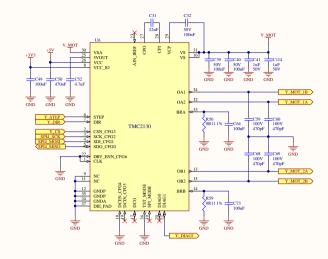
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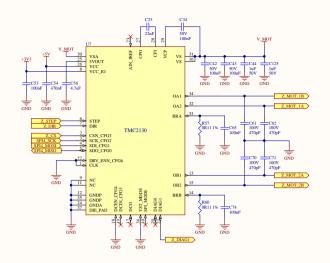
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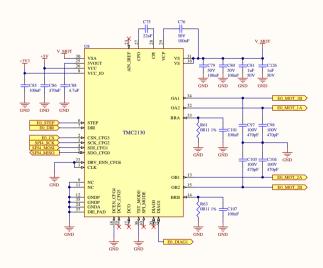


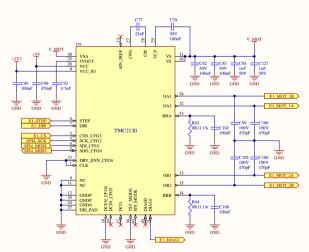
Stepper Drivers













Notes

 $\label{eq:def:Digital_SV} Digital \, 5V \, is \, provided \, by \, board's \, 5V \, rail, \, not \, TMC2130's \, internal \, regulator, \, in \, order \, to \, minimise \, internal \, power \, dissipation. \, TMC2130's \, internal \, analog \, domain \, is \, still \, powered \, from \, internal \, regulator.$

SPI_MODE has internal pull-up, defaults to SPI mode enabled.

 $\ensuremath{\mathsf{DRV_ENN_CFG6}}$ is tied to GND so drivers are always enabled. Can be disabled via SPI command if required.

CLK is tied to GND to enable internal clock.

DIAG1 can be used for sensorless-homing if SPI readout is not supported by firmware. Defaults to open-drain output, and will be left floating unless otherwise configured.

Aus3D

GPLv3 github.com/Aus3D/PicoPrint www.aus3d.com/picoprint

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