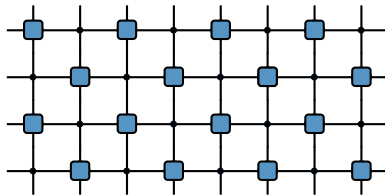


Dual-unitarity round-a-face

- Circuits introduced in Prosen, Chaos 31, 093101 (2021) with similar properties, built from 1-site 2-controlled gates



$$(U_{a,c})_{b,d} =$$

A diagram of a single gate symbol. It consists of a blue square with four legs. The top-left leg is labeled 'a', the top-right leg is labeled 'b', the bottom-left leg is labeled 'c', and the bottom-right leg is labeled 'd'. The legs are represented by vertical lines with dots at the ends.