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EE345M, S2012

TA:

**LAB 1 Report**

**Objectives:**

To implement split screen output and periodic ADC sampling drivers as well as a interpreter to process commands and respond on the UART. Also adding software FIFO’s to UART input / output.

**Hardware Design:**

none

**Software Design:**

See attached files for oLED, ADC, OS, and Interpreter .c/.h files

**Measurement Data:**

1. Estimated time to run periodic timer2 interrupt: \_\_\_\_\_\_\_
2. Measured time to run periodic timer2 interrupt: \_\_\_\_\_\_\_

**Analysis and Discussion:**

1. Range, resolution and precision of ADC

\_\_\_\_\_\_\_\_\_\_\_\_

1. Ways ADC can be started

\_\_\_\_\_\_\_\_\_\_\_\_

1. \_\_\_\_
2. \_\_\_
3. \_\_\_\_