MANE A	ayon Saxma	V C.E [33dsm] L-8	ROLLHO	02
1>	14-08-25	CPUC- Assignment-1	1-30	
		OS-Assignment-1	31-41	
		CPUC-Assignment -2	42-54	
		CSA-Assignment-1	55-64	

Assignment - 1 Evolution of micropraessor: (1) First Generation [+bit Microprocessor] -The intel corporation came out with the first generation of micro-processor in 1971. They were 4-bit processor namely 9 antel 9009. The speed by the processor was 740 RH= thing 60 K instructions per second it had 2300 transitations and 16 Jims inside. Built on a single chip, it was ungle for simple anothernetic and logical operations. A control unit was these to understand the instructions from memory and example the tasks. (2) Second Generation [8-bit Microphocosur]: The second generation began in 1973 by Intel as the first 8-bit micro-processon. 9+ was useful for anithmetic and logic afterations on 8-bit words. The first processor was took to the a class years of 500 little and 50 h instructions per second. Helphared by an 8080 miles processor in 1974 with a speed of 2 MHz and 60 k instruction per second.

possing on obility by 169230

instructions per second with 3MHz

Apreed. 3) Third Generation (16:67 Migrofinescepes) The third generation begon with 80 \$ 86-88 micro-processors in 1978 with 4.77, PX 10 MHz . Speed and 25 million instructions per second. Other important inventions were zilog - 2800, and 80286, leshib temp out in 1982 and could sun 4 million impruetion. per second with 68 pins inside. (1) Youth Generation [32-bit Minghocepor]: Intel was still the leader as many companis come out with 32-bit Micro-processon oneund 1986. Their clack

Speed was between 16 MHz to 33 MHz

With 2+512 transistan Inside. Sirot one was the intel 20486 muniprocessor

g: 1986 with 16-100 MHz clock spred

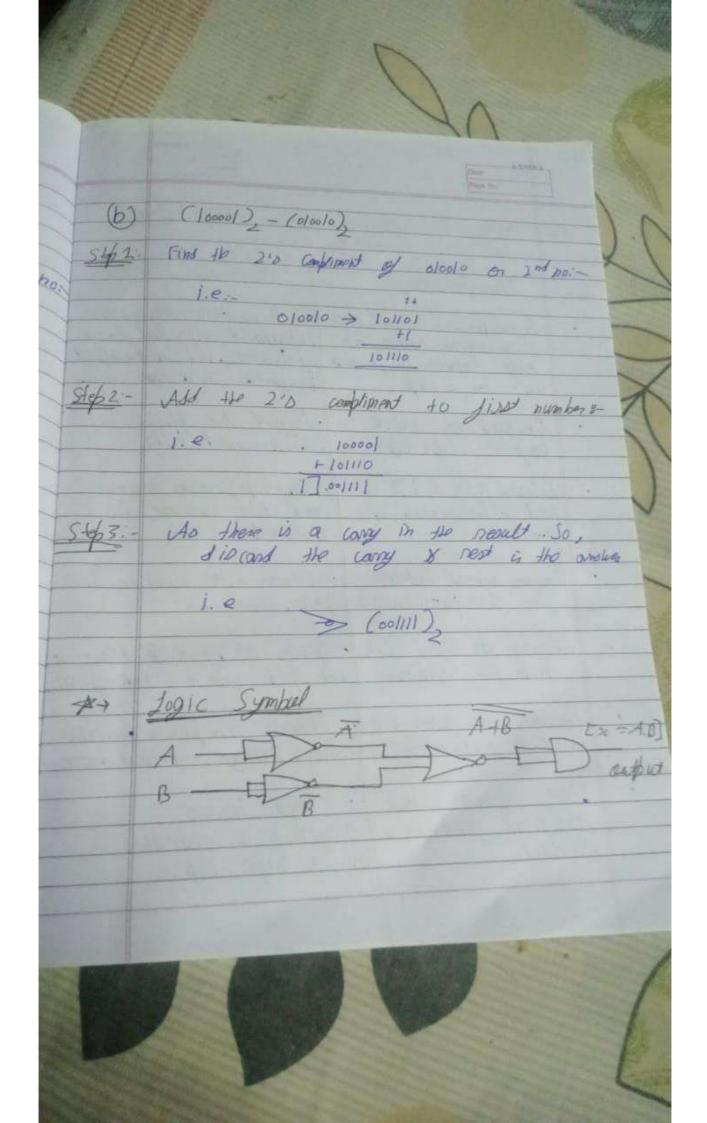
ond 1.2 million fromsister with 8th of coche memory followed by the PENTIUM microprocenor in 1993 which had 66 MHZ clock speed ms 8-bit. of coche menory.

(9) Fifth Generation [64-bit Muniporcents]: Began in 1995, the Pentium processor two one of the first 64 bit processors with 1.2 6/12 to 36Hz dark speed there were 291 million translators and 64 tib instruction per second. Jollowed by 13, 15, 17 micro-processon in 2007, 2009, 2010 suspersively show were some by the boy points of the generation.

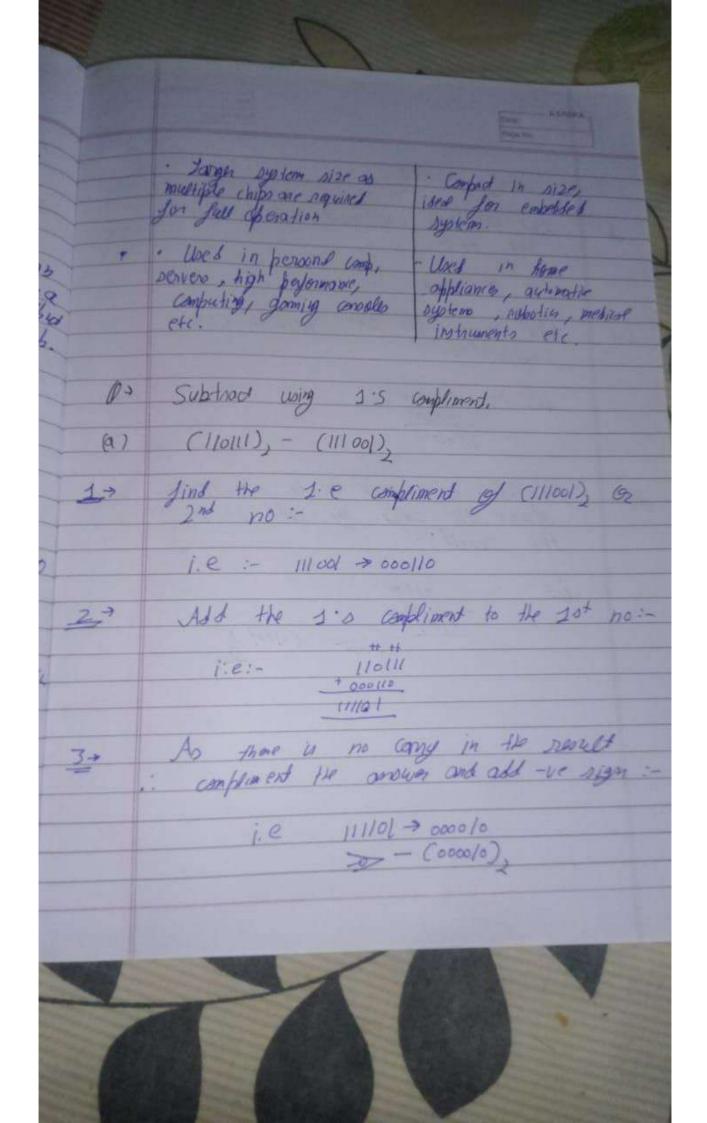
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	Com	bazua Len	Sumpara	1 2	1 Sof	el micro	boxeoun	
None of	Yn of	Wad	Memory	Pino	Nog	Och	Remains	The state of the
Microprocessor	Introde lien	3cgth	Caballay		Lowsialesus	1		i
4004	1971	4 bits	1113	16	2300	450 KHZ	saids tot a	
8008	1972	abito	16 KB	18	3500	800 Kilz	First 8 bit	
8080	1973	Shits	61118	40	600-	21142	Reg. 3 peurs	
8085	1976	Shits	64113	40	6500	3-6 MH 2	noise by day	1000
3086	1978	16613	IMb	40	2 9000	5-/o MHz	Post 10-bit	- Open A
8088	1979	8/16613	1Mb	40	29000	5-3 MH2	urd in	
80186	1982	16.610	IM6	68	Approx	5-2 MH2	1-7-0-	
80286	1 982	18610	16 MBane		1.344		PC (AT) o	100
			190 1010				years	
80386	1985	52 bito	4 gbral	11.00	2.7		Find 3)-	100
DX			6tib viated	P6A	Jokh	MHZ		
80386	1988	16/32	466 red	100	2.75	20		
SX		612	64TB visted		Jaki	MHZ		

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2048		
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Pentium	n 1993 64-bits and	hit
The same	16th care as 1 60-200 2 grot rection and	
-	19 Hz atter, supposts	
b Luc	multimalia	
Pentium	D) SS C C ICANI	-
-10.0	2004 DILK, PhA millian	194
	12 tothe cothe	
Pendium	1997 646ito 676B 242 7.5 400 MHz Coptur,	
II	million lailed in	hos
	Ogi et pro	100
^	Internet	
Pentium	1999 6961to 161111 370 9.5 19Hz E-conner	10
加	Data + 16th PGA million application	
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1	EL SUE SUE SUE SUE ALERS SUE SUE SUE SUE SUE SUE SUE SUE SUE SU	
n tium	2000 64 bits 644B 423 42 1.3-3.2 profost	02
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t a	West 1093	
E A	Next 1093	
	Next 1093	
	Next 1093	
	Next 1993	
	Next 1993	

0> Subtract using 2's compliment =-(10/10)2 - (111100)2 Step 2: Find the 2's compliment of 111100 on 2+1 1.0, 111100 -> 000011 +1 + 000100 SHE! - All the 2's compliment to first no:-+1 1.0,1 110110 1000/00 111010 Stop 3:- As then is no carry in the swell so tope the 2's complimed cof the result and on -ve sign 111010 > 00010] 1.0, 000110 - (000110),



	Date Property	
0>	/ I/I/O	6
40-	Microprocesson Sticker- continued	1
	A microprocessor in the CPU. A micro controller of a computer system at integerated this that only contains the processing CPU, howary, and integerated on a single unit on a chip peripherals on a single	touth the cap
•	Designed for general - Designed for specific burgone computing control-unimited took).
	External RAM & ROM On-chip program med to be connected for and state memory included included.	
	Requires external horsewere. Comes with built for I/O ports, timers etc parts, timers etc.	
*	Speels for complex tooks duck speed since it	
•	Consumes more power sur. Consumer less pour to high performance for battery - 9	
,	More expensive because . Chapter because additional hand were is everything as ent plequined.	redbes .



(b) (11110) - (10101) Find the 2's compliment of (10/0/)2 or 1.e > 0/0/0 Add it to the 20t number C1's confinent) :-11110 i.e :toldo As their is a comy in the result in semone the comy by abd it to 39 the result: i.e :- 01000 olast > (0/00/)