Index	Paper	EC1: Which functional domain(s) does the study analyze and/or modify in relation to SW architecture changes? E.g. ADAS, IVI (In-Vehicle Infotainment), Powertrain, Chassis...	EC2: Which system limitation(s) does the study identify as drivers for SW architecture changes? E.g. Busload, computing power, development costs, development time...	EC3: Which specific technologies does the study identify as enablers or catalysts for changes in the SW architecture? E.g. High-Performance Computing in autmotive, AI & machine learning, electrification, Over-The-Air updates and continuous deployment, connectivity - V2X and 5G...	EC4: How does the study technically address the integration of diverse software requirements (real-time, non-real-time, safety-critical, etc.) within a centralized automotive software architecture? E.g. virtualization via hypervisors, containerization...	EC5: Which architectural patterns or design practices are proposed to systematically support mixed-criticality in centralized automotive software architectures? E.g. Mixed OS environments, Service-Oriented Architectures (SOA), mixed-criticality scheduling, safety island / redundant compute...	Comment
8	Design of Criticality-Aware Scheduling for Advanced Driver Assistance Systems	ADAS	size, weight and power constraints; scalability and fault tolerance; Tasks with varying criticality		isolating via hypervisor	Partitioned Architecture (Hypervisor with different modes)	work-in-progress paper
9	A Security Process for the Automotive Service-Oriented Software Architecture	-		-	ASOA	ASOA	Exclude - no MC or centralization
10	Towards the deployment of a centralized ICT architecture in the automotive domain	General	Growing number of ECUs, heterogeneous networks, lack of fail-operational, complex verification	-	not at all	not at all	Exclude - no MC
11	A Modular Five-Layered V-Shaped Architecture for Autonomous Vehicles	ADAS	robustness	-	not at all	not at all	Exclude - no MC
12	Autonomous driving systems hardware and software architecture exploration: optimizing latency and cost under safety constraints	-	-	-	-	-	Exclude - no MC
13	MPSoC-Based Platform for FailOperational Control of an Automated Research Vehicle	-	-	-	-	-	Exclude - no MC
14	Modelling centralised automotive E/E software architectures	General	-	-	-	-	Exclude - not about architecture, but about describing architecture