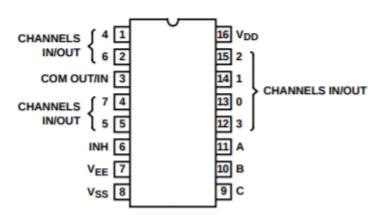
CD4051B (PDIP, CDIP, SOIC, SOP, TSSOP) TOP VIEW



- 1 VDD + 5V
- 2-VSS-GND
- 3 A, B, C Channel select
- 4 Channel IN OUT (0 7)
- 5 Pin 3 = Com (OUT/ IN)

TRUTH TABLES

INPUT STATES				
INHIBIT	С	В	Α	"ON" CHANNEL(S)
CD4051B				
0	0	0	0	0
0	0	0	1	1
0	0	1	0	2
0	0	1	1	3
0	1	0	0	4
0	1	0	1	5
0	1	1	0	6
0	1	1	1	7
1	X	X	X	None