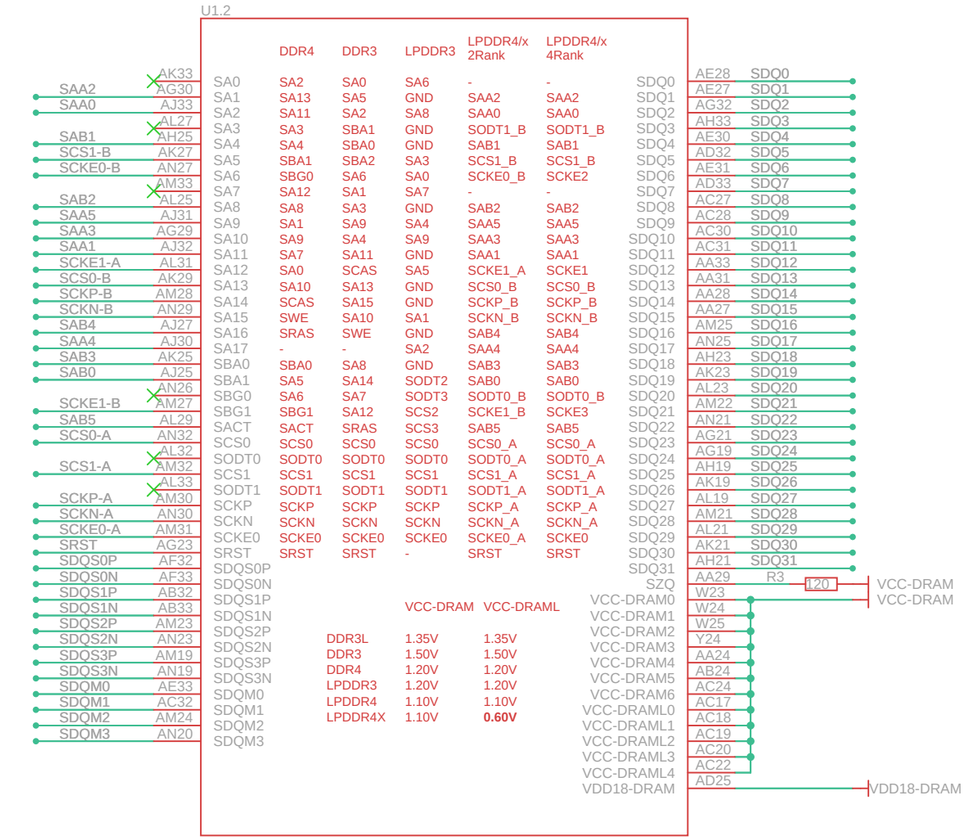
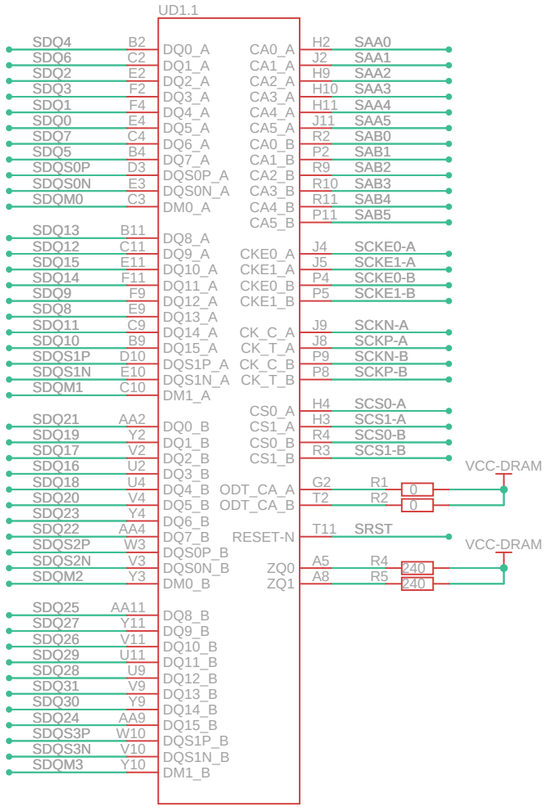
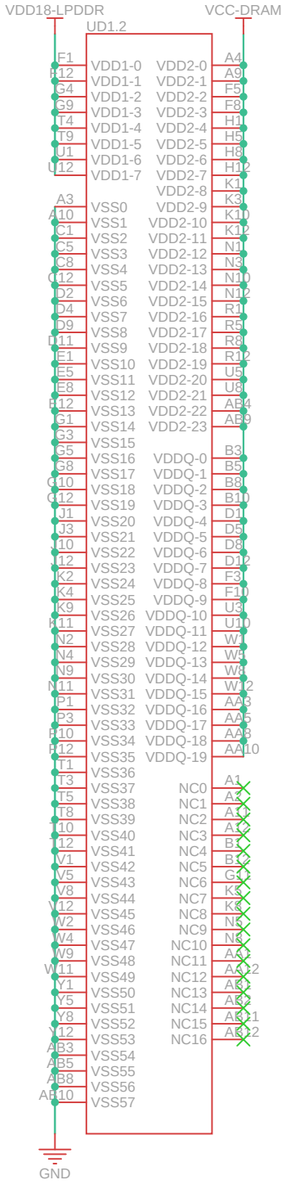
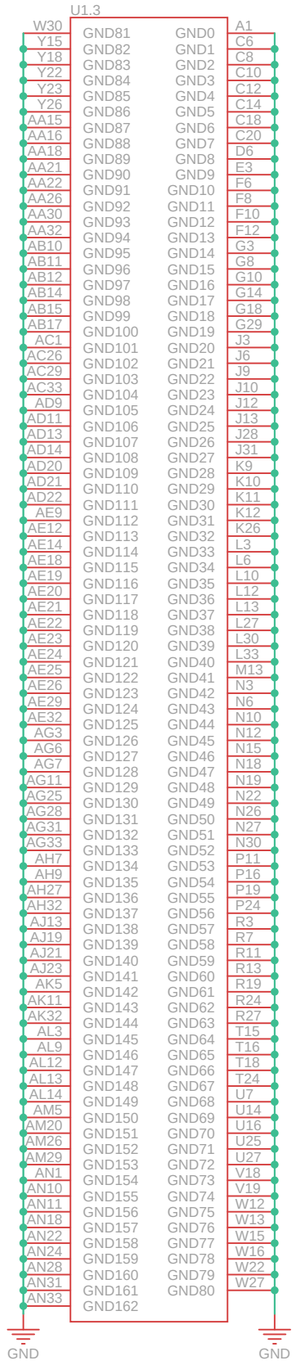
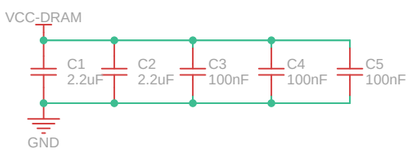


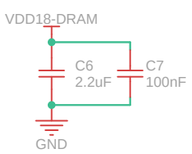


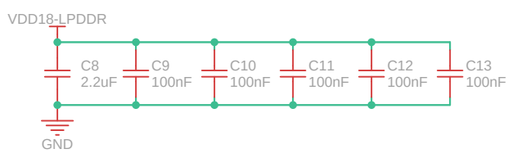


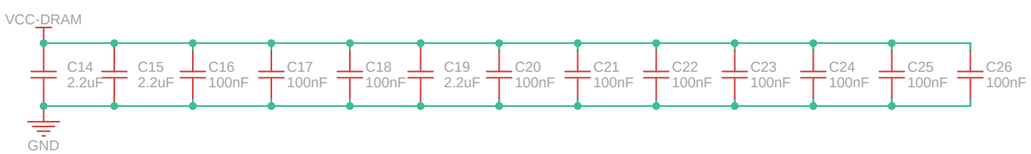


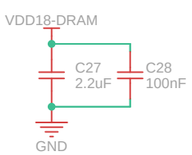


默认使用0201阻容，模板支持更换0402/0603阻容









T527支持DDR3/3L、DDR4、LPDDR3、LPDDR4、LPDDR4x，使用时需先确定DRAM的类型、片选及位宽
DRAM上电时序请严格参照标案原理图设计。LPDDR3、LPDDR4、LPDDR4x颗粒需要的VDD18-LPDDR电源使用BLDO2供电；
DDR4颗粒需要的VCC25-DRAM电源需外挂LDO供电，
使用AXP717B BLDO2使能控制输出；LPDDR4x颗粒需要的VCC-DRAML电源需外挂DCDC供电，使用AXP717B BLDO2使能控制输出。
DRAM外部分立器件的数量、值大小以及精度不允许随意更改，请严格参照标案原理图设计。
DRAM所有的滤波电容不能删减，预留的滤波电容建议在开发阶段预留调试，但可以不贴片，待开发测试验证稳定之后，量产可以根据实际情况酌情删除

TITLE: Avaota A1 八核 AIoT 开发板		REV: 1.0
	Company: YuzukiHD	Sheet: 1 / Total: 1
	Date: 2024-02-14	Drawn By: GloomyGhost