

CSB-32D

LAB - Assignment - 3

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Answer to the Qn. 1

Line coding is the process of connecting digital data to digital signals. We assume data in the form text as well as numbers, graphical, images, audio or video. As are stored in computer memory. As sequence of bits, line coding converts a sequence of bits to a digital signal.

Characteristics of line coding

① Signal Element vs Data element:

Data elements are what we need to send. Signal elements are what we can send.

Data elements are being created, signal elements are the carriers.

2) Data Rate vs Signal rate :

The data rate defines the number of data elements (bits) sent in 1's

The signal rate defines the number of signal elements sent 1 sec. Sometimes the data rate is called bit rate & signal rate is called pulse rate.

3) Bandwidth:

Digital signal that carries information is non periodic. Bandwidth of a non periodic signal is continuous with an infinite range.

However, most digital signals are encountered in real life have a bandwidth with finite values.

4) Baseline wandering: In decoding a digital signal the receiver calculates a running average of the received signal power. This average is called baseline. The incoming signal power is evaluated against this baseline to determine the value of the data element.

5) Dc components: When the voltage level in a digital signal is constant for a while, the spectrum creates very low frequencies. These frequencies around zero, called Dc components.

⑥ Self-synchronization: To correctly interpret the signals received from the sender, the receiver's bit intervals must correspond exactly to the sender's bit intervals.

⑦ Built in Error detection: It is desirable to have a built in error detecting capability in the generated code to detect some or all the errors that accrued during transmission.

8) Immunity to noise and Interface:

Another desirable code characteristic is a code that is immune to noise and other ^{on} interfaces.

9) Complexity : A complex system is more costly to implement than a simple one. For instance, a scheme that uses four signal levels is more difficult to interpret than one that uses only two levels.

Answer to Qn. - 2

PCM (pulse code modulation): It is defined as a digital technique that involves sampling an analog signal at regular intervals and coding the measured amplitude into a series of binary values which are transmitted by modulation of a pulsed, or intermittent, carrier.

A PCM encoder have 3 process.

① Sampling: is done where the analog input signal is sampled at regular intervals. The sampling of the signal is done at the rate of

2) Quantization: It rounds off each sample to the nearest discrete level. This performs the approximation of each sample thus assigning it a particular discrete level.

3) Encoding: is done by conversion of the quantized signal into binary codes.

Answer to the Qn. 3

Hence

$$N = 100 \times 1000$$

$$\approx 100000$$

1 kbps

$$\approx 1000 \text{ bps}$$

$$q_L = 1$$

We assume that the average value of C is $\frac{1}{2}$

The base rate is

$$S = C \times N \times \frac{1}{q_L}$$

$$\approx \frac{1}{2} \times 100000 \times 1$$

$$\approx 50,000$$

$\approx 50 \text{ kbound}$.

Answer to Q.n. 9

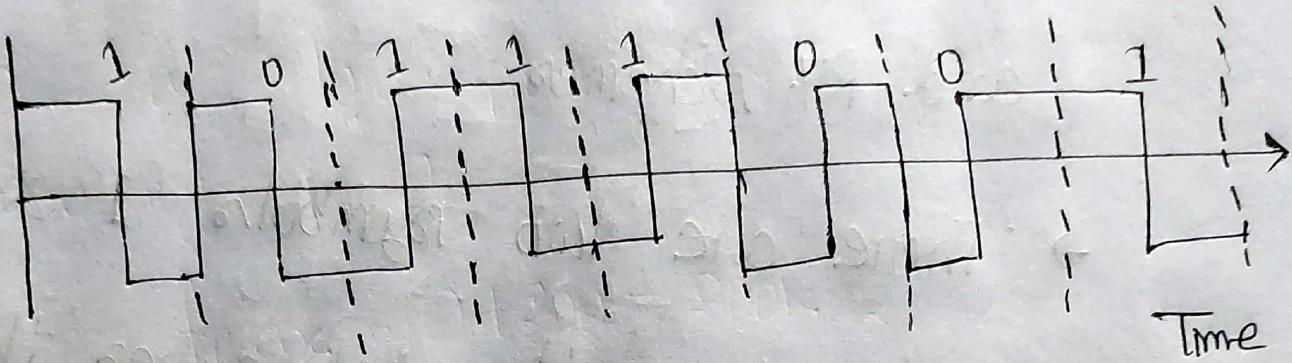
Self synchronizing digital signal includes timing information in the data being transmitted. To correctly interpret the signals received from the sender, the receiver's bit intervals must correspond exactly the sender's bit intervals. Self synchronizing is a unique decodable in which the symbol stream form by a portion of any two adjacent code words.

Due to the lack of synchronization cause huge impact decoding and this is

Know about deadlock situations. Deadlock is a situation where a set of processes are blocked because each process is holding a resource.

A set of processes which share some common resources and one sigma out of two hangs and the other has not terminated or flushed it's output out file can be said

Answer to q.n.-5



Differential Manchester Encoding represents a line code in digital frequency modulation in which data and clock signals are combined.

Here, 0 represented as FL or LF

& 1 represented as LH or HF

so, data stream is 10111001

In brief explain,

- D. Manchester Combines NRZ-I [Non returns to zero] and RZ schemes
- There are two symbols '1' & '0'
- Only one symbol causes the level change.
- We make inversion only for bit '1', & for '0' bit there is no ~~inversion~~ inversion
- It is a line code in digital frequency modulation and so it doesn't require block coding.

Answer to Q.n-6

(a)

Non Return to Zero is basically a binary code that is used in the communication transmission where the data bit 1 is of positive voltage, and 0 data bit is of negative voltage.

NRZ-I basically have long series of 0's or 1's resulting in clock recovery difficult after this. This uses a transition in the middle of the clock cycle. But this actually occurs when only 1 occurs.

NRZ-L: here the level of voltage determines the value of the bit typically binary 1 maps to logic level high and the binary 0 maps

To logic level low.

NRZ-I is better than NRZ-L. For instance, Let's consider a situation where two data wires are wrongly connected in each other's place. So, in NRZ-L all the bits gets reversed since the voltage levels gets swapped whereas in NRZ-I all the bits are recognized by transition. So that all the bits will be correctly interpreted.

Answer to Q.n.7

Discussing about the advantages and disadvantages of Bipolar.

Advantages

- 1) Since they are bipolar signals, ideally no DC components need to be present.
- 2) It provides features for error detection, this is done by detecting absence of expected transitions.
- 3) It also provides a guaranteed mid bit transitions whereby offering synchronisation facility at the receiver side. These are actually called the self clocking codes.

Disadvantages:

- 1) Biphasic is basically used in shorter distance especially in LAN's.
- 2) It maps at least one transition per bit time and probably 2 bits
- 3) Its modulation or signal rate is double that of non-return to zero (NRZ). Therefore higher bandwidth is required for biphasic signals.

Answer to Q.n.-8

(a)

In a low-pass signal, the minimum frequency is 0.

$$f_{\text{max}} = 0 + 200 = 200 \text{ kHz}$$

Nyquist rate $f_s = 2 \times 200000$
 $= 400000 \text{ samples/s}$

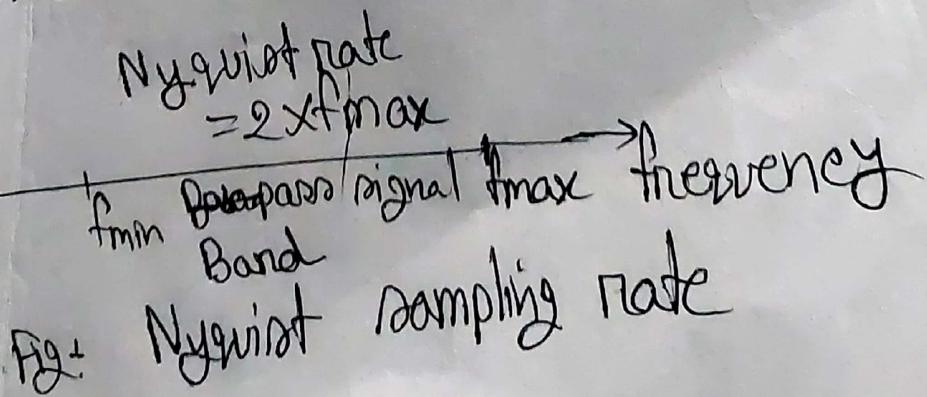
(b)

In a bandpass signal,

The bandwidth = 200 kHz

Lowest frequency = 100 kHz

Amplitude



Here, $f_{\min} = f_{\max}$ [In bandpass signal]

$$f_{\max} = f_{\min} + \text{Bandwidth}$$

$$= 200 + 100$$

$$= 300 \text{ kHz}$$

$$\approx 300,000 \text{ Hz}$$

So,

$$\text{Nyquist rate} = 2 \times f_{\max}$$

$$\approx 2 \times 300,000$$

$$\approx 600,000 \text{ samples}$$

Answer to Qn 9

Let, $n = \text{No. of bits}$

$$n = 4 \text{ [Given]}$$

$$\text{Step size, } \Delta = \frac{40 - (-40)}{2^4} = 5$$

Thus the step size is 5, there should be an increment of 5 in amplitude level.

The ~~codeword~~ codeword corresponding to each quantized values are:

Quantized value - codeword

-40	↔	0000
-35	↔	0001
-30	↔	0010
-25	↔	0011
-20	↔	0100
-15	↔	0101
-10	↔	0110
-5	↔	0111
0	↔	1000
5	↔	1001
10	↔	1010
15	↔	1011
20	↔	1100
25	↔	1101
30	↔	1110
35	↔	1111

Time \leftrightarrow Analog signal value \leftrightarrow Sampled value \leftrightarrow Quantized value \leftrightarrow Quantization error \leftrightarrow Code word

0	\leftrightarrow	23.7	\leftrightarrow	24	\leftrightarrow	25°	\leftrightarrow	-1	\leftrightarrow	110
1	\leftrightarrow	-15.7	\leftrightarrow	-16	\leftrightarrow	-15	\leftrightarrow	-1	\leftrightarrow	10101
2	\leftrightarrow	-29.6	\leftrightarrow	-30	\leftrightarrow	-30	\leftrightarrow	0	\leftrightarrow	0010
3	\leftrightarrow	20.5	\leftrightarrow	20	\leftrightarrow	20	\leftrightarrow	0	\leftrightarrow	0100
4	\leftrightarrow	33.5	\leftrightarrow	33	\leftrightarrow	35	\leftrightarrow	-2	\leftrightarrow	1111

The line coding using Bipolar AMI

