



Certificate no: UC-588d0d8a-543f-4207-9286-d54b10ddd14e
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Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL Fundamentals for Digital Design and Verification

Instructors **Ovidiu Plugariu**

Ammu avinash sastry

Date **Oct. 22, 2024**

Length **5.5 total hours**