Common SSE Instructions

movss, movsd	Copies a scalar floating-point value between two XMM registers or between a memory location and an XMM register.
movapd	move two aligned packed double-precision floating-point values between XMM registers and memory
movhpd	move high packed double-precision floating-point value to or from the high quadword of an XMM register and memory
movlpd	move low packed single-precision floating-point value to or from the low quadword of an XMM register and memory
adds, addsd	Performs a scalar addition using the specified operands.
subs, subsd	Performs a scalar subtraction using the specified operands. Source operand is subtracted from destination operand.
mulss, mulsd	Performs a scalar multiplication using the specified operands.
divss, divsd	Performs a scalar division using the specified operands. Destination operand is divided by source operand.
sqrtss, sqrtsd	Computes the square root of the specified source operand.
maxss, maxsd	Compares the source and destination operands and saves the larger value in the destination operand.
minss, minsd	Compares the source and destination operands and saves the smaller value in the destination operand.
cvtsd2ss	Converts a DPFP value to a SPFP value. The source operand can be a memory location or an XMM register. The destination operand must be an XMM register.
cvtss2sd	Converts a SPFP value to a DPFP value. The source operand can be a memory location or an XMM register. The destination operand must be an XMM register.
cvtsi2ss, cvtsi2sd	Converts a signed-doubleword integer to a floating-point value. The source operand can be a memory location or a general-purpose register. The destination operand must be an XMM register.
cvtss2si, cvtsd2si	Converts a floating-point value to a signed doubleword integer. The source operand can be a memory location or an XMM register. The destination operand must be a general-purpose register.
cvttsd2si	Convert with truncation scalar double-precision floating-point values to scalar doubleword integers.
cvttpd2pi	convert with truncation packed double-precision floating-point values to packed doubleword integers

cvttpd2dq	convert with truncation packed double-precision floating-point values to packed doubleword integers.
cvtps2pd	convert packed single-precision floating-point values to packed double-precision floating-point values
andnpd	perform bitwise logical AND NOT of packed double-precision floating-point values
andpd	perform bitwise logical AND of packed double-precision floating-point values
orpd	perform bitwise logical OR of packed double-precision floating-point values
xorpd	perform bitwise logical XOR of packed double-precision floating-point values
andnps	perform bitwise logical AND NOT of packed single-precision floating-point values
orps	perform bitwise logical OR of packed single-precision floating-point values
pavgb	compute average of packed unsigned byte integers
pavgw	compute average of packed unsigned byte integers
cmpps	compare packed single-precision floating-point values
cmpss	compare scalar single-precision floating-point values
comiss	perform ordered comparison of scalar single-precision floating-point values and set flags in EFLAGS register
ucomiss	perform unordered comparison of scalar single-precision floating-point values and set flags in EFLAGS register
movdq2q	move quadword integer from XMM to MMX registers
movdqa	move aligned double quadword
movdqu	move unaligned double quadword
movq2dq	move quadword integer from MMX to XMM registers
movdq2q	move quadword integer from XMM to MMX registers
pmuludq	multiply packed unsigned doubleword integers
shufps	shuffles values in packed single-precision floating-point operands
unpckhps	unpacks and interleaves the two high-order values from two single-precision floating-point operands
unpcklps	unpacks and interleaves the two low-order values from two single-precision floating-point operands